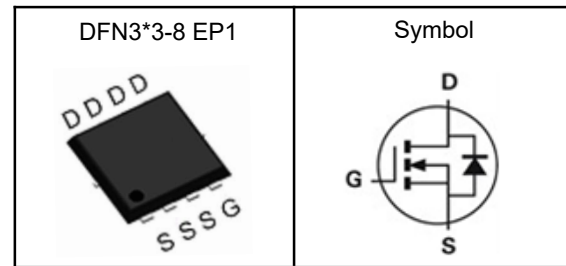# N-Channel Enhancement Mode MOSFET

Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% Avalanche Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description

DFN3*3-8 EP1	Symbol

V_{DSS}	100	V
$R_{DS(ON)-Typ}$	13	mΩ
I_D	43	A

Absolute Maximum Ratings(T_C=25℃, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	100	V
V_{GSS}	Gate-Source Voltage	±20	V
T_J	Maximum Junction Temperature	-55 to 150	℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
I_{DM}[①]	Pulse Drain Current Tested	172	A
I_D	Continuous Drain Current	43	A
P_D	Maximum Power Dissipation	65.8	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{θJA}$	Thermal Resistance-Junction to Ambient	62.5	℃/W
$R_{θJC}$	Thermal Resistance-Junction to Case	1.9	℃/W

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150℃.

Note ③ : Surface Mounted on 1in^2 FR-4 board with 1oz.

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Electrical Characteristics (T$_J$=25℃, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV$_{DSS}$	Drain-Source Breakdown Voltage	V$_{GS}$=0V, I$_D$=250uA	100	---	---	V
I$_{DSS}$	Zero Gate Voltage Drain Current	V$_{DS}$=100V, V$_{GS}$=0V	---	---	1	uA
V$_{GS(th)}$	Gate Threshold Voltage	V$_{DS}$=V$_{GS}$, I$_D$=250uA	1.0	---	2.5	V
I$_{GSS}$	Gate Leakage Current	V$_{GS}$=±20V, V$_{DS}$=0V	---	---	±100	nA
R$_{DS(ON)}$	Drain-Source On-state Resistance	V$_{GS}$=10V, I$_D$=20A	---	13	18	mΩ
		V$_{GS}$=4.5V, I$_D$=10A	---	17	22	mΩ
Dynamic Characteristics[5]						
C$_{iss}$	Input Capacitance	V$_{GS}$=0V, V$_{DS}$=50V, Freq.=1MHz	---	1220	---	pF
C$_{oss}$	Output Capacitance		---	143	---	
C$_{rss}$	Reverse Transfer Capacitance		---	13.8	---	
T$_{d(on)}$	Turn-on Delay Time	V$_{DS}$=50V, I$_{DS}$=20A, V$_{GEN}$=10V, R$_G$=3Ω	---	9.2	---	nS
T$_r$	Turn-on Rise Time		---	3.6	---	
T$_{d(off)}$	Turn-off Delay Time		---	25.6	---	
T$_f$	Turn-off Fall Time		---	4.4	---	
Q$_g$	Total Gate Charge	V$_{DS}$=50V , V$_{GS}$=10V , I$_{DS}$=20A	---	24	---	nC
Q$_{gs}$	Gate-Source Charge		---	3	---	
Q$_{gd}$	Gate-Drain Charge		---	5	---	
Source-Drain Characteristics						
V$_{SD}$	Diode Forward Voltage	I$_S$=20A, V$_{GS}$=0V	---	---	1.3	V

Note ④： Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.
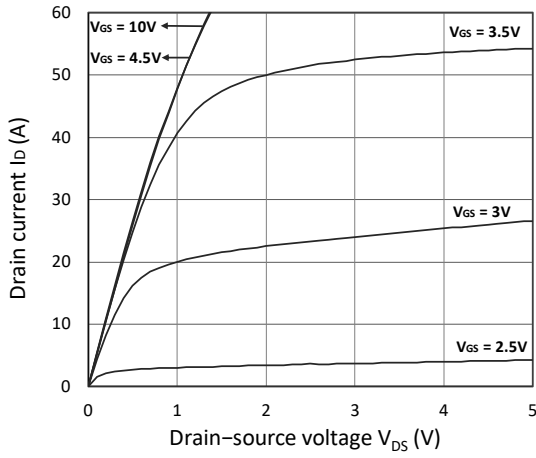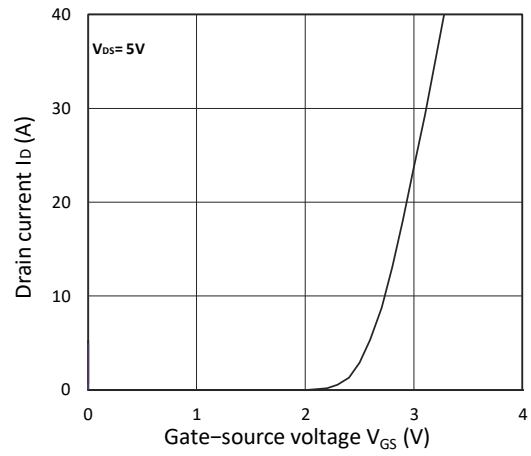
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Typical Characteristics

Figure 1. Output Characteristics
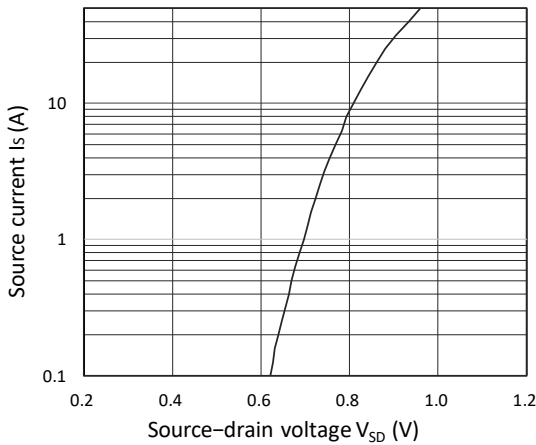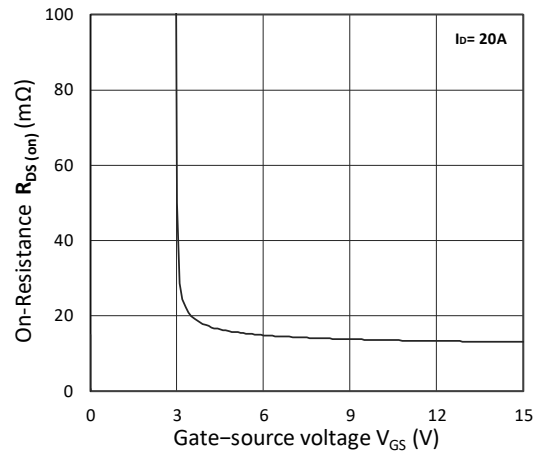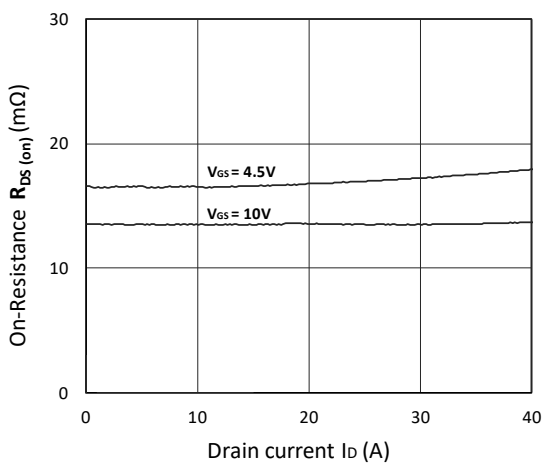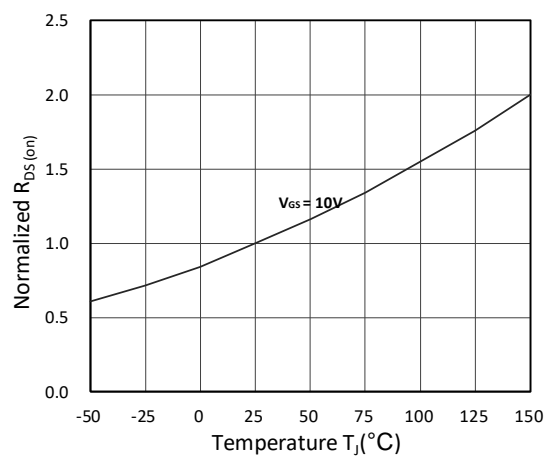
Figure 2. Transfer Characteristics

Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{DS(ON)}$ vs. V_{GS}

Figure 5. $R_{DS(ON)}$ vs. I_D
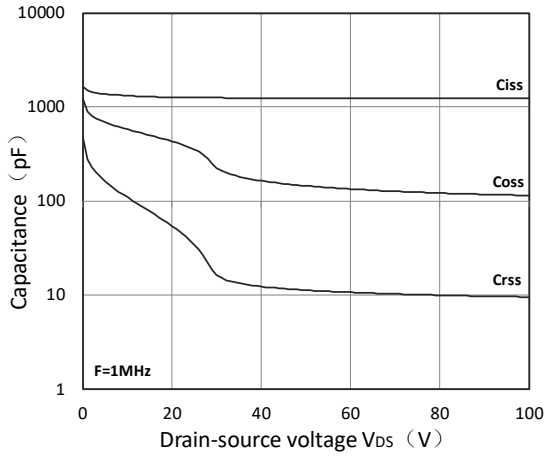
Figure 6. Normalized $R_{DS(on)}$ vs. Temperature
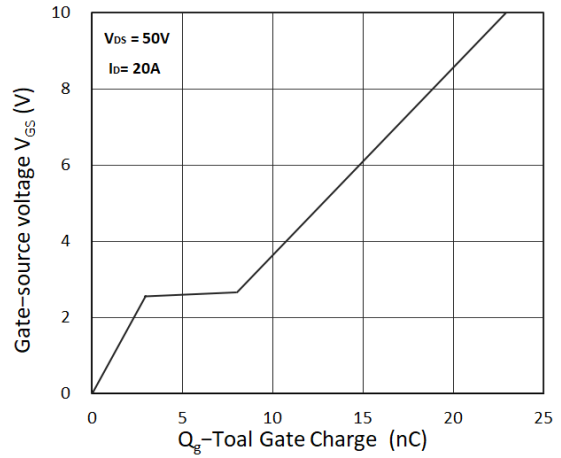
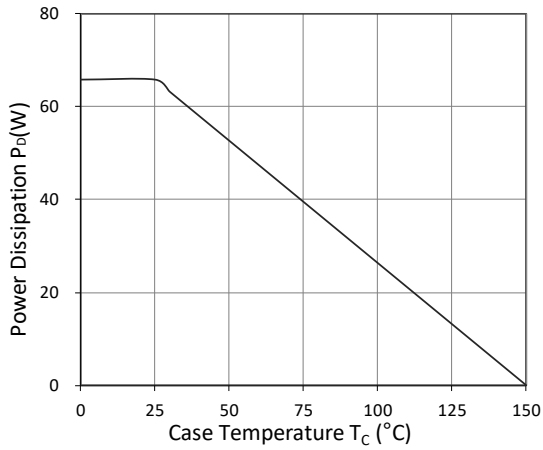Figure 7. Capacitance Characteristics

Figure 8. Gate Charge Characteristics

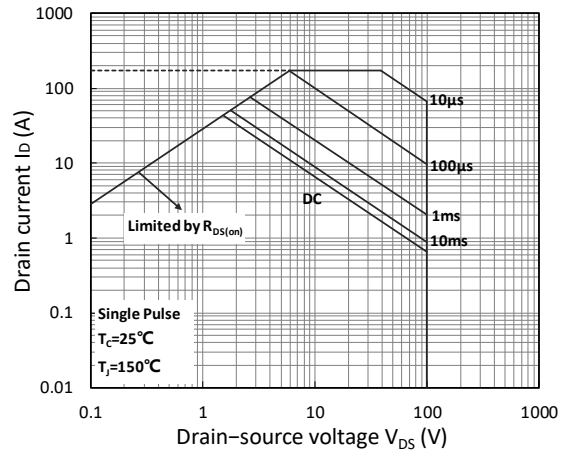
Figure 9. Power Dissipation
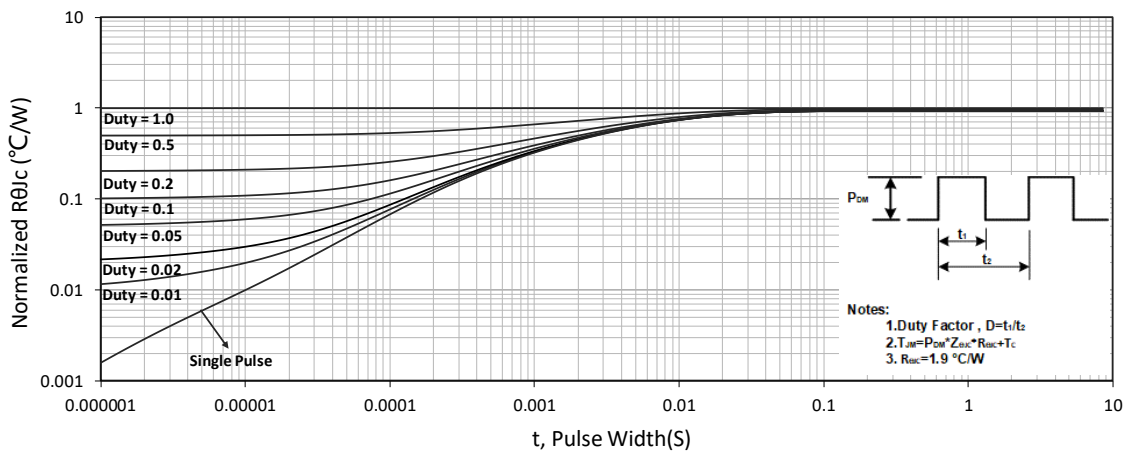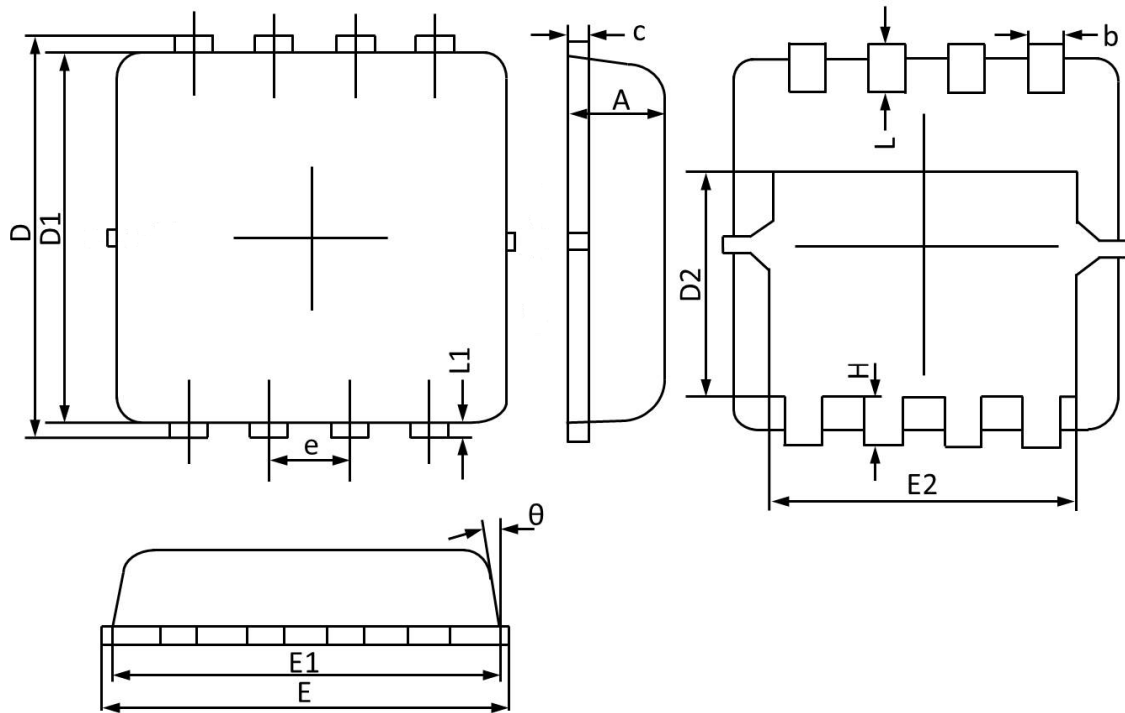

Figure10. Safe Operating Area

Figure 11. Normalized Maximum Transient Thermal Impedance

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DFN3*3-8 EP1 Package Outline Data

Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	**Typ**	**Max**		**Min**	**Typ**	**Max**
A	0.70	0.75	0.85	**E1**	2.90	3.10	3.25
b	0.24	0.30	0.35	**E2**	2.35	2.50	2.60
c	0.10	0.17	0.25	**e**	0.65 BSC		
D	3.10	3.30	3.45	**H**	0.30	0.40	0.50
D1	2.90	3.05	3.20	**L**	0.30	0.40	0.50
D2	1.45	1.70	1.95	**L1**	--	0.13	--
E	3.05	3.25	3.40	**θ**	0°		14°