

P-Channel Enhancement Mode MOSFET

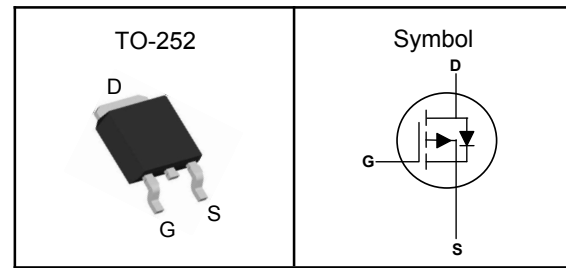
Features

- Low $R_{ds(on)}$ for low conduction loss
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description



V_{DSS}	-60	V
$R_{DS(ON)-Typ}$	23	m Ω
I_D	-35	A

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	P-Channel	Unit
V_{DSS}	Drain-Source Voltage	-60	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	-140	A
I_D	Continuous Drain Current	-35	A
P_D	Maximum Power Dissipation	58	W
E_{AS}	Single Pulse Avalanche Energy	115	mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{③}$	Thermal Resistance-Junction to Ambient	65	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case	2.15	$^\circ\text{C/W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in^2 FR-4 board with 1oz.

**P-Channel Enhancement Mode MOSFET****Electrical Characteristics** ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-48V, V_{GS}=0V$	---	---	-1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	---	-2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(ON)}$	Drain-Source On-state Resistance	$V_{GS}=-10V, I_D=-10A$	---	23	30	m Ω
		$V_{GS}=-4.5V, I_D=-6A$	---	27	36	
gfs	Forward Transconductance	$V_{DS}=-10V, I_D=-10A$	---	32	---	S
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=-30V,$ Freq.=1MHz	---	4235	---	pF
C_{oss}	Output Capacitance		---	164	---	
C_{rss}	Reverse Transfer Capacitance		---	141	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=-30V, V_{GS}=-10V,$ $R_G=3\Omega, I_D=-10A$	---	22	---	nS
T_r	Turn-on Rise Time		---	25.6	---	
$T_{d(off)}$	Turn-off Delay Time		---	86	---	
T_f	Turn-off Fall Time		---	30	---	
Q_g	Total Gate Charge	$V_{DS}=-30V,$ $V_{GS}=-10V, I_D=-10A$	---	75	---	nC
Q_{gs}	Gate-Source Charge		---	7.5	---	
Q_{gd}	Gate-Drain Charge		---	8.5	---	
Source-Drain Characteristics						
V_{SD} ^④	Diode Forward Voltage	$I_S=-10A, V_{GS}=0V$	---	---	-1.2	V

Note ④: Pulse test (pulse width 300us, duty cycle 2%).

Note ⑤: Guaranteed by design, not subject to production testing.

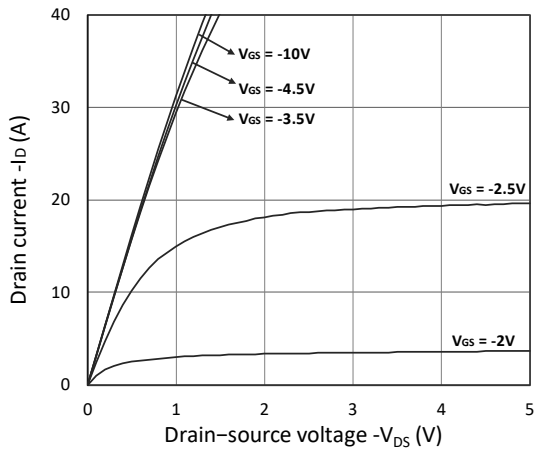
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Typical Characteristics


Figure 1. Output Characteristics

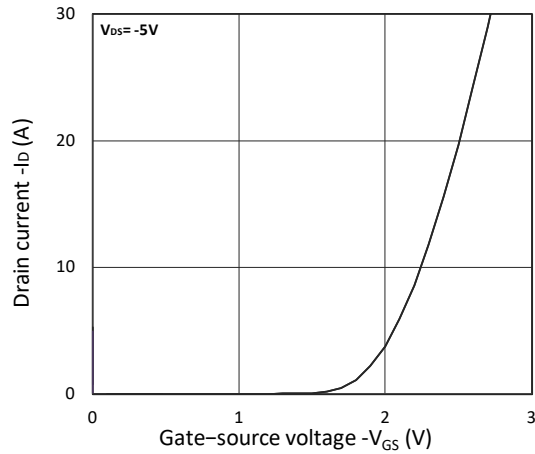


Figure 2. Transfer Characteristics

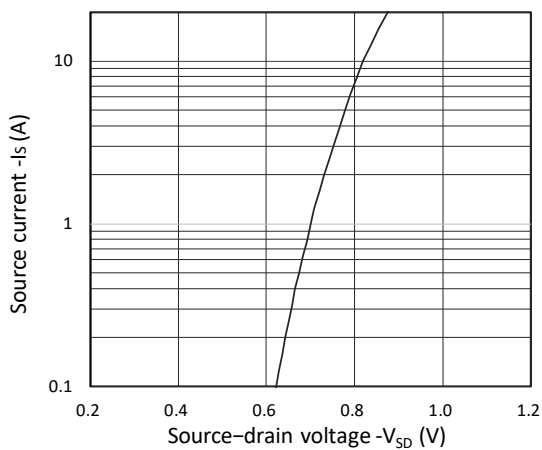
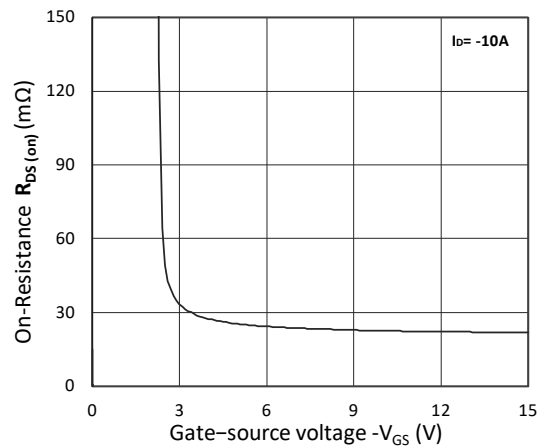
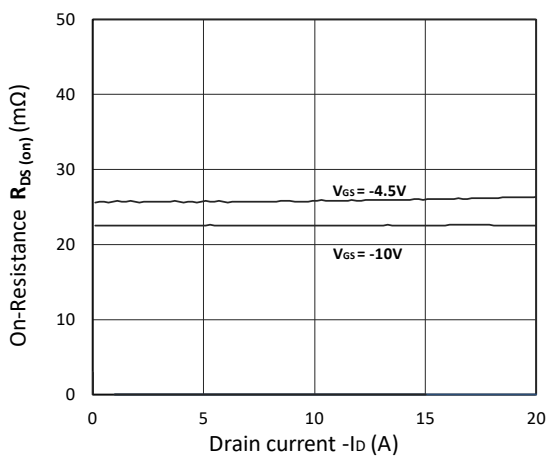
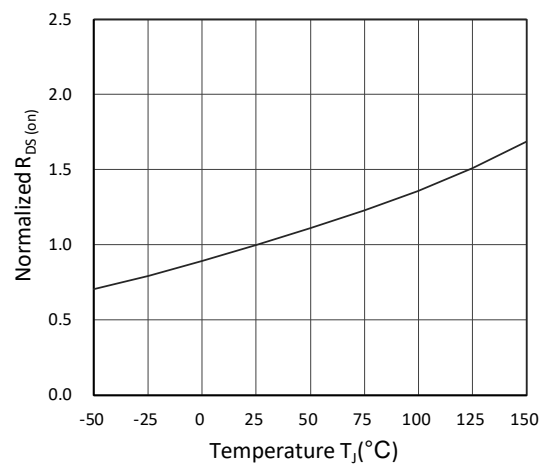


Figure 3. Forward Characteristics of Reverse


 Figure 4. $R_{DS(on)}$ vs. V_{GS}

 Figure 5. $R_{DS(on)}$ vs. I_D

 Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

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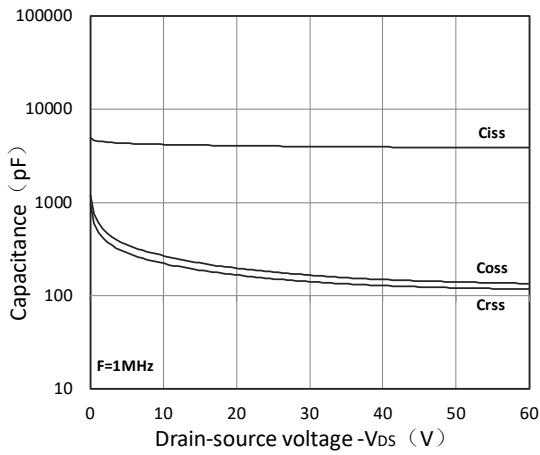


Figure 7. Capacitance Characteristics

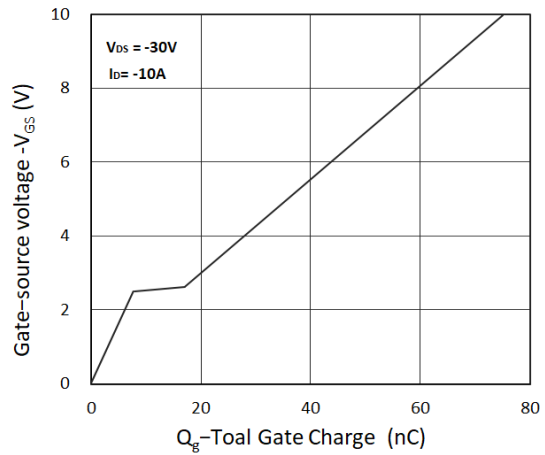


Figure 8. Gate Charge Characteristics

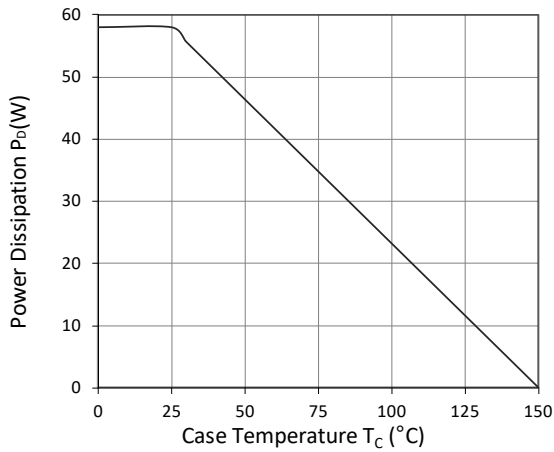


Figure 9. Power Dissipation

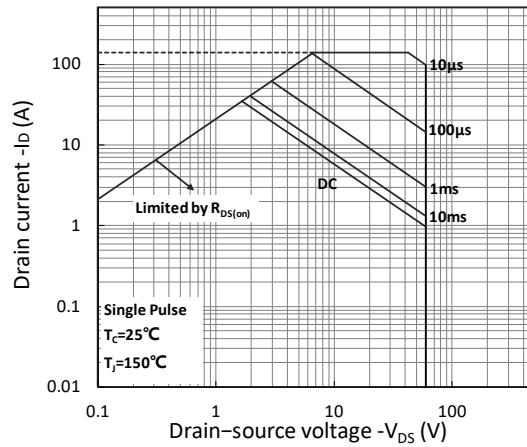


Figure 10. Safe Operating Area

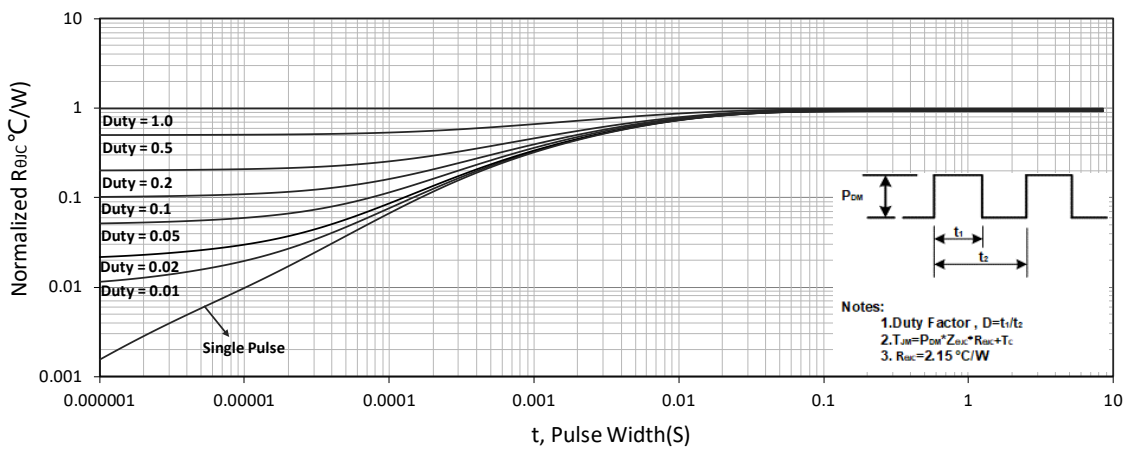
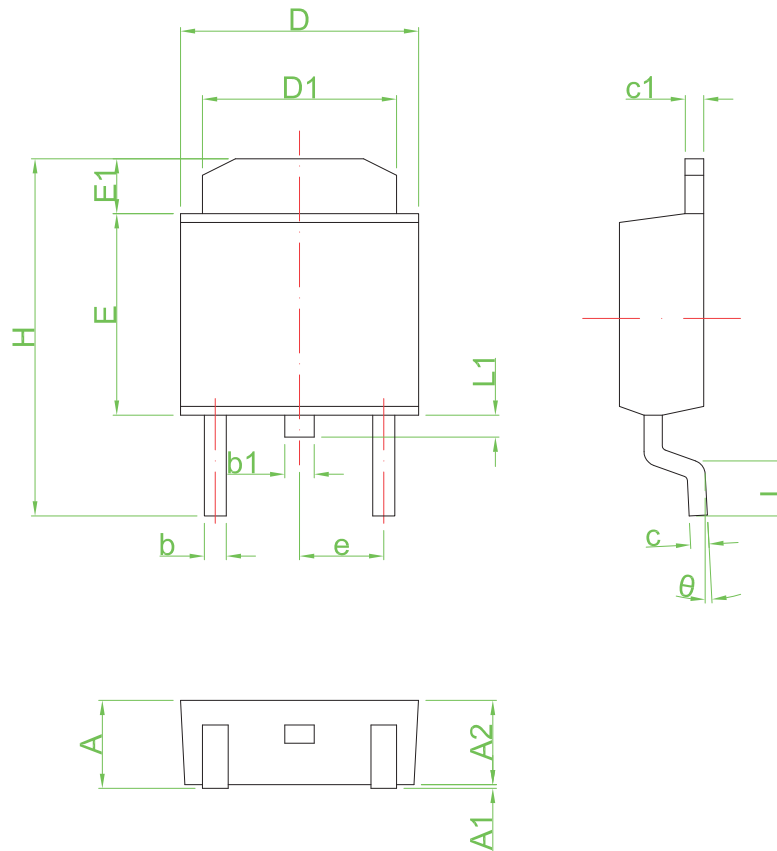


Figure 11. Normalized Maximum Transient Thermal Impedance

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TO-252 Package Outline Dimensions


Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	2.25	2.65	0.089	0.104
A1	0.00	0.15	0.000	0.006
A2	2.20	2.40	0.087	0.094
b	0.50	0.70	0.020	0.028
b1	0.70	0.90	0.028	0.035
c	0.46	0.66	0.018	0.026
c1	0.46	0.66	0.018	0.026
D	6.30	6.70	0.248	0.264
D1	5.20	5.40	0.205	0.213
E	5.30	5.70	0.209	0.224
E1	1.40	1.60	0.055	0.063
H	9.40	9.90	0.370	0.390
e	2.30 TYP		0.09 TYP	
L	1.40	1.77	0.055	0.070
L1	0.50	0.70	0.020	0.028
θ	0°	8°	0°	8°