

N-Channel Enhancement Mode MOSFET

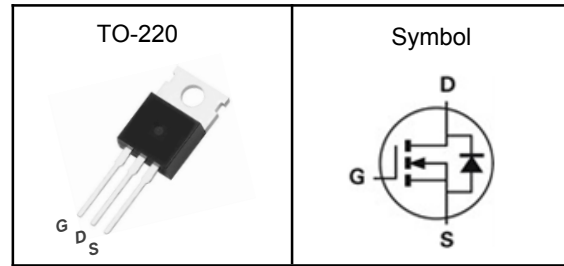
Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description



V_{DSS}	60	V
$R_{DS(ON)-Typ}$	7.5	m Ω
I_D	65	A

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	60	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
EAS	Single Pulse Avalanche Energy ^③	90	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	204	A
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$ 65	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$ 80	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁	1.56	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$.

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.



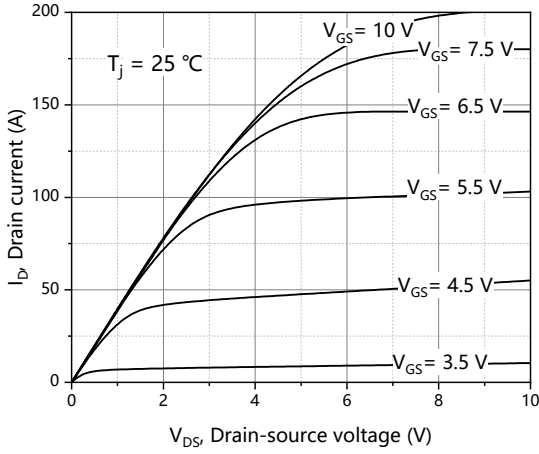
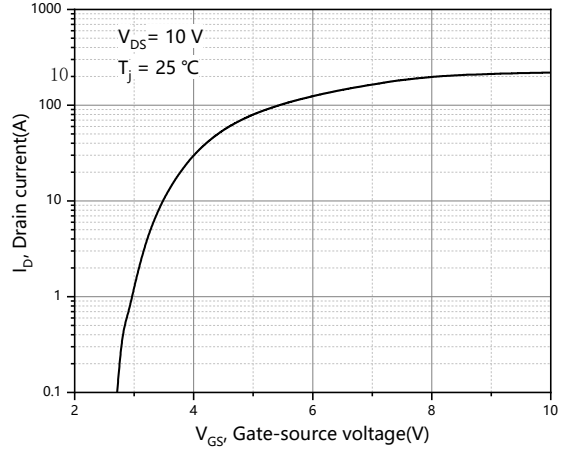
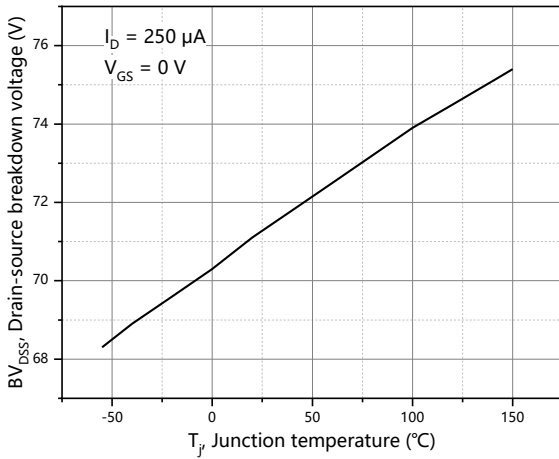
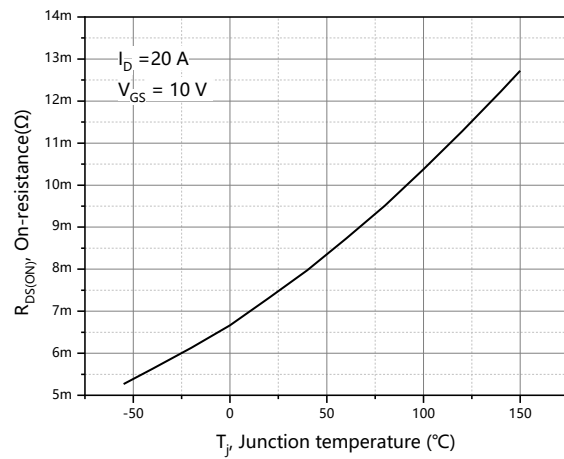
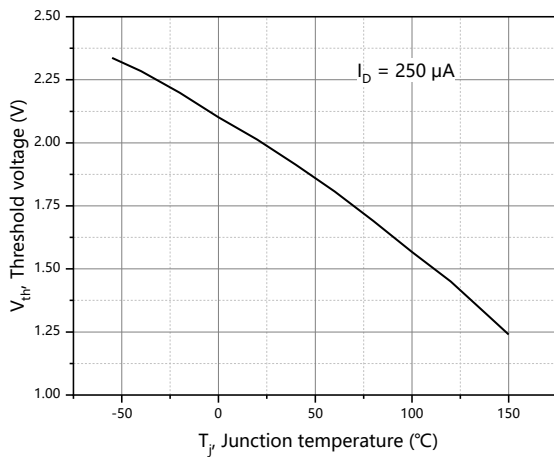
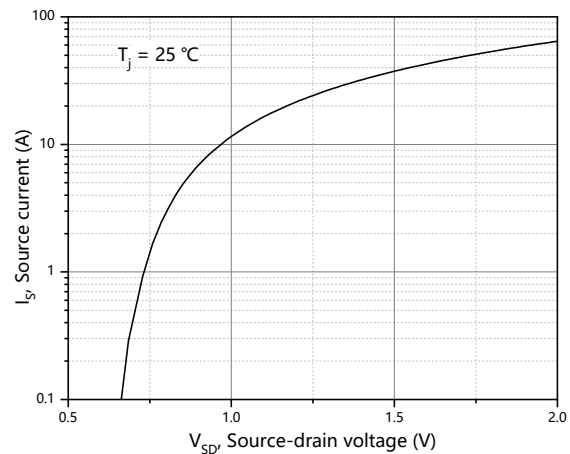
N-Channel Enhancement Mode MOSFET

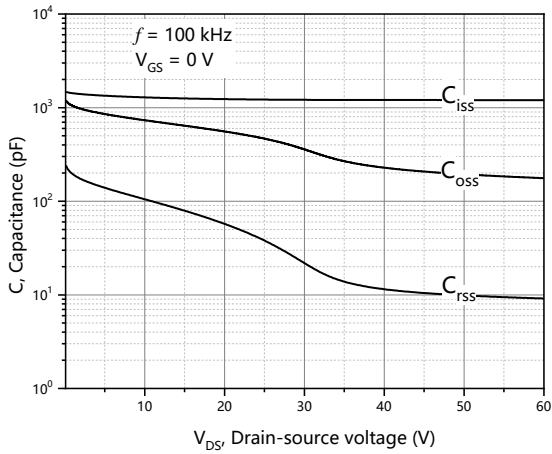
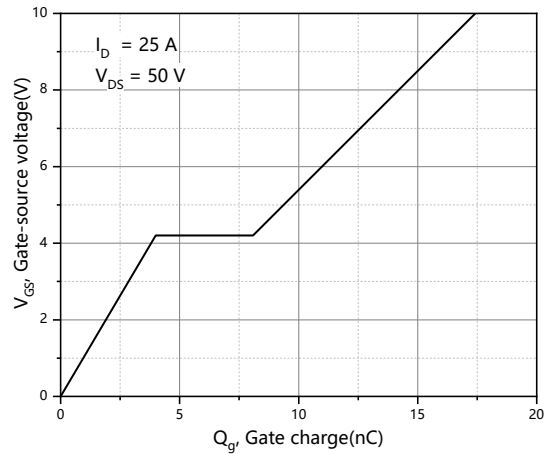
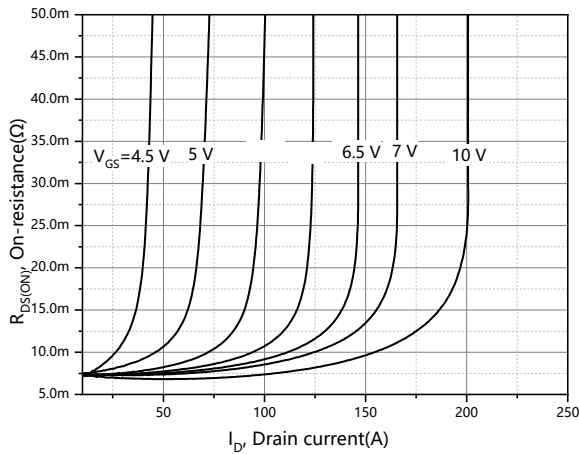
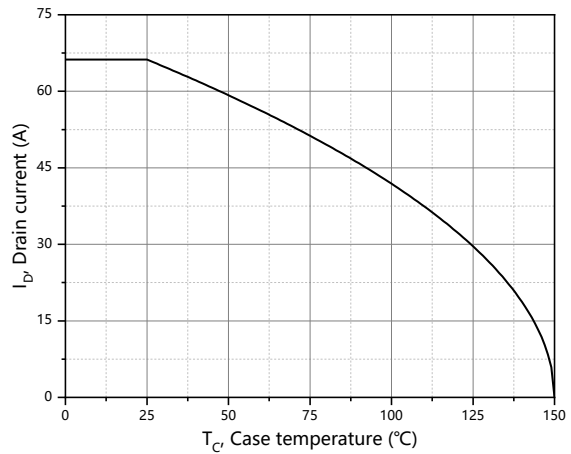
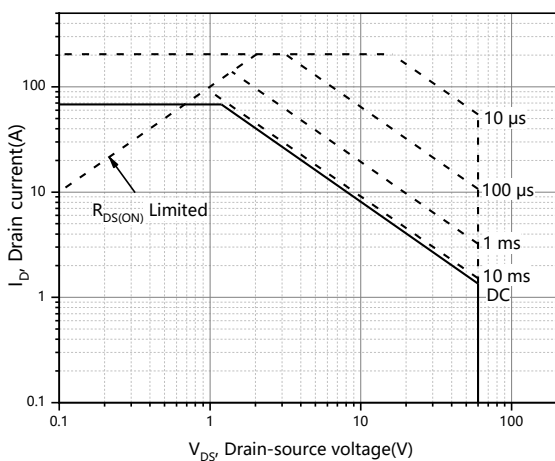
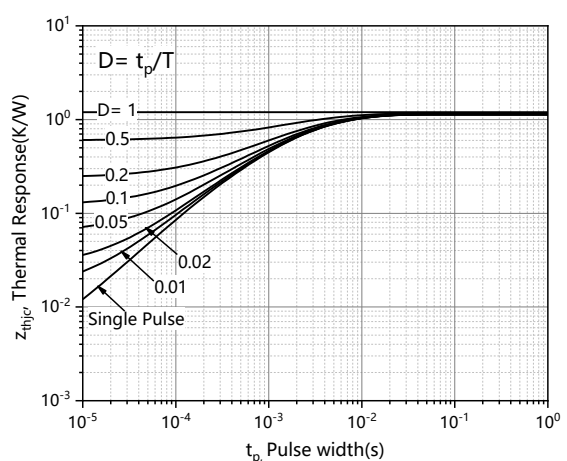
Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

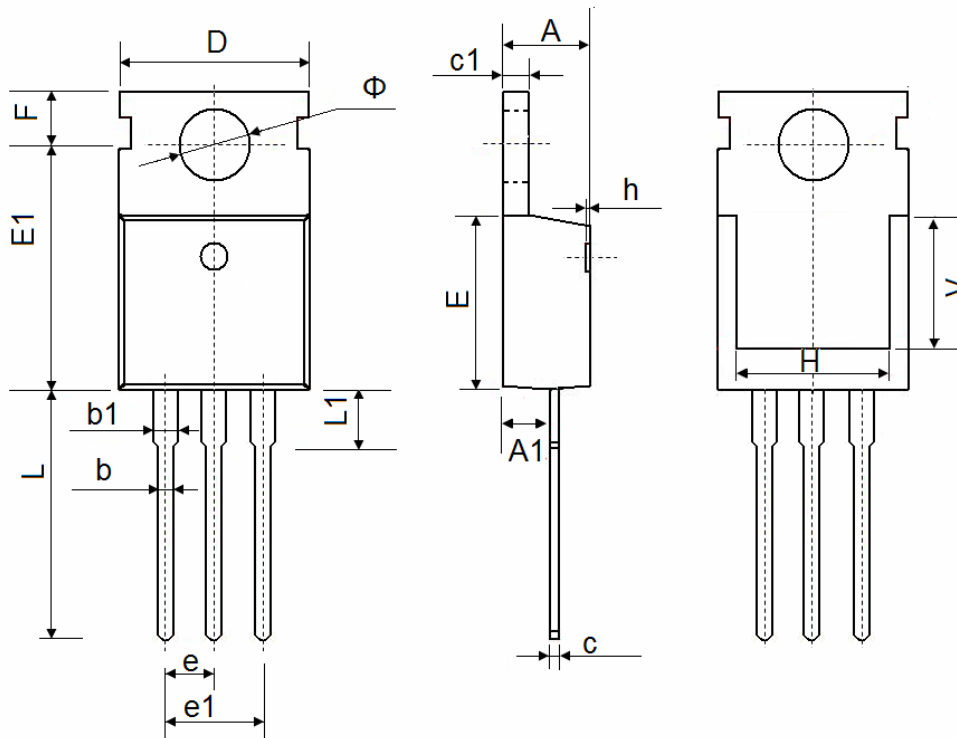
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=60V, V_{GS}=0V$	---	---	1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	---	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(ON)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=20A$	---	7.5	10	$m\Omega$
		$V_{GS}=4.5V, I_D=10A$	---	10	14	$m\Omega$
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, \text{Freq.}=1\text{MHz}$	---	1280	---	pF
C_{oss}	Output Capacitance		---	194	---	
C_{rSS}	Reverse Transfer Capacitance		---	10	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DS}=50V, V_{GS}=10V, I_D=25A, R_G=2\Omega$	---	23.9	---	nS
T_r	Turn-on Rise Time		---	4.6	---	
$T_{d(off)}$	Turn-off Delay Time		---	37.8	---	
T_f	Turn-off Fall Time		---	4.6	---	
Q_g	Total Gate Charge	$V_{DS}=50V, V_{GS}=10V, I_D=25A$	---	17.5	---	nC
Q_{gs}	Gate-Source Charge		---	3.8	---	
Q_{gd}	Gate-Drain Charge		---	4.2	---	
Source-Drain Characteristics						
V_{SD}	Diode Forward Voltage	$I_S=20A, V_{GS}=0V$	---	---	1.3	V
t_{rr}	Reverse Recovery Time	$I_F=20A, V_{GS}=0V, di/dt=100A/\mu s$	---	42.6	---	nS
Q_{rr}	Reverse Recovery Charge		---	36.3	---	nC

Note ④: Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤: Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET
Typical Characteristics

Figure 1. Typ. output characteristics

Figure 2. Typ. transfer characteristics

Figure 3. Drain-source breakdown voltage

Figure 4. Drain-source on-state resistance

Figure 5. Threshold voltage

Figure 6. Forward characteristic of body diode

N-Channel Enhancement Mode MOSFET

Figure 7. Typ. capacitances

Figure 8. Typ. gate charge

Figure 9. Drain-source on-state resistance

Figure 10. Drain current

Figure 11. Safe operation area $T_C=25\text{ }^\circ\text{C}$

Figure 12. Max. transient thermal impedance

N-Channel Enhancement Mode MOSFET
TO-220 Package Outline Data


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.350	4.650
A1	2.250	2.550
b	0.710	0.910
b1	1.170	1.400
c	0.330	0.650
c1	1.200	1.400
D	9.910	10.250
E	8.9500	9.750
E1	12.650	12.950
e	2.540 TYP.	
e1	4.980	5.180
F	2.650	2.950
H	7.900	8.100
h	0.000	0.300
L	12.700	13.500
L1	2.850	3.250
V	7.500 REF.	
Φ	3.400	3.800