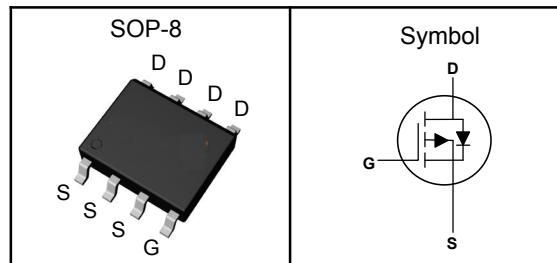


## P-Channel Enhancement Mode MOSFET

### Features

- Low  $R_{DS(on)}$  for low conduction loss
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

|                  |     |                  |
|------------------|-----|------------------|
| $V_{DSS}$        | -40 | V                |
| $R_{DS(ON)-Typ}$ | 38  | $\text{m}\Omega$ |
| $I_D$            | -7  | A                |

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

| Symbol       | Parameter                                  | P-Channel  | Unit             |
|--------------|--|------------|------------------|
| $V_{DSS}$    | Drain-Source Voltage                       | -40        | V                |
| $V_{GSS}$    | Gate-Source Voltage                        | $\pm 20$   | V                |
| $T_J$        | Maximum Junction Temperature               | -55 to 150 | $^\circ\text{C}$ |
| $T_{STG}$    | Storage Temperature Range                  | -55 to 150 | $^\circ\text{C}$ |
| $I_{DM}^{①}$ | Pulse Drain Current Tested                 | -20        | A                |
| $I_D$        | Continuous Drain Current                   | -7         | A                |
| $P_D$        | Maximum Power Dissipation                  | 3          | W                |
| $E_{AS}$     | Single Pulse Avalanche Energy <sup>3</sup> | 18         | $\text{mJ}$      |

### Thermal Characteristics

| Symbol              | Parameter                              | Rating | Unit               |
|---------------------|--|--------|--------------------|
| $R_{\theta JA}^{③}$ | Thermal Resistance-Junction to Ambient | 42     | $^\circ\text{C/W}$ |

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

## P-Channel Enhancement Mode MOSFET

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

| Symbol   | Parameter                        | Test Conditions   | Min  | Typ  | Max       | Unit             |
|--|----------------------------------|---|------|------|-----------|------------------|
| <b>Static Electrical Characteristics</b>                       |                                  |   |      |      |           |                  |
| $\text{BV}_{\text{DSS}}$                                       | Drain-Source Breakdown Voltage   | $V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=-250\mu\text{A}$  | -40  | ---  | ---       | V                |
| $I_{\text{DSS}}$   | Zero Gate Voltage Drain Current  | $V_{\text{DS}}=-32\text{V}$ , $V_{\text{GS}}=0\text{V}$   | ---  | ---  | -1        | $\mu\text{A}$    |
| $V_{\text{GS(th)}}$  | Gate Threshold Voltage           | $V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=-250\mu\text{A}$  | -1.0 | ---  | -2.5      | V                |
| $I_{\text{GSS}}$   | Gate Leakage Current             | $V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$  | ---  | ---  | $\pm 100$ | $\text{nA}$      |
| $R_{\text{DS(ON)}}$  | Drain-Source On-state Resistance | $V_{\text{GS}}=-10\text{V}$ , $I_{\text{D}}=-6\text{A}$   | ---  | 38   | 50        | $\text{m}\Omega$ |
|  |                                  | $V_{\text{GS}}=-4.5\text{V}$ , $I_{\text{D}}=-3\text{A}$  | ---  | 58   | 75        | $\text{m}\Omega$ |
| $g_{\text{fs}}$  | Forward Transconductance         | $V_{\text{DS}}=-5\text{V}$ , $I_{\text{D}}=-6\text{A}$  | ---  | 11   | ---       | S                |
| <b>Dynamic Characteristics<sup>⑤</sup></b>                     |                                  |   |      |      |           |                  |
| $C_{\text{iss}}$   | Input Capacitance                | $V_{\text{GS}}=0\text{V}$ ,<br>$V_{\text{DS}}=-15\text{V}$ ,<br>Freq.=1MHz  | ---  | 1080 | ---       | pF               |
| $C_{\text{oss}}$   | Output Capacitance               |   | ---  | 100  | ---       |                  |
| $C_{\text{rss}}$   | Reverse Transfer Capacitance     |   | ---  | 80   | ---       |                  |
| $T_{\text{d(on)}}$   | Turn-on Delay Time               | $V_{\text{DD}}=-15\text{V}$ , $V_{\text{GS}}=-10\text{V}$ ,<br>$R_{\text{G}}=3.3\Omega$ , $I_{\text{D}}=-1\text{A}$ | ---  | 12   | ---       | nS               |
| $T_r$  | Turn-on Rise Time                |   | ---  | 10   | ---       |                  |
| $T_{\text{d(off)}}$  | Turn-off Delay Time              |   | ---  | 25   | ---       |                  |
| $T_f$  | Turn-off Fall Time               |   | ---  | 8.6  | ---       |                  |
| $Q_g$  | Total Gate Charge                | $V_{\text{DS}}=-20\text{V}$ ,<br>$V_{\text{GS}}=-10\text{V}$ , $I_{\text{D}}=-6\text{A}$                            | ---  | 17   | ---       | nC               |
| $Q_{\text{gs}}$  | Gate-Source Charge               |   | ---  | 4.3  | ---       |                  |
| $Q_{\text{gd}}$  | Gate-Drain Charge                |   | ---  | 3.9  | ---       |                  |
| <b>Source-Drain Characteristics</b> ( $T_J=25^\circ\text{C}$ ) |                                  |   |      |      |           |                  |
| $V_{\text{SD}}^{④}$  | Diode Forward Voltage            | $V_{\text{GS}}=0\text{V}$ , $I_{\text{S}}=-1\text{A}$ , $T_J=25^\circ\text{C}$                                      | ---  | ---  | -1.2      | V                |

Note ④ : Pulse test (pulse width $\leq 300\text{us}$ , duty cycle $\leq 2\%$ ).

Note ⑤ : Guaranteed by design, not subject to production testing.

## P-Channel Enhancement Mode MOSFET

### Typical Characteristics

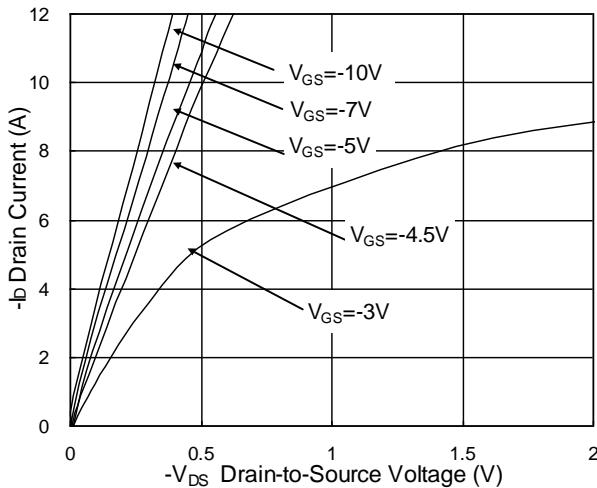


Fig.1 Typical Output Characteristics

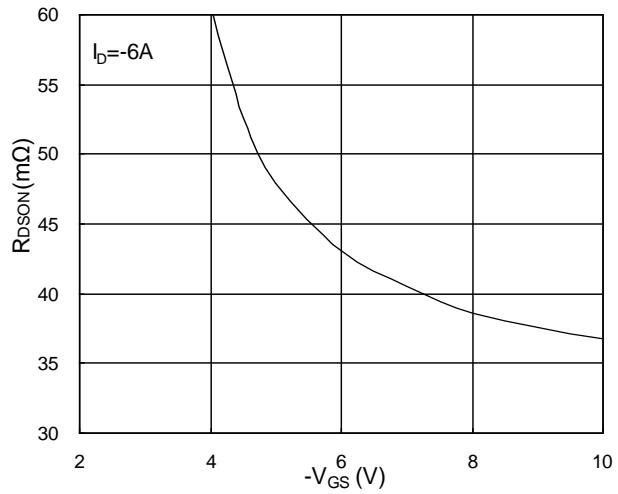


Fig.2 On-Resistance v.s Gate-Source

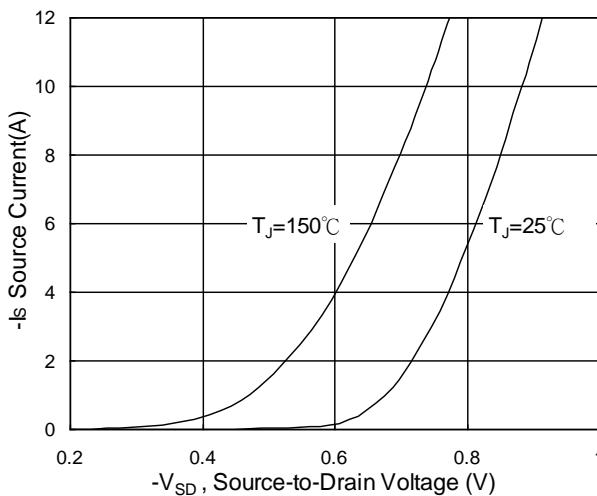


Fig.3 Forward Characteristics of Reverse

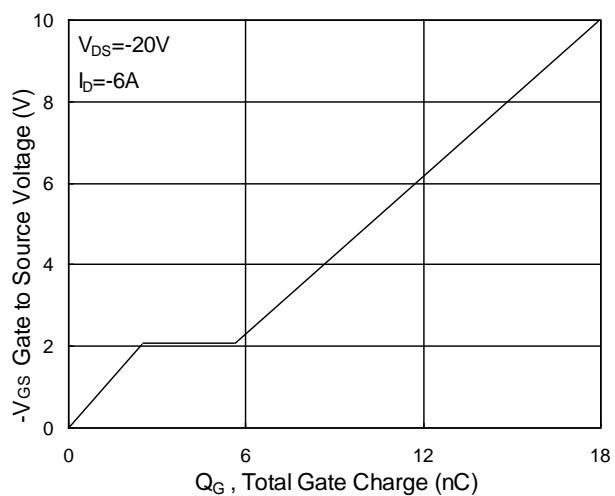


Fig.4 Gate-Charge Characteristics

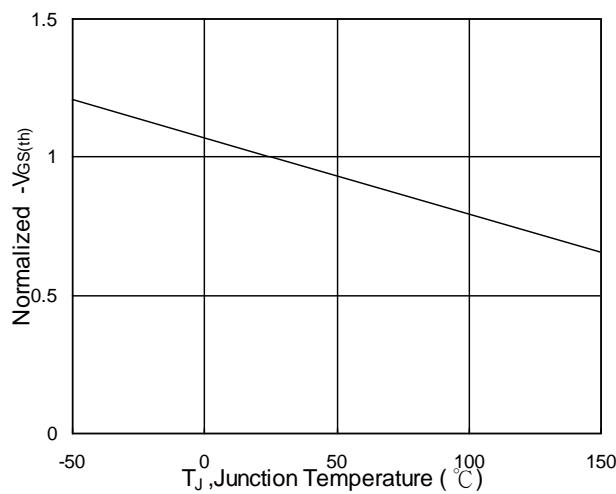


Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$

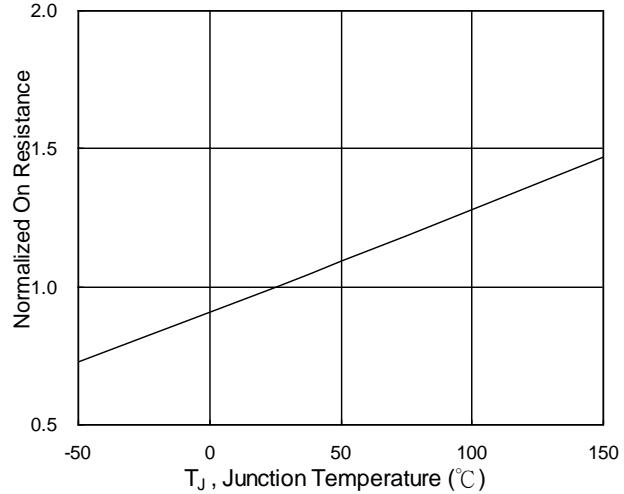
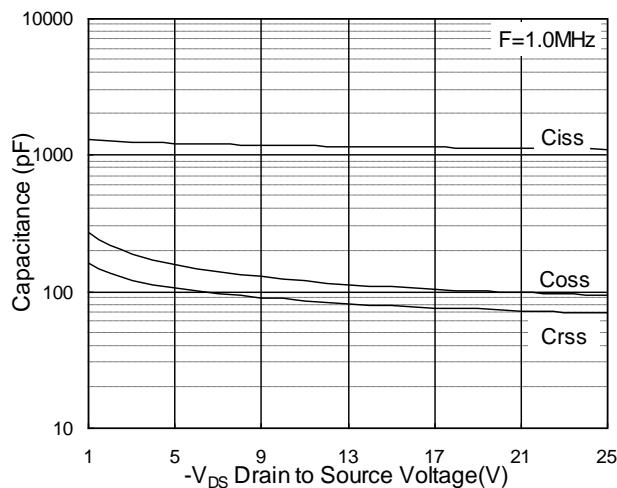
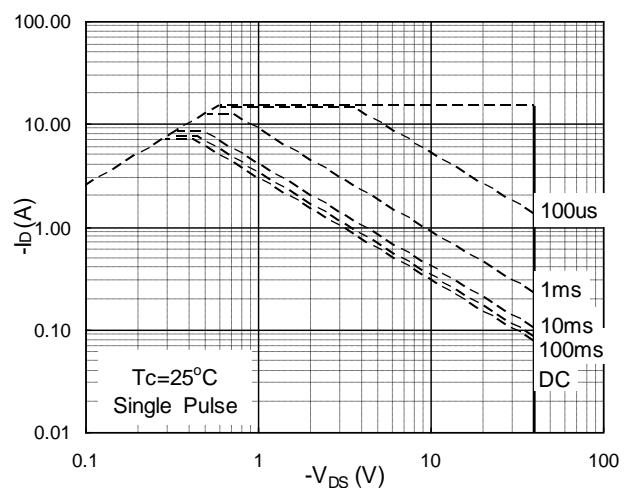


Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$

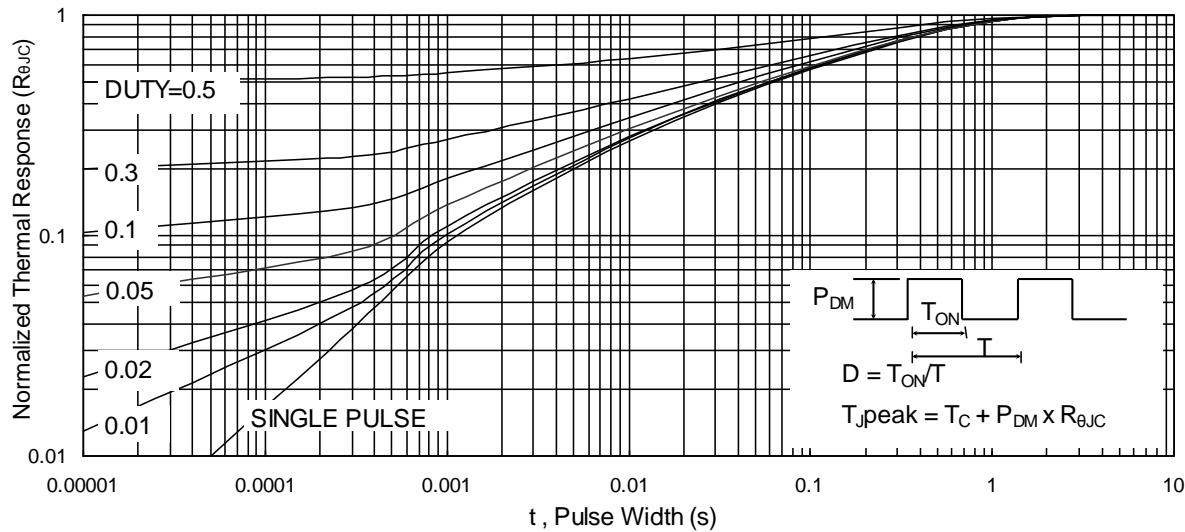
## P-Channel Enhancement Mode MOSFET



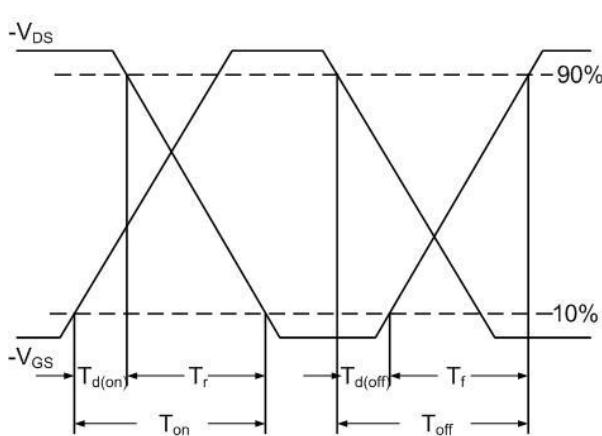
**Fig.7 Capacitance**



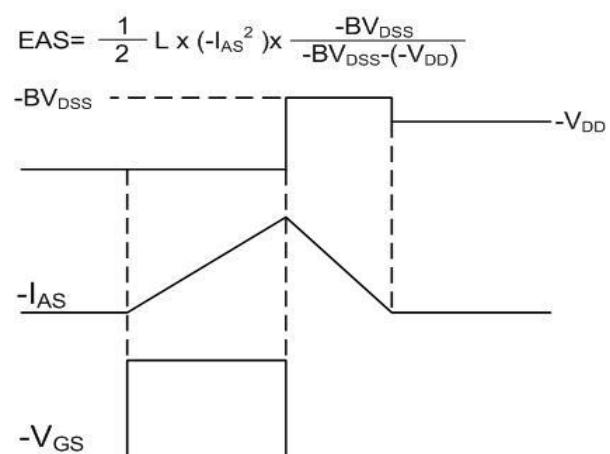
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



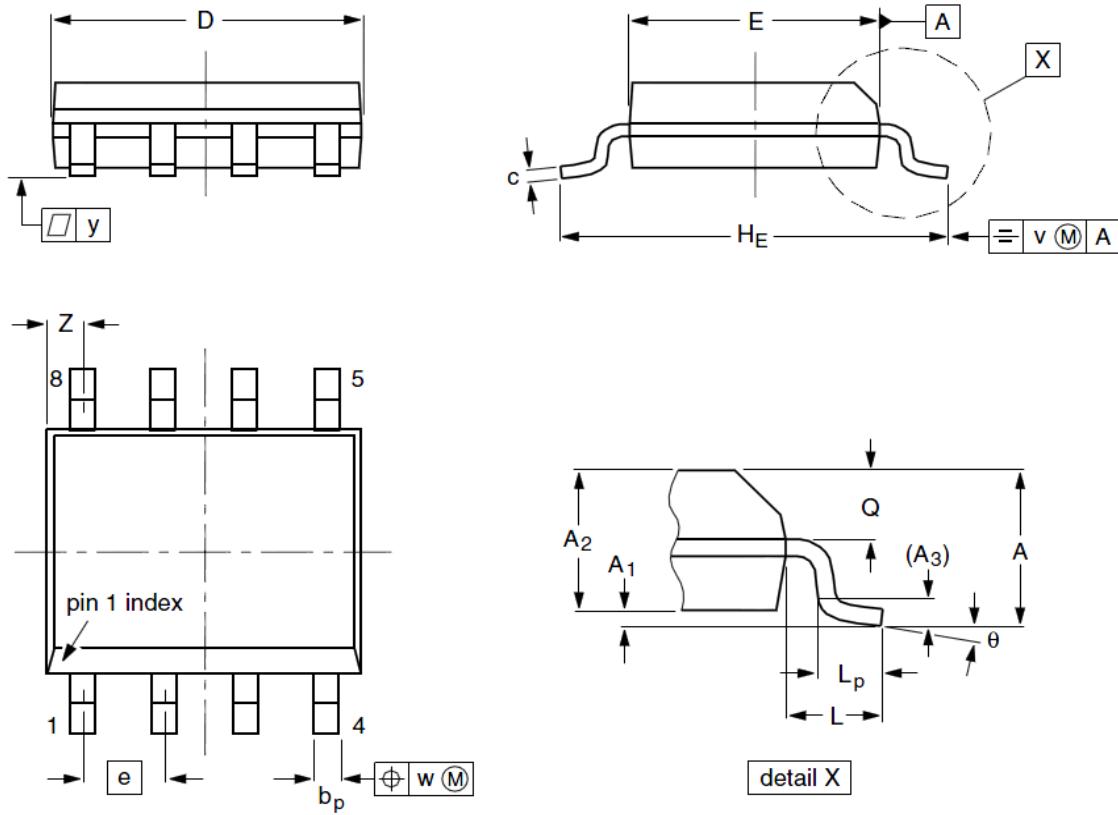
**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Waveform**

## P-Channel Enhancement Mode MOSFET

### SOP-8 Package Outline Dimensions



| Symbol               | Dimensions (unit:mm) |      |      | Symbol               | Dimensions (unit:mm) |      |      |
|----------------------|----------------------|------|------|----------------------|----------------------|------|------|
|                      | Min                  | Typ  | Max  |                      | Min                  | Typ  | Max  |
| <b>A</b>             | 1.35                 | 1.55 | 1.75 | <b>A<sub>1</sub></b> | 0.10                 | 0.18 | 0.25 |
| <b>A<sub>2</sub></b> | 1.25                 | 1.45 | 1.65 | <b>A<sub>3</sub></b> | --                   | 0.25 | --   |
| <b>b<sub>p</sub></b> | 0.36                 | 0.42 | 0.51 | <b>c</b>             | 0.19                 | 0.22 | 0.25 |
| <b>D</b>             | 4.70                 | 4.92 | 5.10 | <b>E</b>             | 3.80                 | 3.90 | 4.00 |
| <b>e</b>             | --                   | 1.27 | --   | <b>H<sub>E</sub></b> | 5.80                 | 6.00 | 6.20 |
| <b>L</b>             | --                   | 1.05 | --   | <b>L<sub>P</sub></b> | 0.40                 | 0.68 | 1.00 |
| <b>Q</b>             | 0.60                 | 0.65 | 0.73 | <b>v</b>             | --                   | 0.25 | --   |
| <b>w</b>             | --                   | 0.25 | --   | <b>y</b>             | --                   | 0.10 | --   |
| <b>Z</b>             | 0.30                 | 0.50 | 0.70 | <b>θ</b>             | 0°                   |      | 8°   |