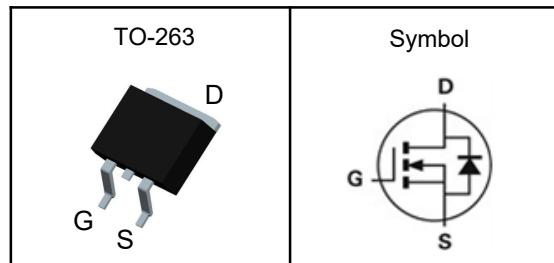


N-Channel Enhancement Mode MOSFET

Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Pin Description



Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

V_{DSS}	40	V
$R_{DS(ON)-Typ}$	1.3	$\text{m}\Omega$
I_D	180	A

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	40	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	720	A
I_D	Continuous Drain Current	180	A
P_D	Maximum Power Dissipation	250	W
E_{AS}	Avalanche Energy, Single pulse	1512	mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	42	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.5	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

N-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

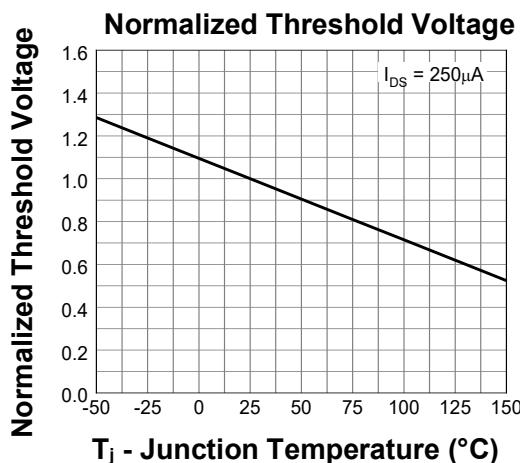
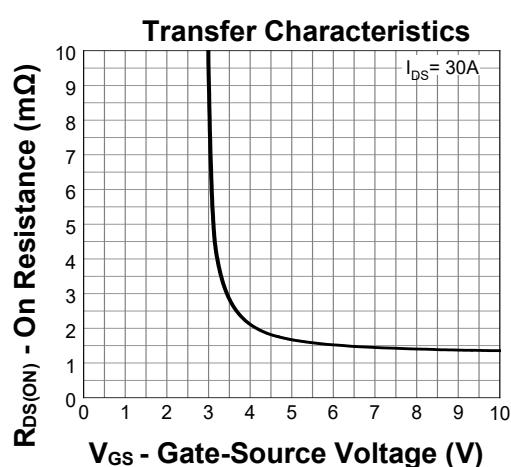
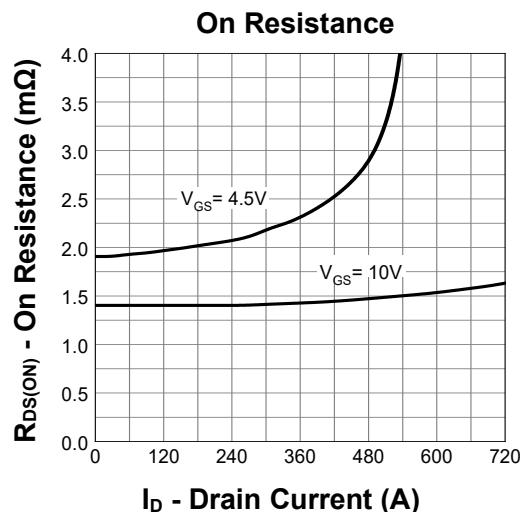
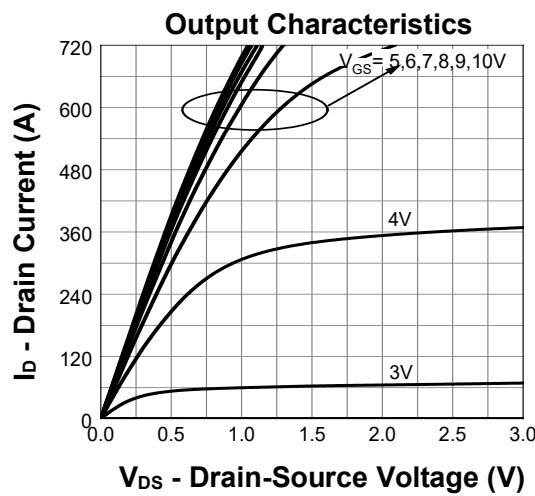
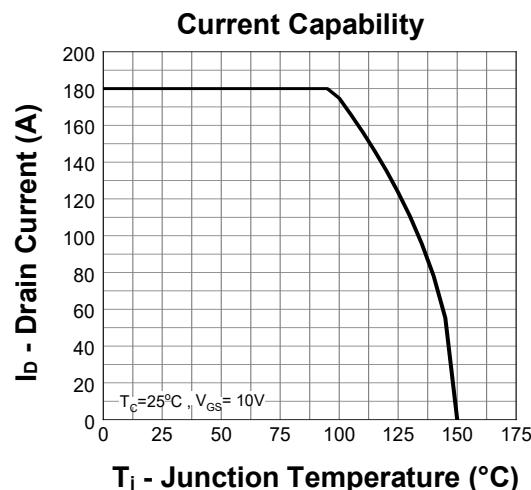
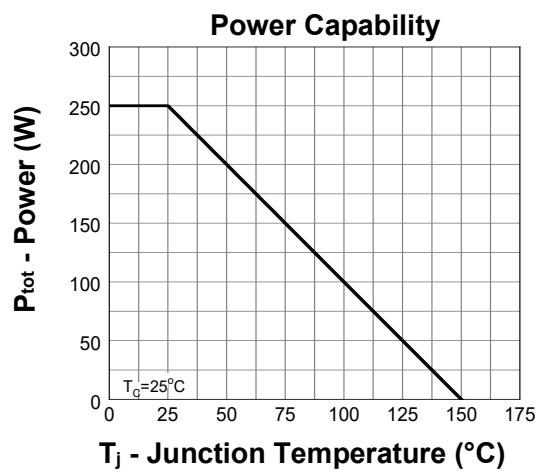
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	40	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=32\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$	---	---	1	μA
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}$, $\text{I}_D=250\mu\text{A}$	1.0	---	2.0	V
I_{GSS}	Gate Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
$\text{R}_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=30\text{A}$	---	1.3	1.6	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=20\text{A}$	---	1.8	2.2	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}$, $\text{V}_{\text{DS}}=20\text{V}$, Freq.=1MHz	---	14308	---	pF
C_{oss}	Output Capacitance		---	1036	---	
C_{rss}	Reverse Transfer Capacitance		---	308	---	
$\text{T}_{\text{d(on)}}$	Turn-on Delay Time	$\text{V}_{\text{DD}}=20\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_G=3.9\Omega$, $\text{I}_D=30\text{A}$	---	25	---	nS
T_r	Turn-on Rise Time		---	110	---	
$\text{T}_{\text{d(off)}}$	Turn-off Delay Time		---	190	---	
T_f	Turn-off Fall Time		---	126	---	
Q_g	Total Gate Charge	$\text{V}_{\text{GS}}=10\text{V}$, $\text{V}_{\text{DS}}=20\text{V}$, $\text{I}_D=30\text{A}$	---	238	---	nC
Q_{gs}	Gate-Source Charge		---	53	---	
Q_{gd}	Gate-Drain Charge		---	32	---	
Source-Drain Characteristics						
$\text{V}_{\text{SD}}^{④}$	Diode Forward Voltage	$\text{I}_S=30\text{A}$, $\text{V}_{\text{GS}}=0\text{V}$	---	---	1.3	V
t_{rr}	Reverse Recovery Time	$\text{I}_F=30\text{A}$, $d\text{I}_F/dt=100\text{A}/\mu\text{s}$	---	34	---	nS
Q_{rr}	Reverse Recovery Charge		---	25	---	nC

Note ④: Pulse test (pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$).

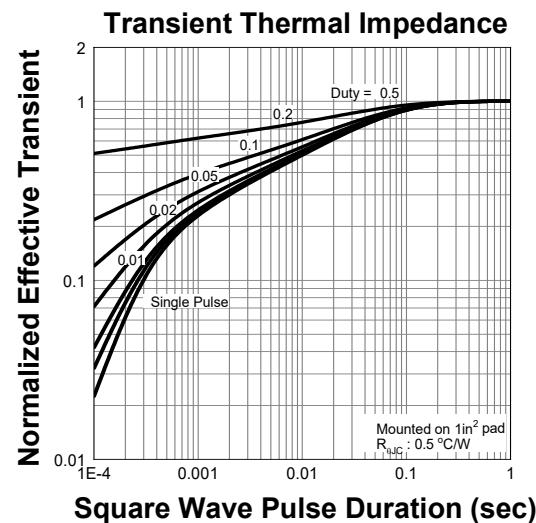
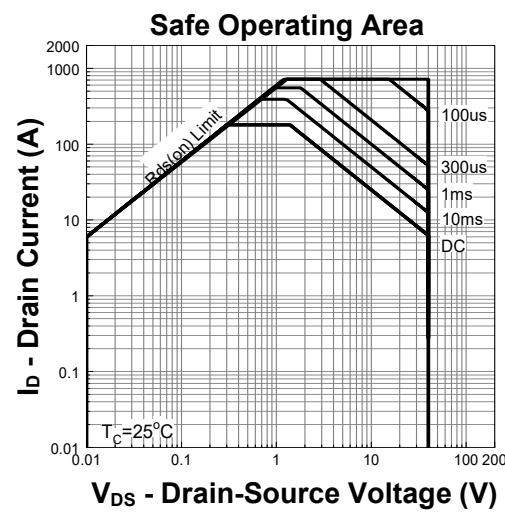
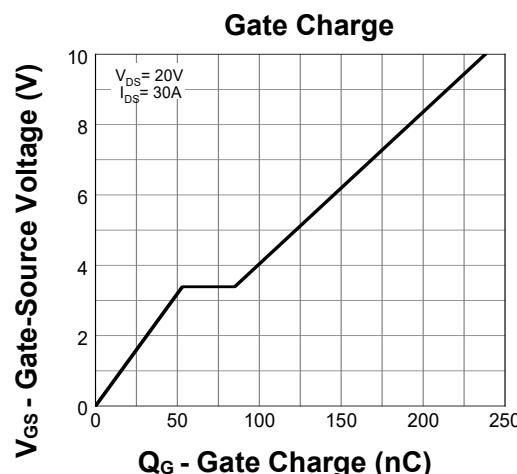
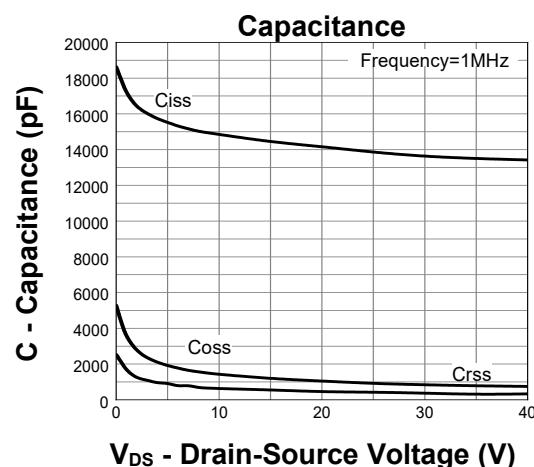
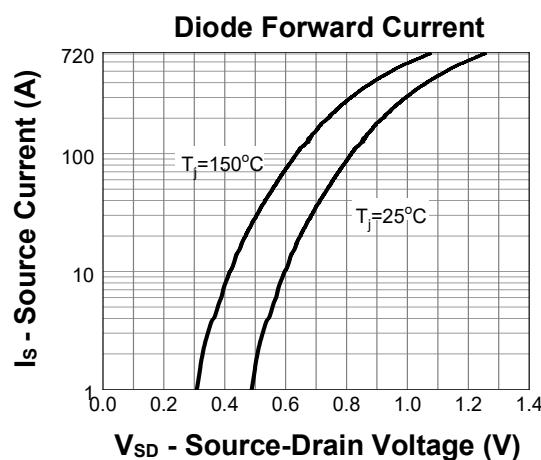
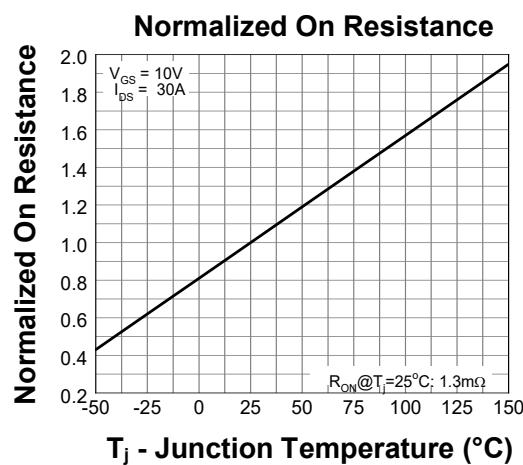
Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET

Typical Characteristics



N-Channel Enhancement Mode MOSFET



N-Channel Enhancement Mode MOSFET

TO-263 Package Outline Data

