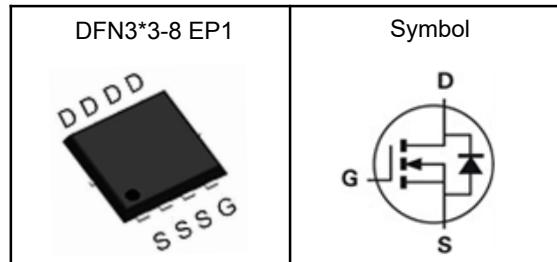


## N-Channel Enhancement Mode MOSFET

### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% Avalanche Tested

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

$V_{DSS}$	30	V
$R_{DS(ON)-Typ}$	7	$\text{m}\Omega$
$I_D$	46	A

### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	30	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{\text{(1)}}$	Pulse Drain Current Tested	92	A
$I_D$	Continuous Drain Current	46	A
$P_D$	Maximum Power Dissipation	29	W
$E_{AS}$	Avalanche Energy, Single pulse	57.8	mJ

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	75	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	4.32	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

## N-Channel Enhancement Mode MOSFET

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

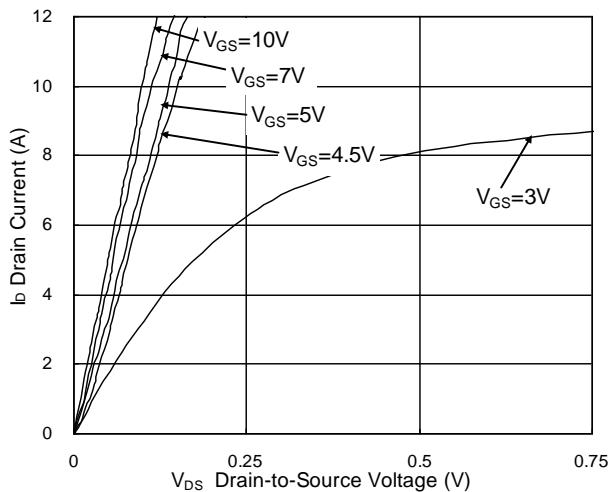
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	30	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\mu\text{A}$	1	---	2.5	V
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm100$	$\text{nA}$
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=12\text{A}$	---	7	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=10\text{A}$	---	10	13	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=15\text{V}$ , Freq.=1MHz	---	1317	---	pF
$C_{\text{oss}}$	Output Capacitance		---	163	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	131	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=12\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_{\text{G}}=3.3\Omega$ , $I_{\text{D}}=5\text{A}$	---	4.5	---	nS
$T_{\text{r}}$	Turn-on Rise Time		---	10.8	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	25.5	---	
$T_{\text{f}}$	Turn-off Fall Time		---	9.6	---	
$Q_{\text{g}}$	Total Gate Charge	$V_{\text{GS}}=10\text{V}$ , $V_{\text{DS}}=4.5\text{V}$ , $I_{\text{D}}=12\text{A}$	---	12.8	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	3.3	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	6.5	---	
<b>Source-Drain Characteristics</b>						
$V_{\text{SD}}$	Diode Forward Voltage	$I_{\text{S}}=1\text{A}$ , $V_{\text{GS}}=0\text{V}$	---	---	1.0	V
$I_{\text{S}}$	Continuous Source Current <sup>1,5</sup>	$V_{\text{G}}=V_{\text{D}}=0\text{V}$ , Force Current	---	---	46	A
$I_{\text{SM}}$	Pulsed Source Current <sup>2,5</sup>		---	---	92	A

Note ④: Pulse test (pulse width $\leq300\text{us}$ , duty cycle $\leq2\%$ ).

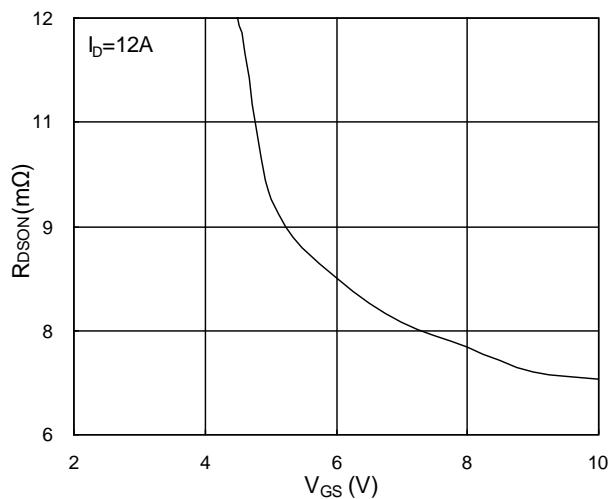
Note ⑤ : Guaranteed by design, not subject to production testing.

## N-Channel Enhancement Mode MOSFET

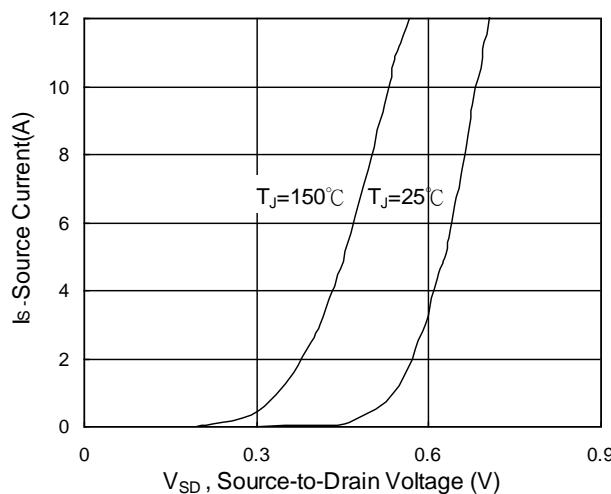
### Typical Characteristics



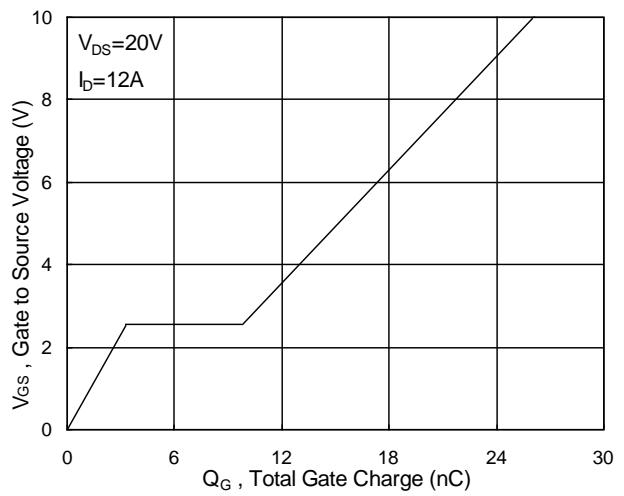
**Fig.1 Typical Output Characteristics**



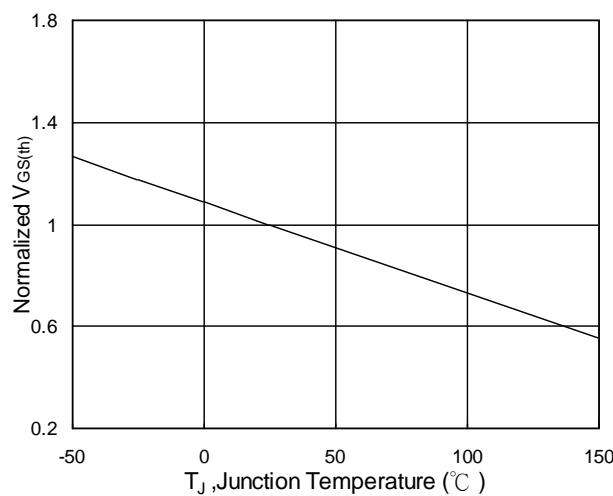
**Fig.2 On-Resistance vs. G-S Voltage**



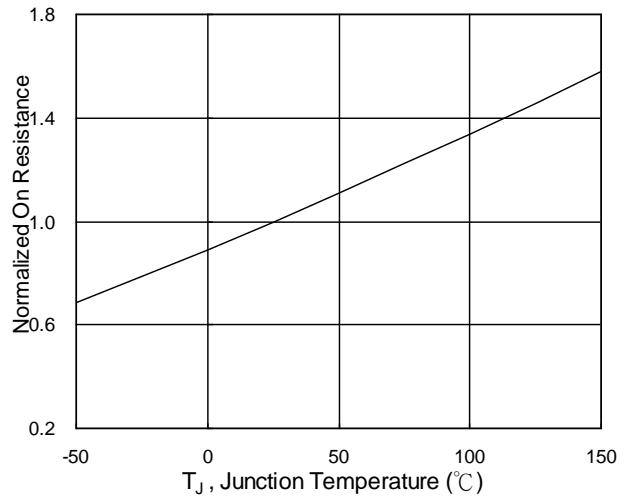
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge Characteristics**

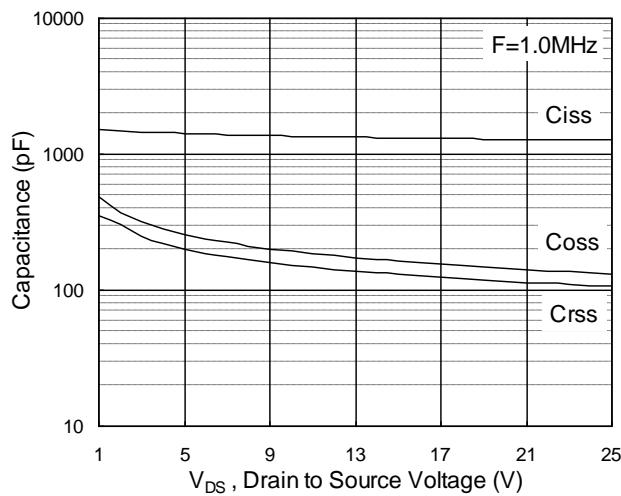


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**

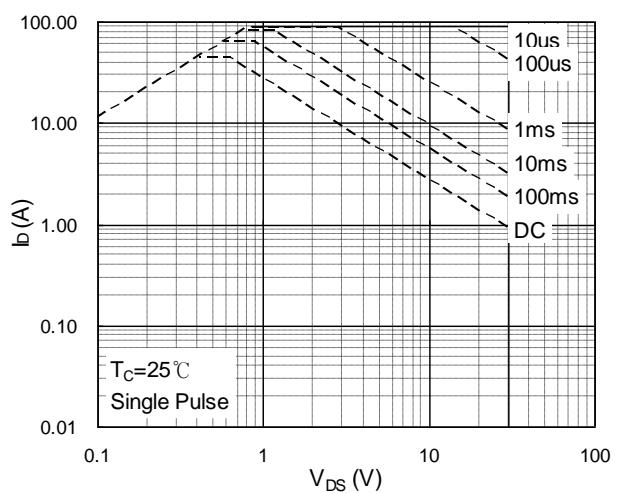


**Fig.6 Normalized  $R_{DSON}$  vs.  $T_J$**

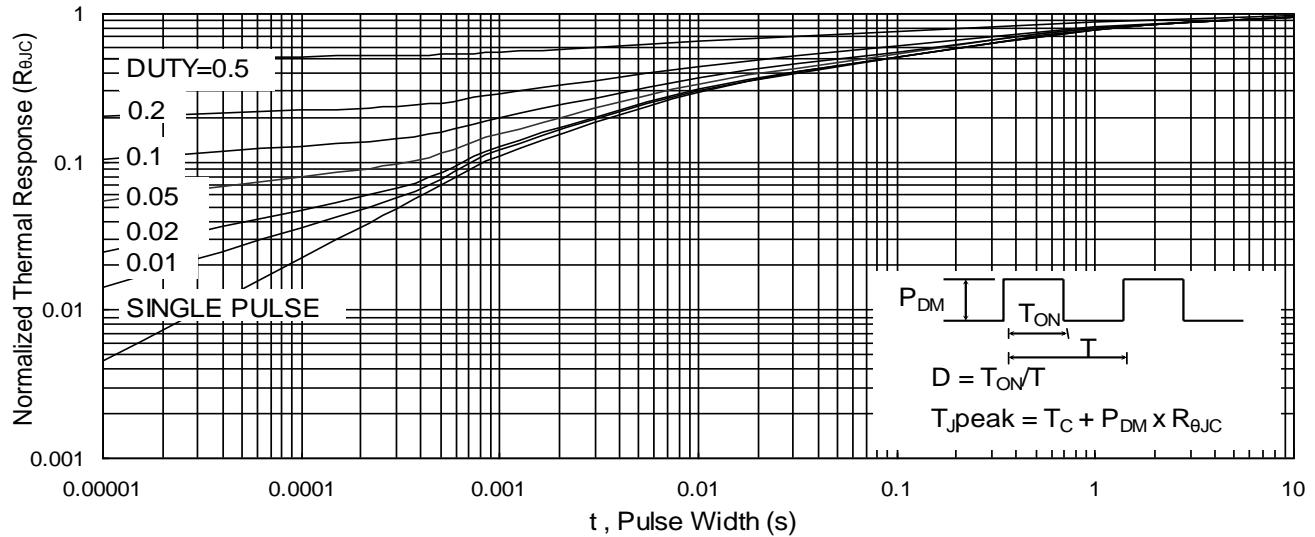
## N-Channel Enhancement Mode MOSFET



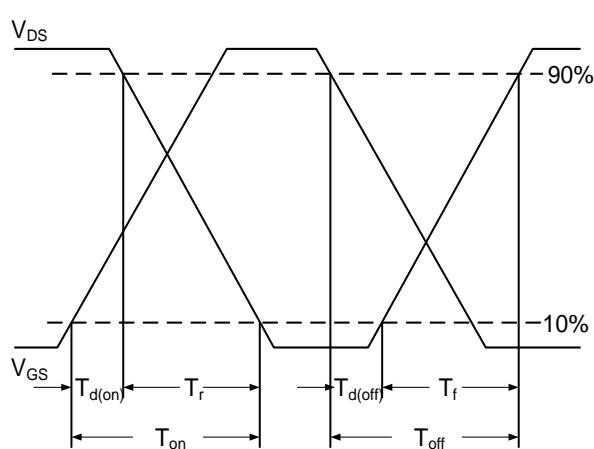
**Fig.7 Capacitance**



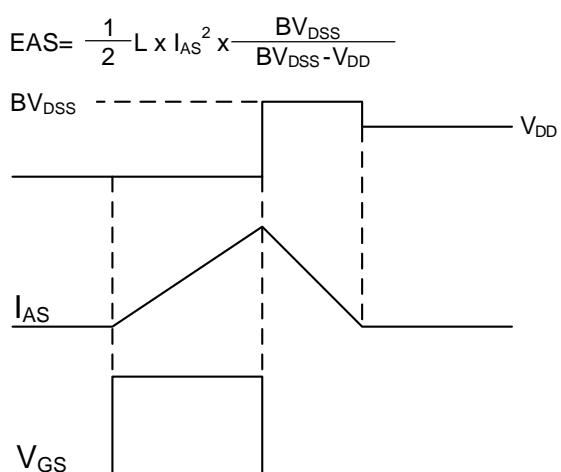
**Fig.8 Safe Operating Area**



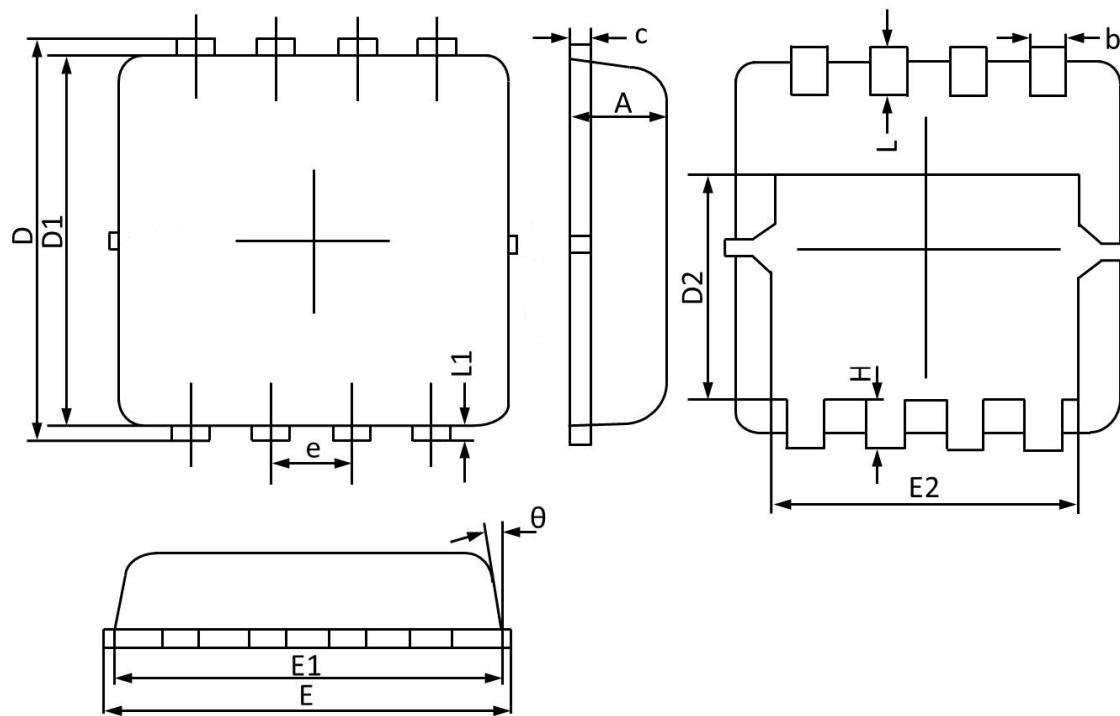
**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

**N-Channel Enhancement Mode MOSFET**
**DFN3\*3-8 EP1 Package Outline Data**


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.70	0.75	0.85	<b>E1</b>	2.90	3.10	3.25
<b>b</b>	0.24	0.30	0.35	<b>E2</b>	2.35	2.50	2.60
<b>c</b>	0.10	0.17	0.25	<b>e</b>	0.65 BSC		
<b>D</b>	3.10	3.30	3.45	<b>H</b>	0.30	0.40	0.50
<b>D1</b>	2.90	3.05	3.20	<b>L</b>	0.30	0.40	0.50
<b>D2</b>	1.45	1.70	1.95	<b>L1</b>	--	0.13	--
<b>E</b>	3.05	3.25	3.40	<b>θ</b>	0°		14°