

N-Channel Enhancement Mode MOSFET

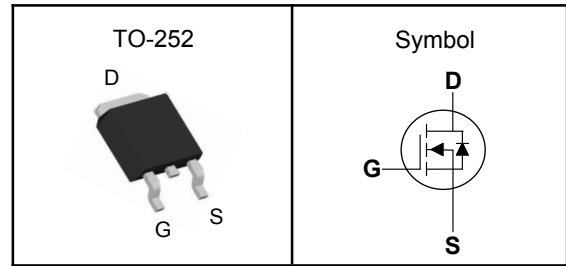
Features

- Advanced Trench technology
- High Speed Power Switching
- Reliable and Rugged
- ROHS Compliant
- 100% Avalanche Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description



V_{DSS}	30	V
$R_{DS(ON)-Typ}$	3.8	m Ω
I_D	90	A

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	360	A
I_D	Continuous Drain Current	90	A
P_D	Maximum Power Dissipation	90	W
E_{AS}	Avalanche Energy, Single pulse	90	mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1.67	$^\circ\text{C/W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$.

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.



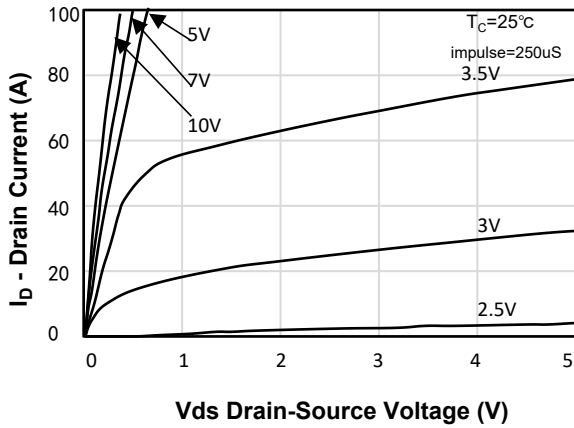
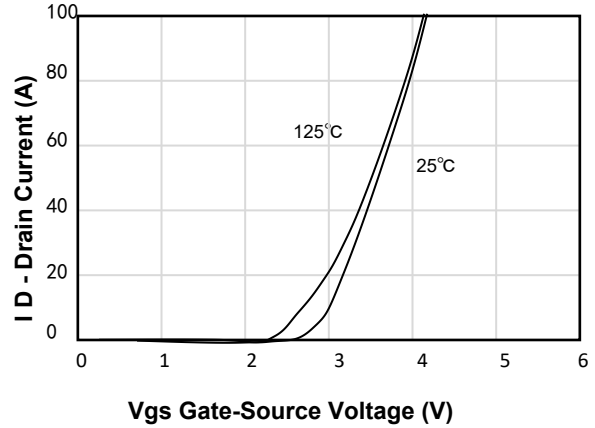
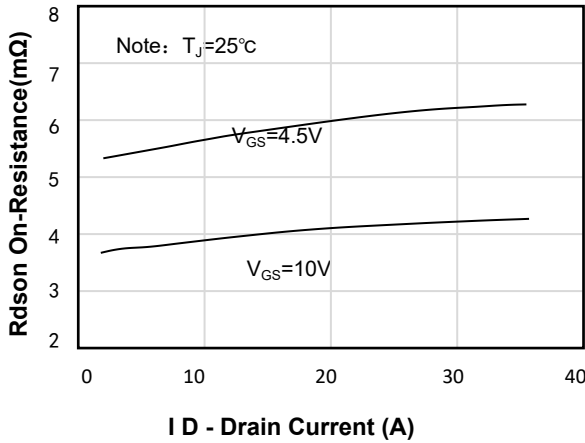
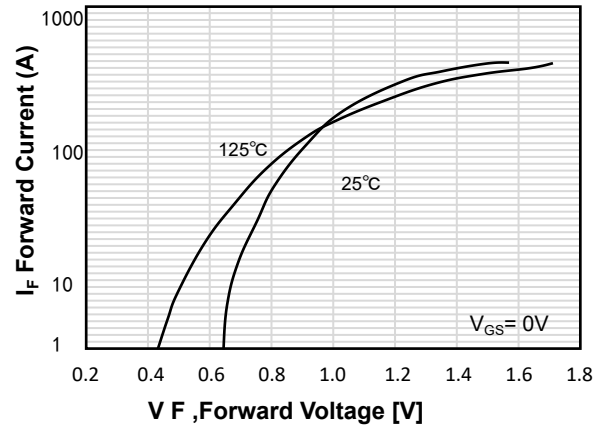
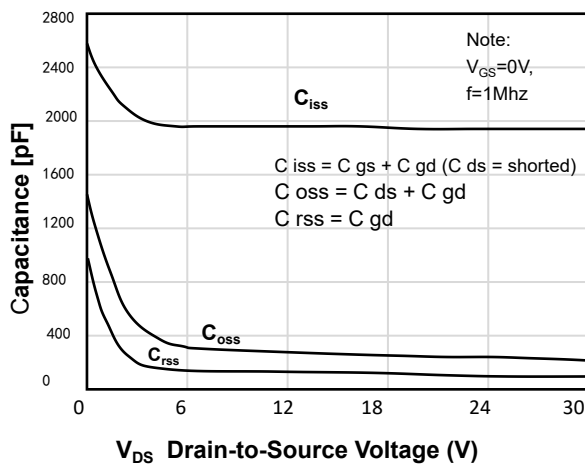
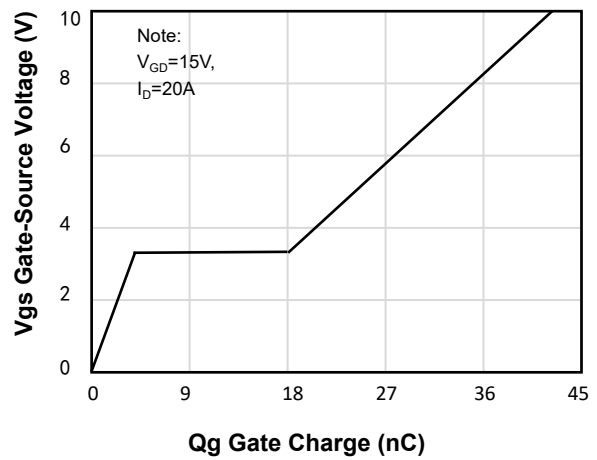
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Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$	---	---	1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	---	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(ON)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=30A$	---	3.8	4.5	$m\Omega$
		$V_{GS}=4.5V, I_D=20A$	---	5.5	7.0	$m\Omega$
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, \text{Freq.}=1\text{MHz}$	---	1960	---	pF
C_{oss}	Output Capacitance		---	320	---	
C_{rSS}	Reverse Transfer Capacitance		---	240	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DS}=15V, V_{GS}=10V, I_D=30A, R_G=3\Omega$	---	13	---	nS
T_r	Turn-on Rise Time		---	36	---	
$T_{d(off)}$	Turn-off Delay Time		---	43	---	
T_f	Turn-off Fall Time		---	16	---	
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_D=30A$	---	45	---	nC
Q_{gs}	Gate-Source Charge		---	4	---	
Q_{gd}	Gate-Drain Charge		---	14	---	
Source-Drain Characteristics						
I_S	Continuous Source Current		---	---	90	A
I_S	Pulsed Source Current		---	---	360	A
V_{SD}	Diode Forward Voltage	$I_S=30A, V_{GS}=0V$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=20A, V_{GS}=0V, dl/dt=100A/\mu s$	---	16	---	nS
Q_{rr}	Reverse Recovery Charge		---	5	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

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Typical Characteristics

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics

Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

Figure 5. Capacitance Characteristics

Figure 6. Gate Charge Characteristics

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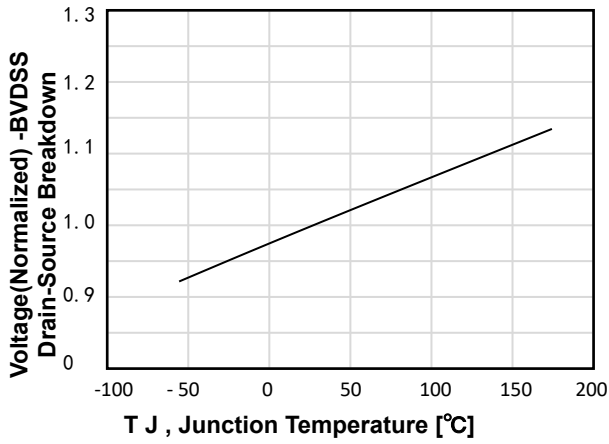


Figure 7. Breakdown Voltage Variation vs Temperature

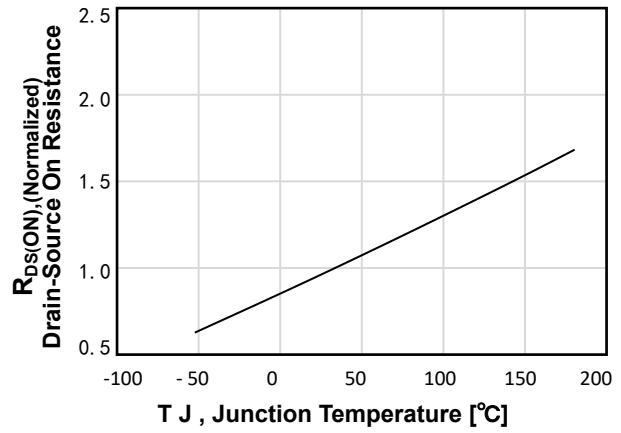


Figure 8. On-Resistance Variation vs Temperature

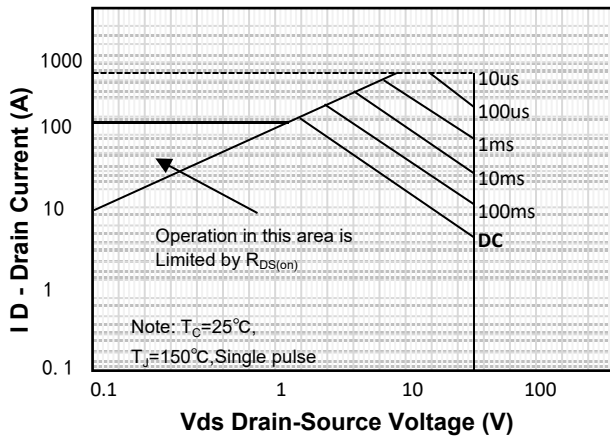


Figure 9. Maximum Safe Operating Area

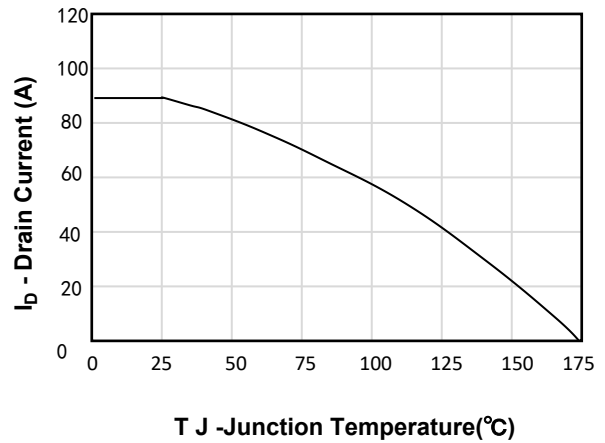


Figure 10. Maximum PContinuous Drain Current vs Case Temperature

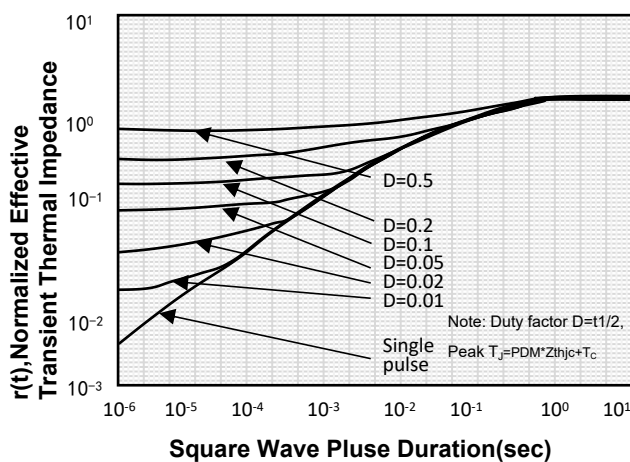
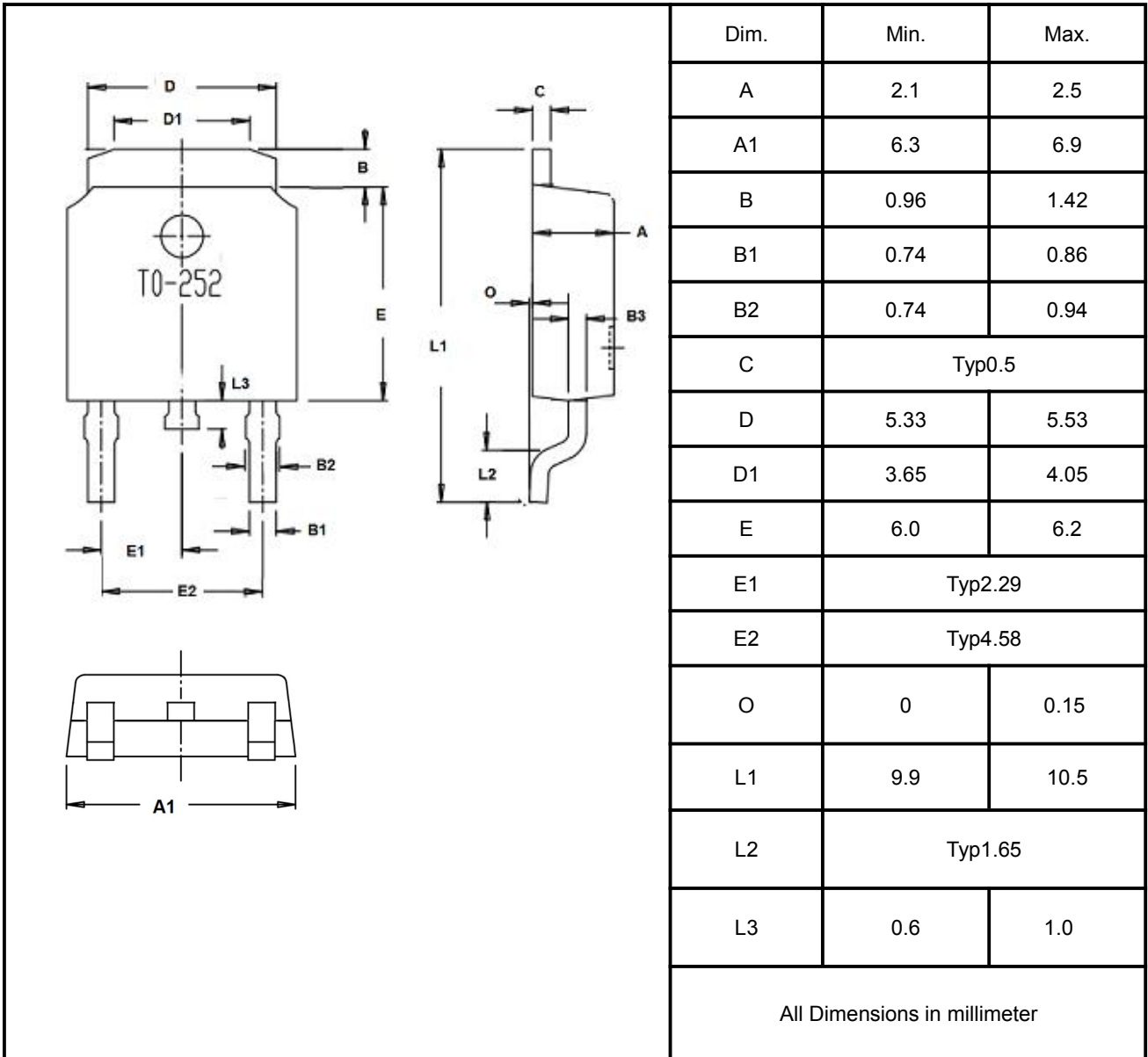


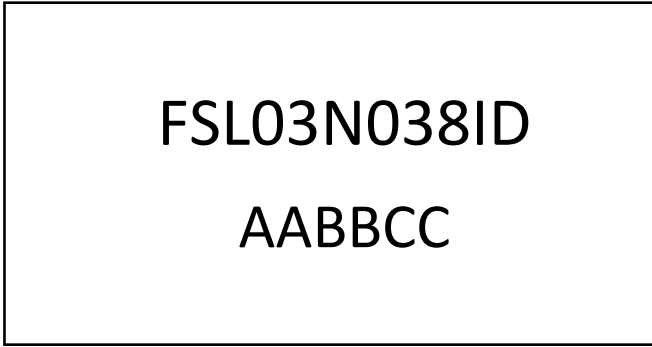
Figure 11. Transient Thermal Response Curve

N-Channel Enhancement Mode MOSFET
TO-252 Package Outline Dimensions




印字说明

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第一行标记为物料型号代码

第二行为AA为内部识别码，BB为表示年份，例如22即表示2022年，CC表示周期，例如01即表示第一周；2201即表示2022年第一周生产。