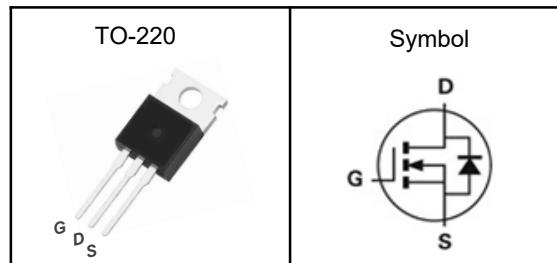


N-Channel Enhancement Mode MOSFET

Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Pin Description



Applications

- Power Management in Desktop Computer
- DC/DC Converters

V_{DSS}	30	V
$R_{DS(ON)-Typ}$	2.8	$\text{m}\Omega$
I_D	176	A

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	350	A
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	A
	Continuous Drain Current	$T_C=100^\circ\text{C}$	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{②}$	Thermal Resistance-Junction to Ambient	25	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1.1	$^\circ\text{C/W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

N-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=30\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$	---	---	1	μA
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}$, $\text{I}_D=250\mu\text{A}$	1.2	---	2.5	V
I_{GSS}	Gate Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=10\text{V}$, $\text{I}_D=10\text{A}$	---	40	---	S
$\text{R}_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=24\text{A}$	---	2.8	3.5	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=12\text{A}$	---	4.0	5.0	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}$, $\text{V}_{\text{DS}}=25\text{V}$, Freq.=1MHz	---	5200	---	pF
C_{oss}	Output Capacitance		---	650	---	
C_{rss}	Reverse Transfer Capacitance		---	500	---	
Q_{g}	Total Gate Charge	$\text{V}_{\text{DD}}=25\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=30\text{A}$	---	104	---	nC
Q_{gs}	Gate-Source Charge		---	14	---	
Q_{gd}	Gate-Drain Charge		---	31	---	
Source-Drain Characteristics						
$\text{V}_{\text{SD}}^{④}$	Diode Forward Voltage	$\text{I}_S=24\text{A}$, $\text{V}_{\text{GS}}=0\text{V}$	---	0.85	1.28	V

Note ④ : Pulse test (pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET

Typical Characteristics

Fig.1 Power Dissipation

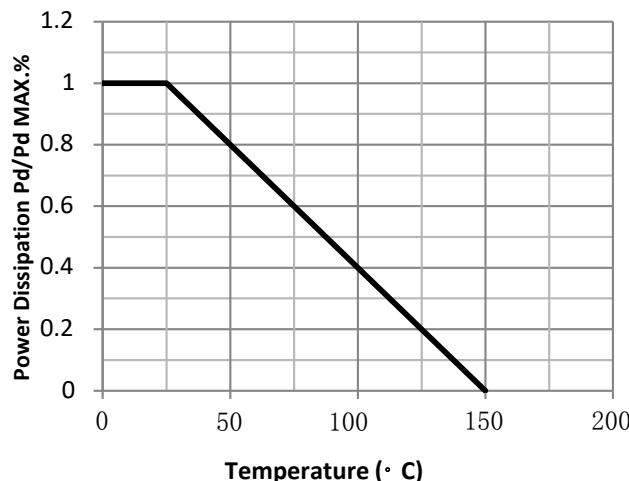


Fig.2 Typical output Characteristics

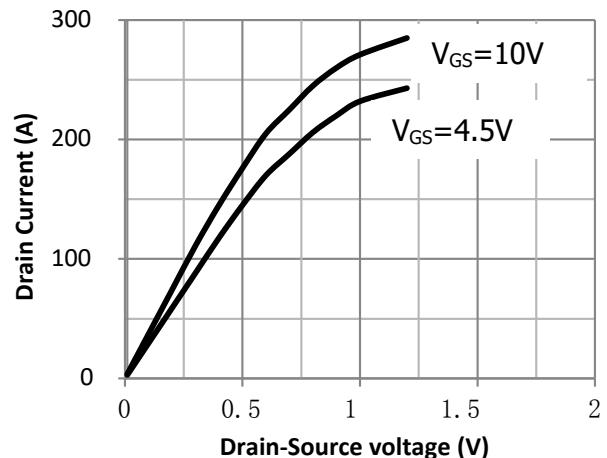


Fig.3 Threshold Voltage V.S Junction Temperature

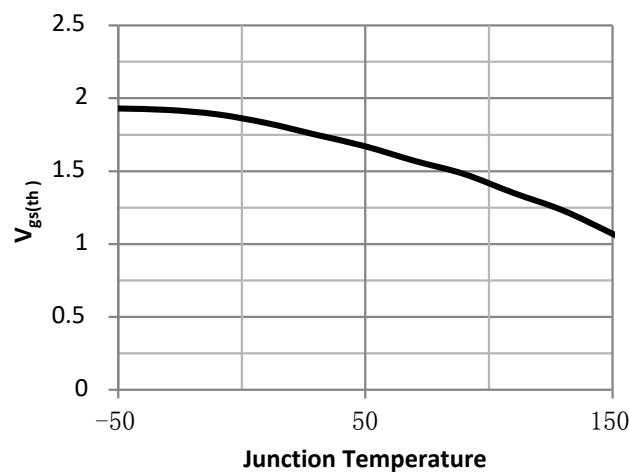


Fig.4 Resistance V.S Drain Current

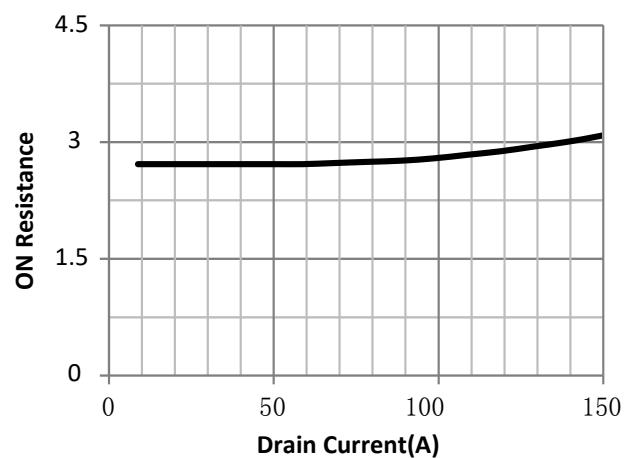


Fig.5 On-Resistance VS Gate Source Voltage

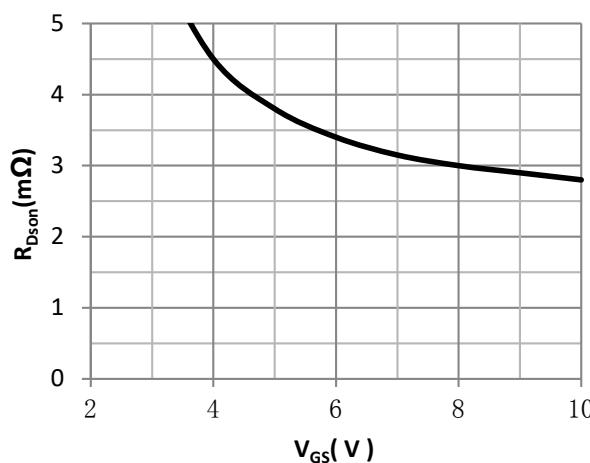
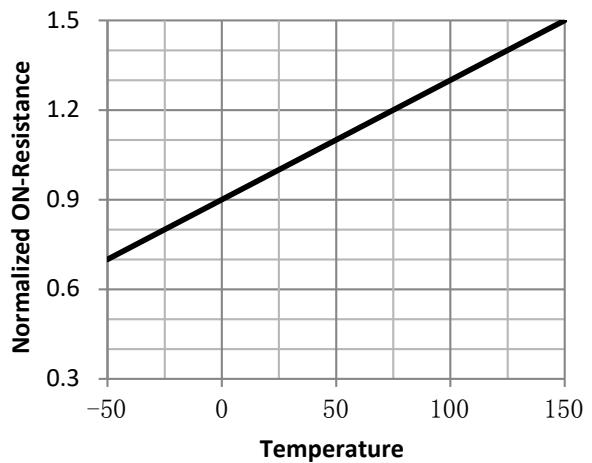


Fig.6 On-Resistance V.S Junction Temperature



N-Channel Enhancement Mode MOSFET

Fig.7 Switching Time Measurement Circuit

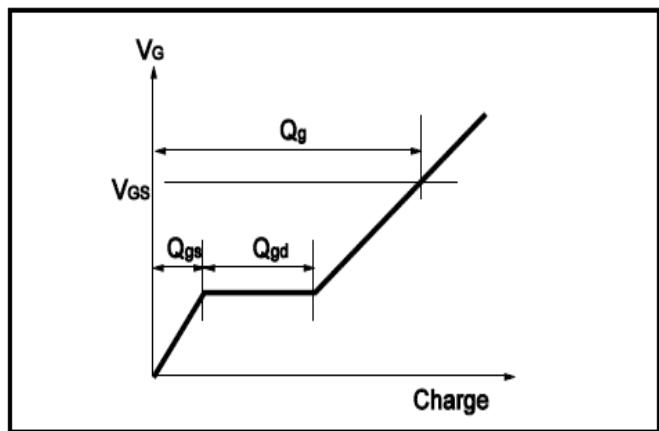
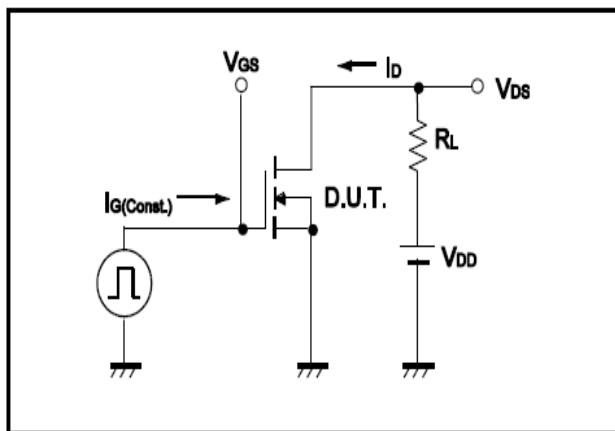


Fig.9 Switching Time Measurement Circuit

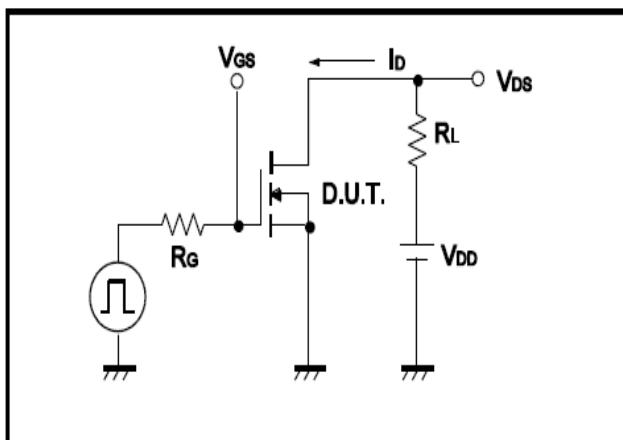


Fig.10 Gate Charge Waveform

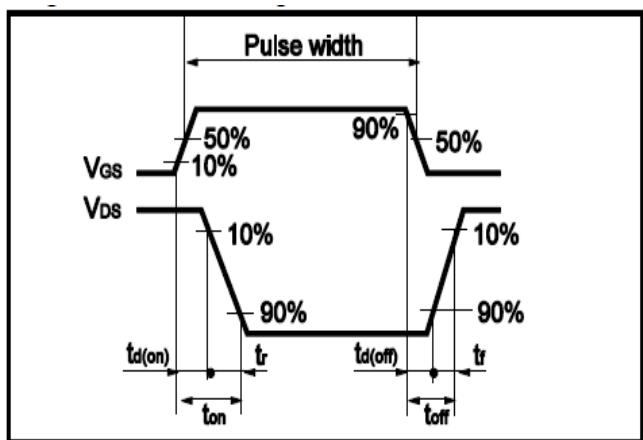


Fig.11 Avalanche Measurement Circuit

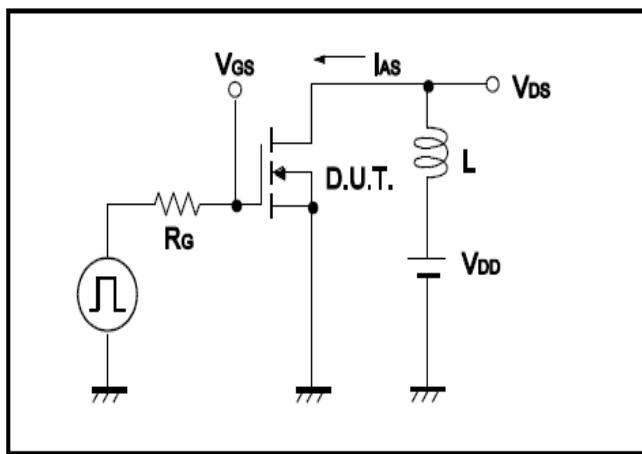
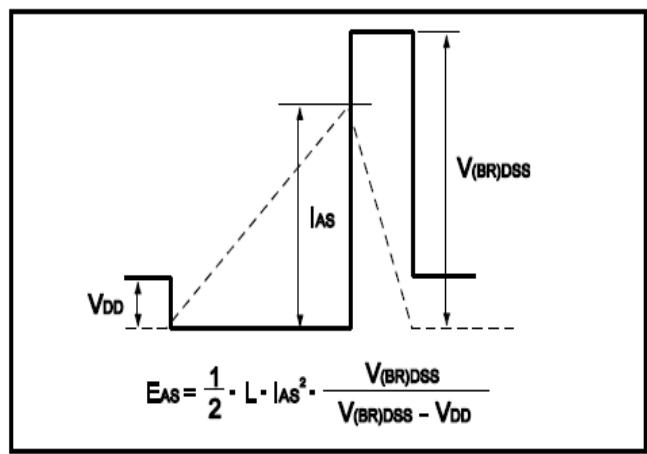
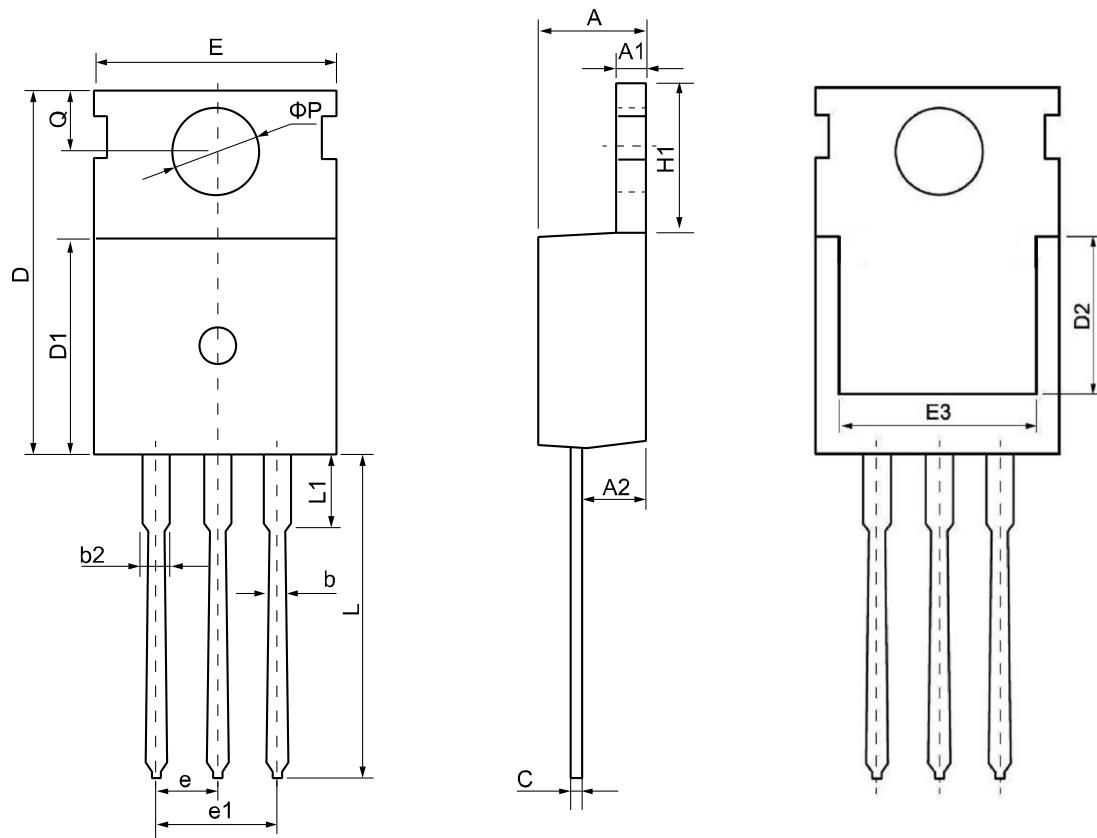


Fig.12 Avalanche Waveform



N-Channel Enhancement Mode MOSFET
TO-220 Package Outline Data


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	4.30	4.55	4.75	E	9.65	10.00	10.25
A1	1.15	1.30	1.45	E3	7.00	--	--
A2	2.20	2.40	2.60	e	2.54 BSC		
b	0.70	0.80	0.95	e1	5.08 BSC		
b2	1.17	1.27	1.47	H1	6.30	6.50	6.80
c	0.40	0.50	0.65	L	12.70	13.50	14.10
D	15.30	15.60	15.90	L1	--	3.20	3.95
D1	8.90	9.10	9.35	ϕP	3.40	3.60	3.80
D2	5.50	--	--	Q	2.60	2.80	3.00



FSL03N028FE

N-Channel Enhancement Mode MOSFET

印字说明

印字说明

FSL03N028FE

AABBCC

第一行标记为物料型号代码

第二行为AA为内部识别码，BB为表示年份，例如22即表示2022年，CC表示周期，例如01即表示第一周；
2201即表示2022年第一周生产。