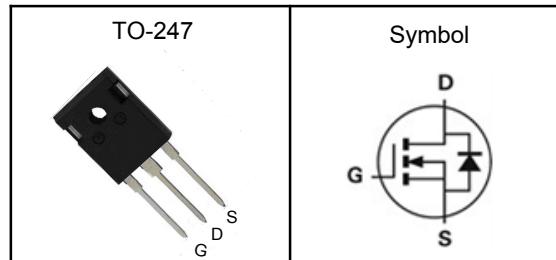


N-Channel Enhancement Mode MOSFET

Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

Pin Description



Applications

- Power Management in Desktop Computer
- DC/DC Converters

V_{DSS}	200	V
$R_{DS(ON)-Typ}$	9.1	$\text{m}\Omega$
I_D	130	A

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	200	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 175	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ\text{C}$
E_{AS}	Single Pulse Avalanche Energy	960	mJ
$I_{DM}^{\text{(1)}}$	Pulse Drain Current Tested	520	A
I_D	Continuous Drain Current $T_c=25^\circ\text{C}$	130	A
	Continuous Drain Current $T_c=100^\circ\text{C}$	92	A
P_D	Maximum Power Dissipation $T_c=25^\circ\text{C}$	507	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{\text{(3)}}$	Thermal Resistance-Junction to Ambient	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.3	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

N-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	200	---	---	V
$I_{\text{DS}(\text{S})}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=200\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	10	μA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	3.0	---	5.0	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
$R_{\text{DS}(\text{ON})}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=35\text{A}$	---	9.1	9.8	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=50\text{V}$, Freq.=1.0MHz	---	11972	---	pF
C_{oss}	Output Capacitance		---	860	---	
C_{rss}	Reverse Transfer Capacitance		---	380	---	
$T_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$, $V_{\text{DD}}=130\text{V}$, $I_D=81\text{A}$, $R_G=2.7\Omega$	---	40	---	nS
T_r	Turn-on Rise Time		---	105	---	
$T_{\text{d}(\text{off})}$	Turn-off Delay Time		---	65	---	
T_f	Turn-off Fall Time		---	75	---	
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}$, $V_{\text{DD}}=100\text{V}$, $I_D=81\text{A}$	---	201	---	nC
Q_{gs}	Gate-Source Charge		---	76	---	
Q_{gd}	Gate-Drain Charge		---	70	---	
R_g	Gate resistance	f=1 MHz, open drain	---	0.8	---	Ω
Source-Drain Characteristics						
I_s	Continuous Source Current		--	---	130	A
ISM	Maximum Pulsed Drain-Source Diode Forward Current		--	---	520	A
V_{SD}	Diode Forward Voltage	$I_s=81\text{A}$, $V_{\text{GS}}=0\text{V}$	---	---	1.4	V
t_{rr}	Reverse recovery time	$I_s=30\text{A}$, $V_{\text{GS}}=0\text{V}$ diF/dt=100A/ μs	---	153	---	ns
Q_{rr}	Reverse recovery charge		---	1.06	---	nC
Irrm	Peak Reverse Recovery Current		---	13.5	---	A

Note ④: Pulse test (pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$).

Note ⑤: Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET

Typical Characteristics

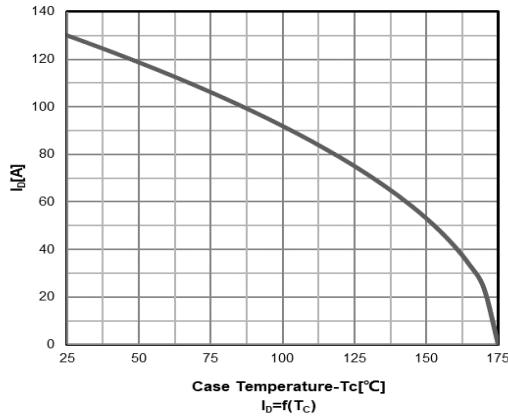


Figure 1: Continuous Drain Current vs Temperature

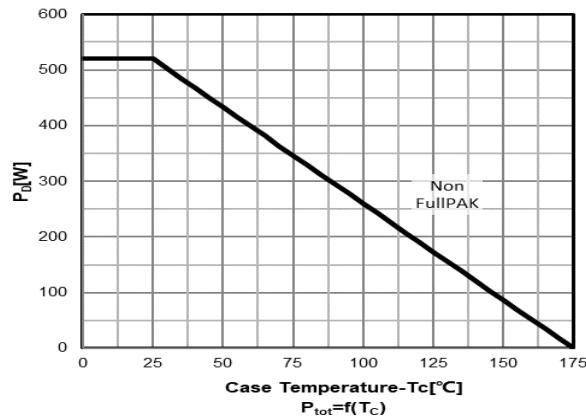


Figure 2: Power Dissipation

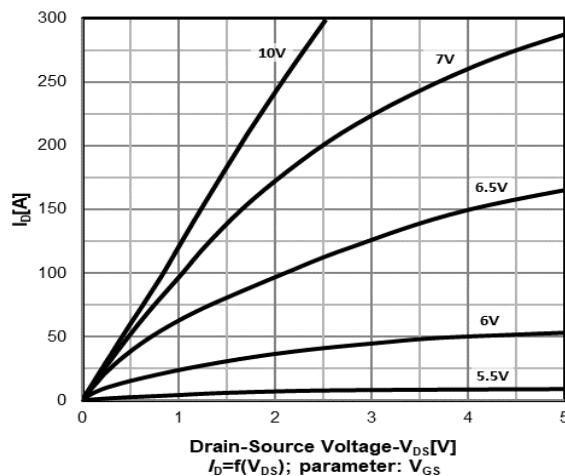


Figure 3: Typ. output characteristics
Parameter: V_{GS}

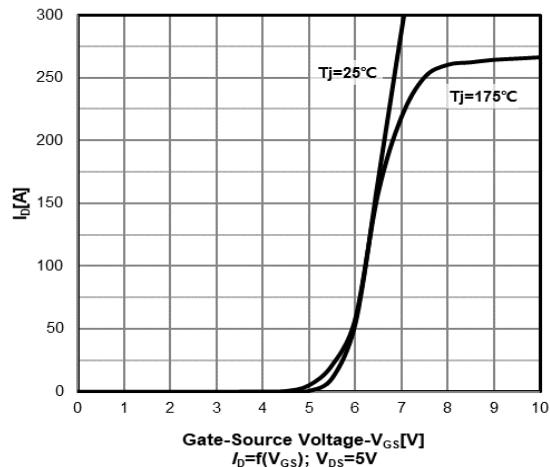


Figure 4: Typ. transfer characteristics

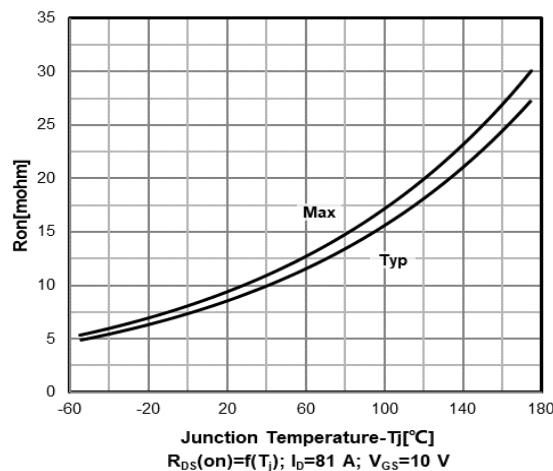


Figure 5: On-Resistance vs Junction Temperature

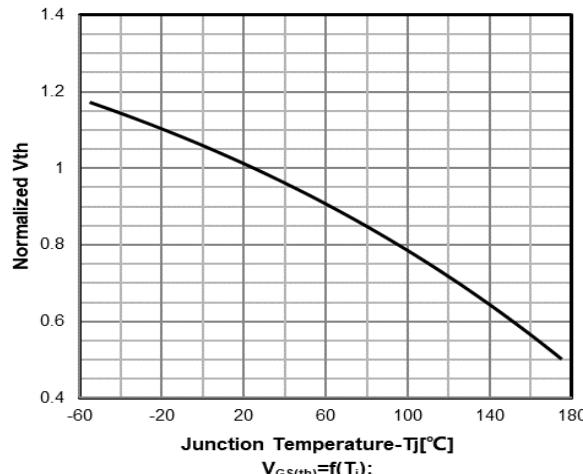


Figure 6: V_{th} vs Junction Temperature

N-Channel Enhancement Mode MOSFET

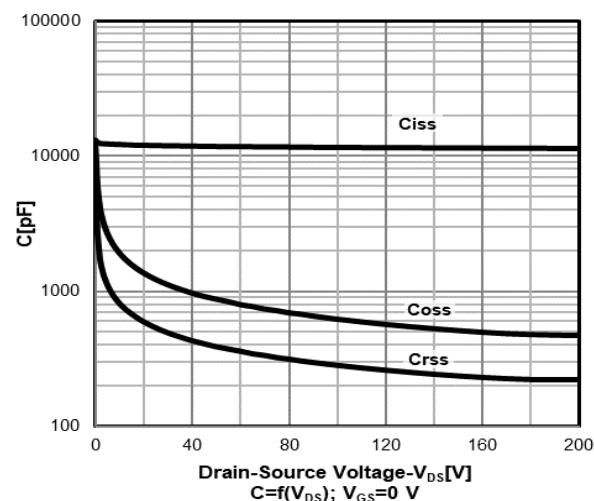
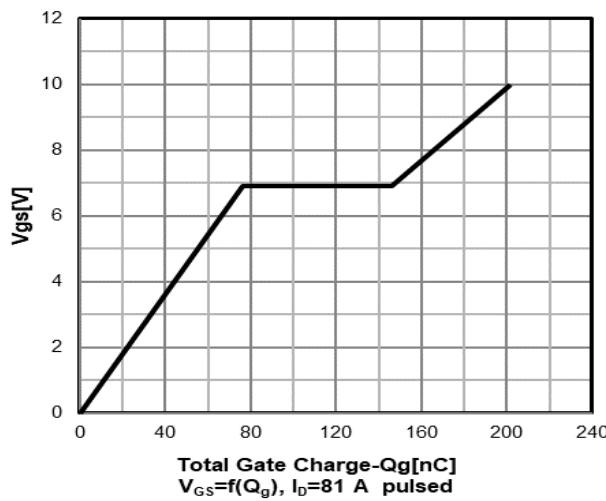


Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characteristics

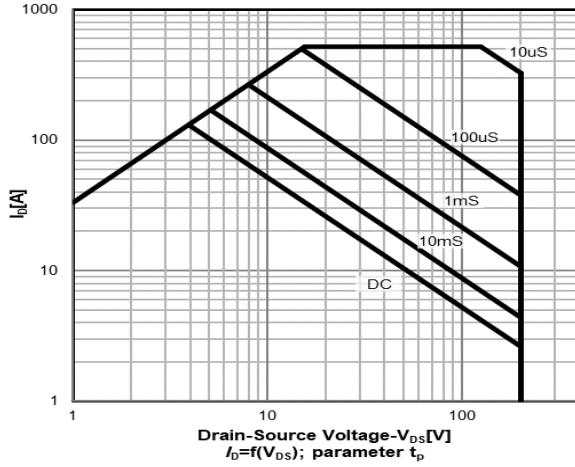
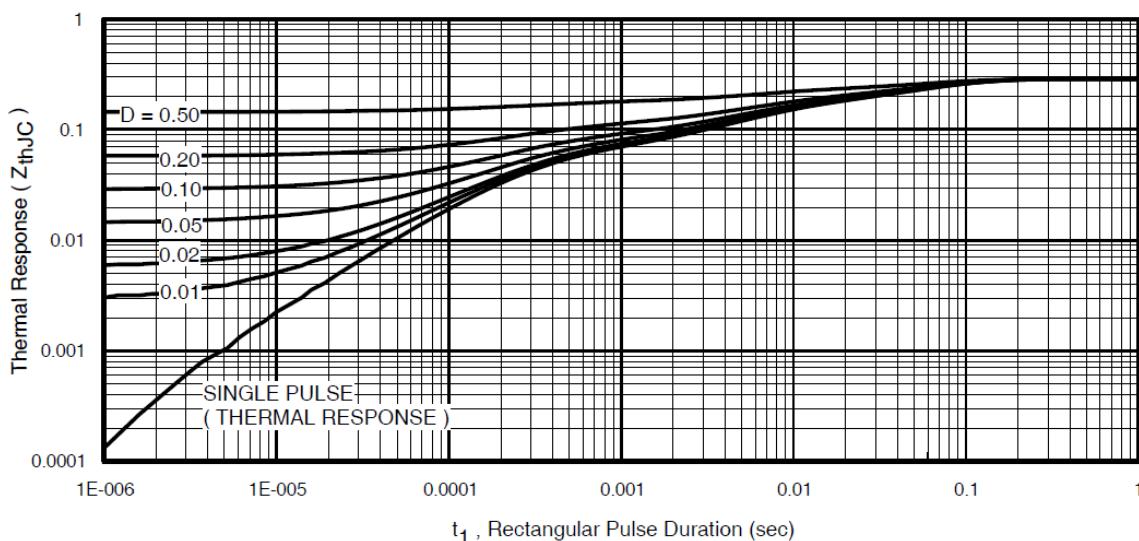
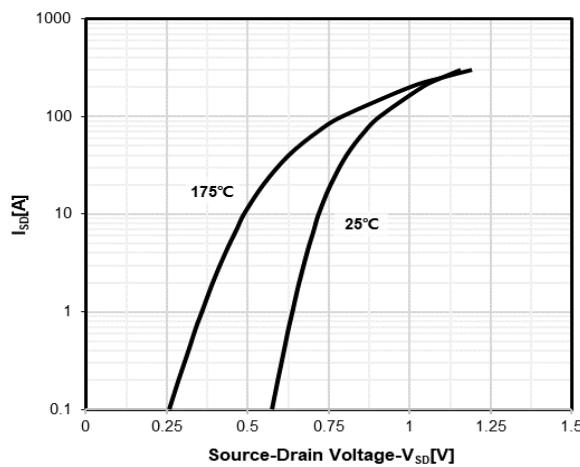
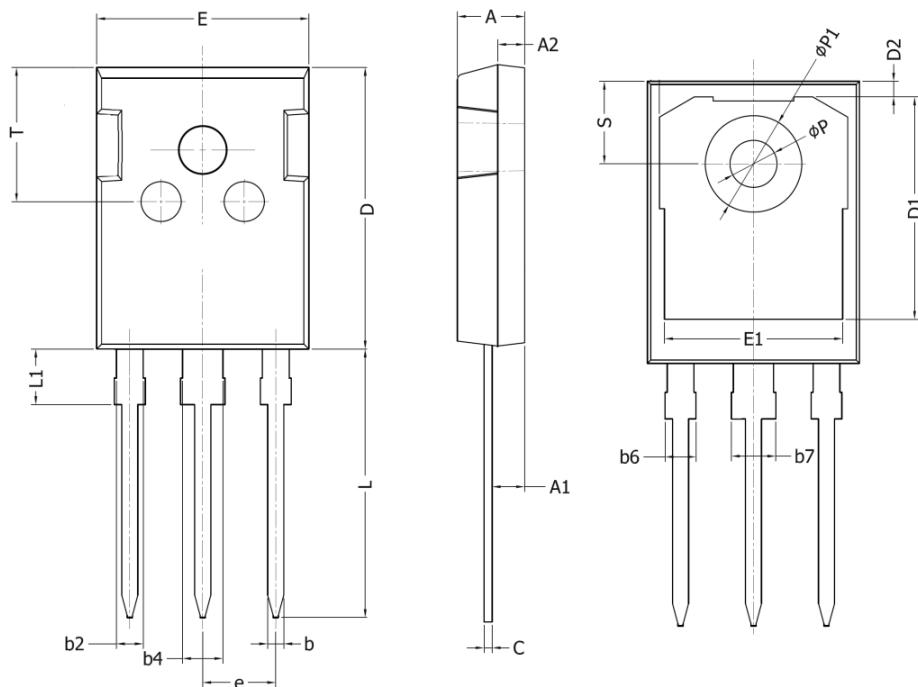


Figure 9: Maximum Forward Biased Safe Operating Area
Parameter: t_p



N-Channel Enhancement Mode MOSFET

TO-247 Package Outline Dimensions



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.90	5.20
A1	2.31	2.51
A2	1.9	2.1
b	1.16	1.26
b2	1.96	2.06
b4	2.96	3.06
b6	-	2.25
b7	-	3.25
C	0.59	0.66
D	20.90	21.20
D1	16.25	16.85
D2	1.05	1.35
E	15.75	16.10
E1	13.00	13.60
e	5.436 BSC	
L	19.80	20.20
L1	-	4.30
P	3.40	3.60
P1	7.00	7.40
S	6.05	6.25
T	9.80	10.20