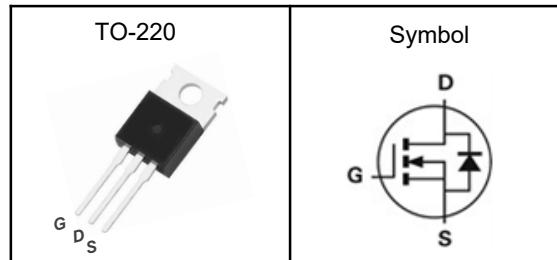


## N-Channel Enhancement Mode MOSFET

### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

$V_{DSS}$	100	V
$R_{DS(ON)-Typ}$	4.2	$\text{m}\Omega$
$I_D$	125	A

### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 175	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 175	$^\circ\text{C}$
$I_{DM}^{\text{(1)}}$	Pulse Drain Current Tested	500	A
$I_D$	Continuous Drain Current	125	A
$P_D$	Maximum Power Dissipation	200	W
$E_{AS}$	Avalanche Energy, Single pulse	320	mJ

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{\text{(3)}}$	Thermal Resistance-Junction to Ambient	56	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.75	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

**N-Channel Enhancement Mode MOSFET**
**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	100	---	---	V
$I_{\text{DS}}^{\text{SS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=100\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\mu\text{A}$	2.5	---	3.5	V
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=20\text{A}$	---	4.2	5.8	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=0\text{V}$ , Freq.=1MHz	---	3810	---	pF
$C_{\text{oss}}$	Output Capacitance		---	558	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	18	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=20\text{A}$ , $R_{\text{G}}=3\Omega$	---	14.9	---	nS
$T_r$	Turn-on Rise Time		---	14	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	32.5	---	
$T_f$	Turn-off Fall Time		---	15	---	
$Q_g$	Total Gate Charge	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=20\text{A}$	---	51	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	15	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	14	---	
<b>Source-Drain Characteristics</b>						
$V_{\text{SD}}$	Diode Forward Voltage	$I_{\text{S}}=20\text{A}$ , $V_{\text{GS}}=0\text{V}$	---	---	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_{\text{S}}=20\text{A}$ , $V_{\text{GS}}=0\text{V}$ , $dI_{\text{F}}/dt=100\text{A}/\mu\text{s}$	---	63	---	nS
$Q_{\text{rr}}$	Reverse Recovery Charge		---	164	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu\text{s}$ , duty cycle $\leq 2\%$ ).

Note ⑤ : Guaranteed by design, not subject to production testing.

## N-Channel Enhancement Mode MOSFET

### Typical Characteristics

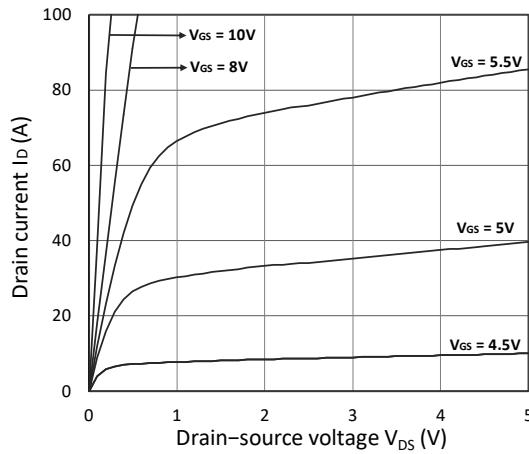


Figure 1. Output Characteristics

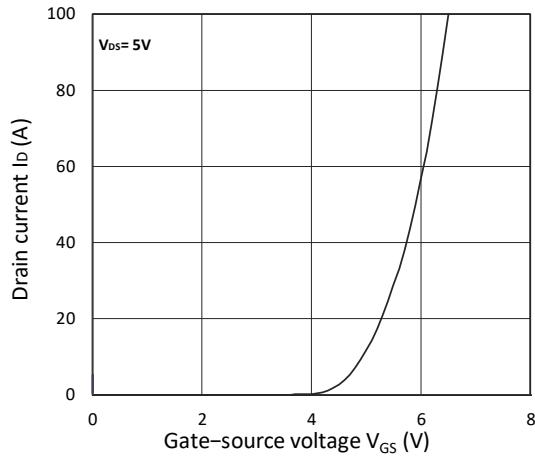


Figure 2. Transfer Characteristics

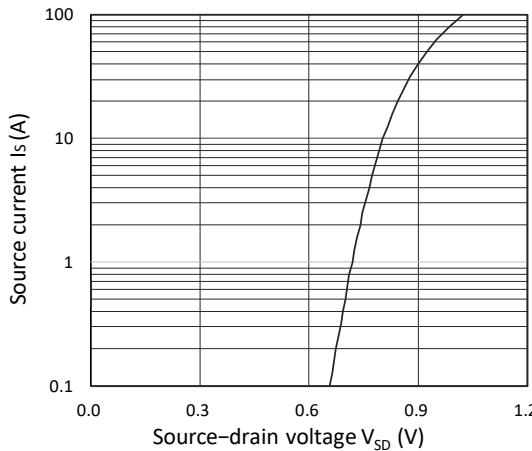


Figure 3. Forward Characteristics of Reverse

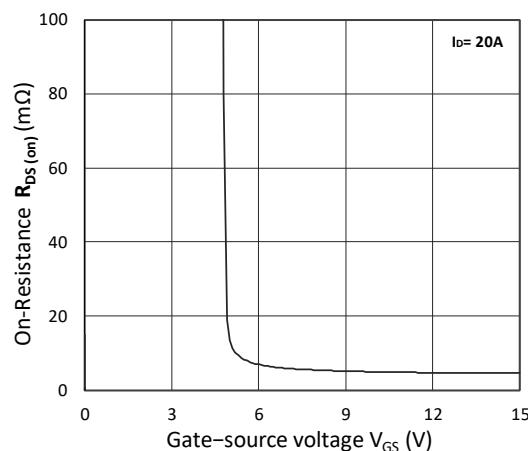


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

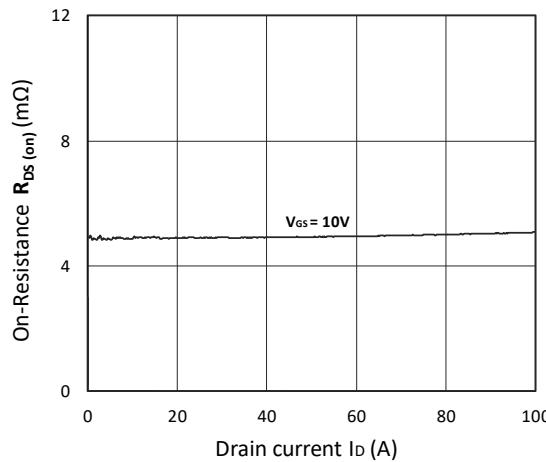


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

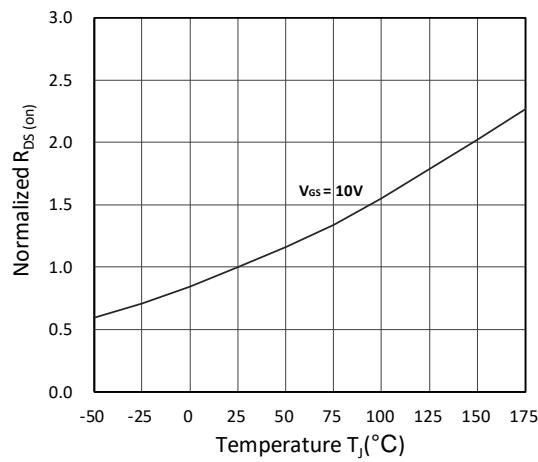


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature

## N-Channel Enhancement Mode MOSFET

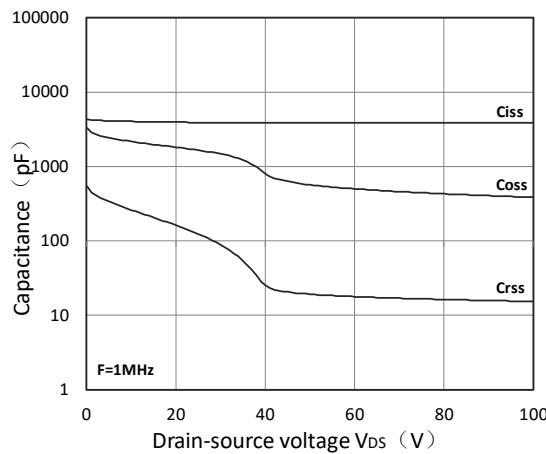


Figure 7. Capacitance Characteristics

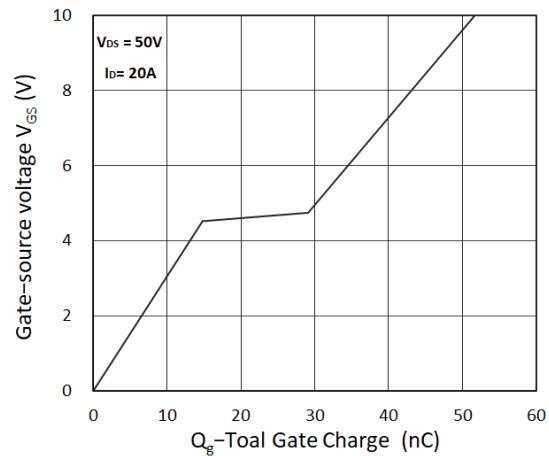


Figure 8. Gate Charge Characteristics

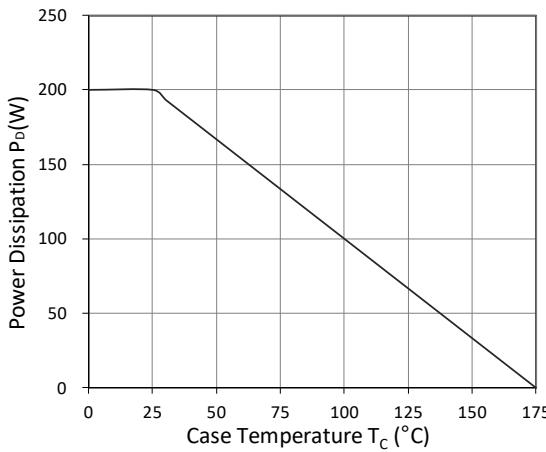


Figure 9. Power Dissipation

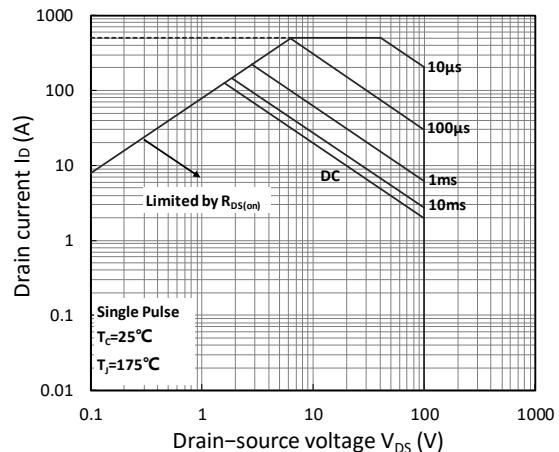


Figure 10. Safe Operating Area

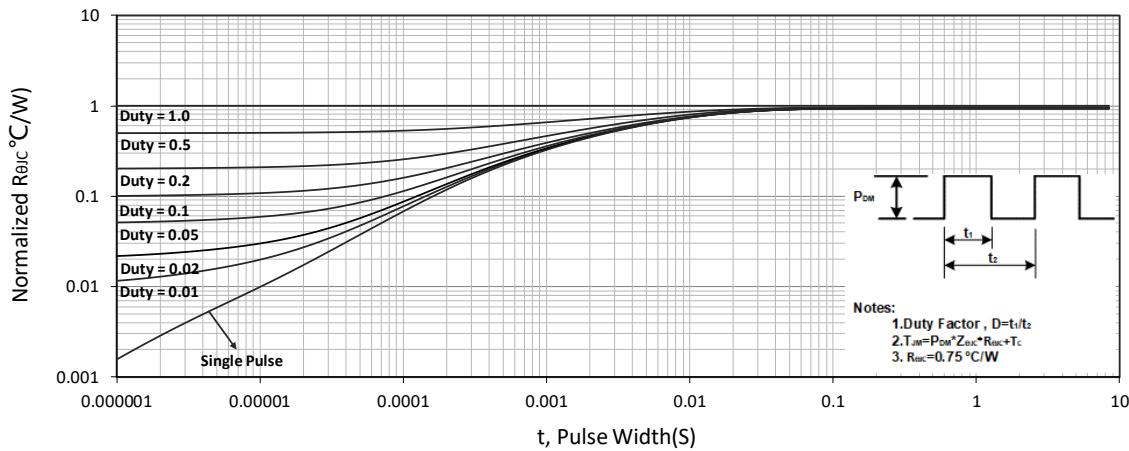
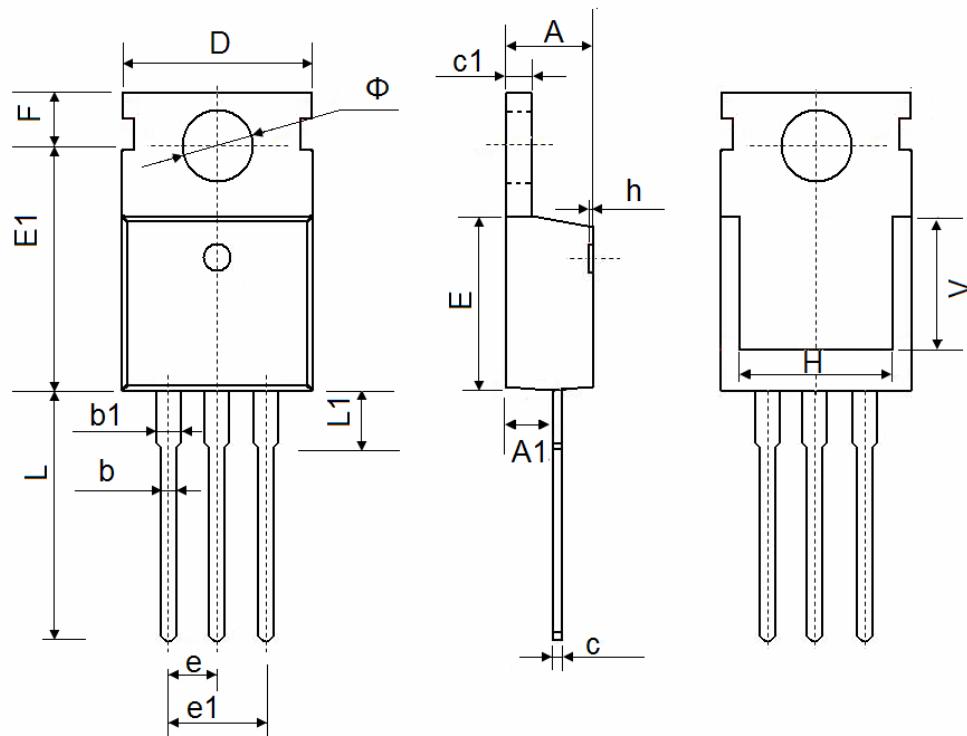


Figure 11. Normalized Maximum Transient Thermal Impedance

**N-Channel Enhancement Mode MOSFET**
**TO-220 Package Outline Data**


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.350	4.650
A1	2.250	2.550
b	0.710	0.910
b1	1.170	1.400
c	0.330	0.650
c1	1.200	1.400
D	9.910	10.250
E	8.9500	9.750
E1	12.650	12.950
e	2.540 TYP.	
e1	4.980	5.180
F	2.650	2.950
H	7.900	8.100
h	0.000	0.300
L	12.700	13.500
L1	2.850	3.250
V	7.500 REF.	
Φ	3.400	3.800