

N-Channel Enhancement Mode MOSFET

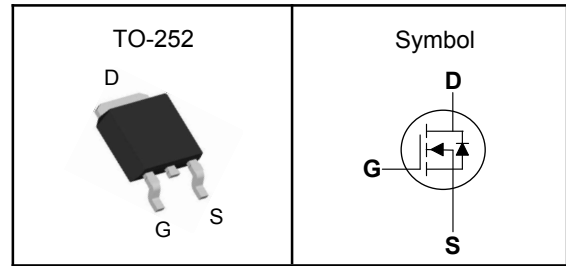
Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description



V _{DSS}	650	V
R _{DS(ON)-Typ}	1100	mΩ
I _D	7	A

Absolute Maximum Ratings (T_C=25°C, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V _{DSS}	Drain-Source Voltage	650	V
V _{GSS}	Gate-Source Voltage	±30	V
T _J	Maximum Junction Temperature	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
E _{AS}	Single Pulse Avalanche Energy ³	230	mJ
I _{DM} ^①	Pulse Drain Current Tested	28	A
I _D	Continuous Drain Current	7	A
P _D	Maximum Power Dissipation	97	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ₁ (Max)	60	°C/W
R _{θJC}	Thermal Resistance Junction-Case ₁	1.29	°C/W

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C.

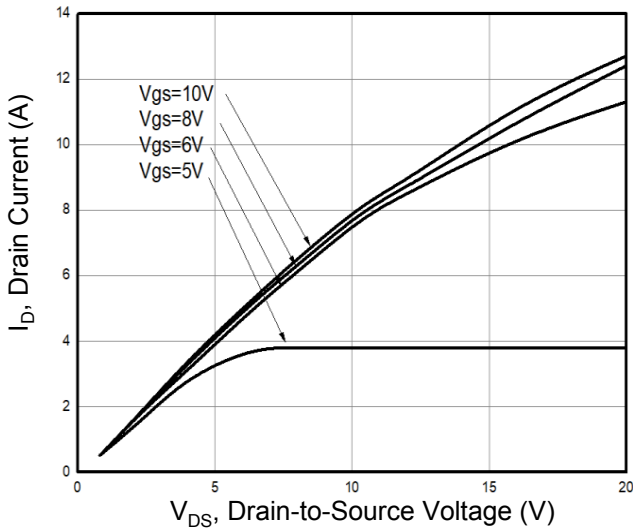
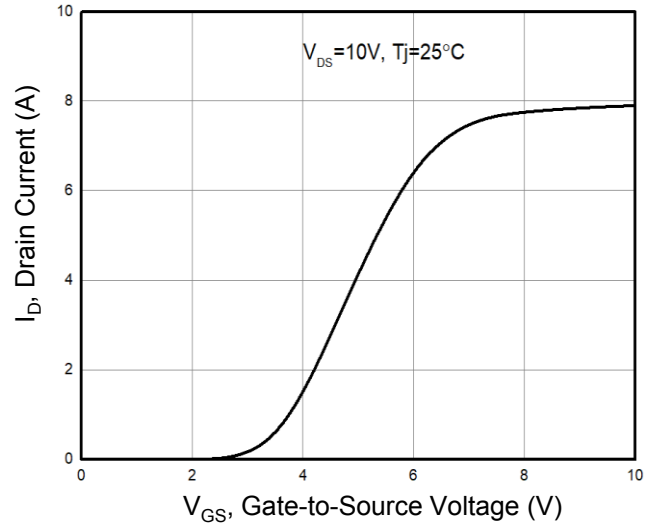
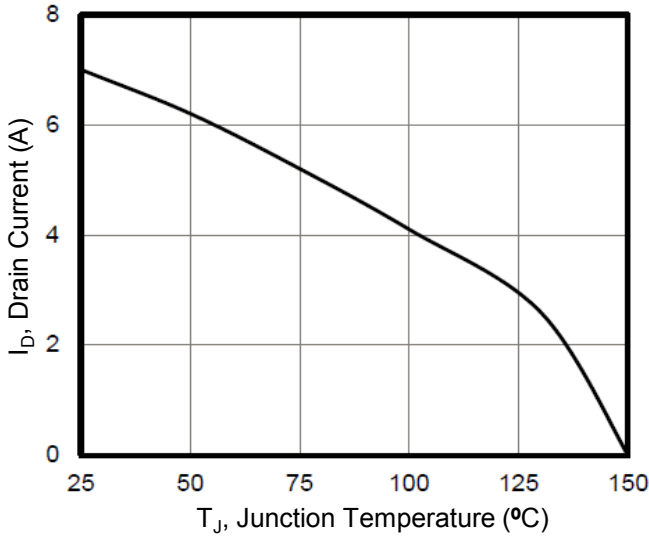
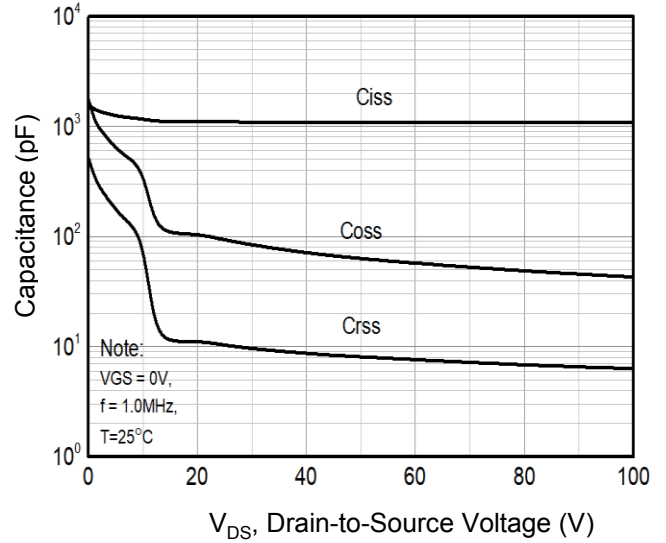
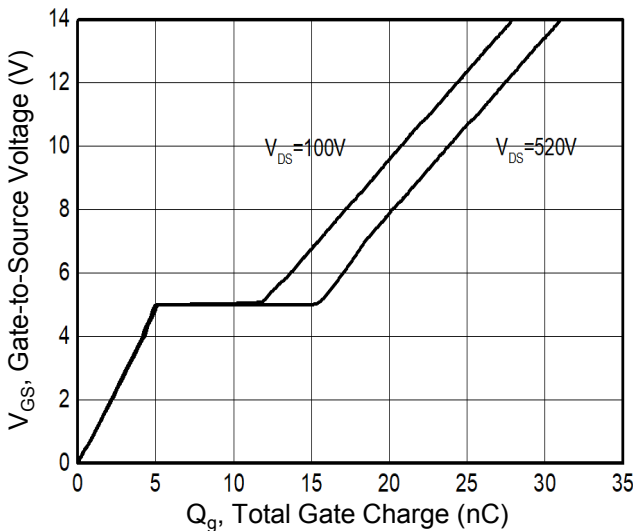
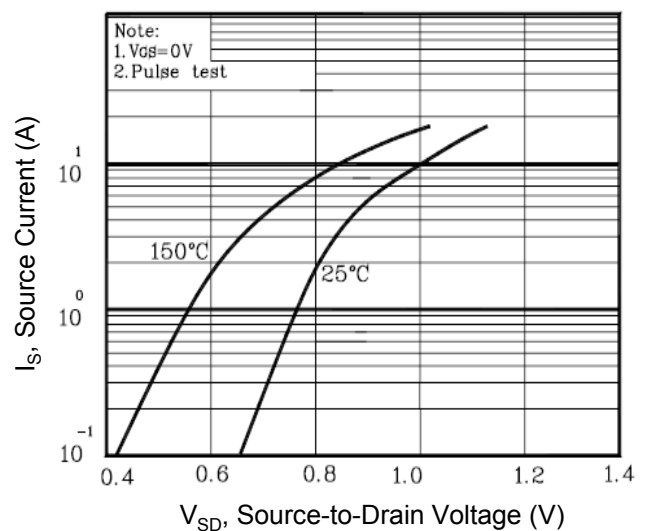
Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

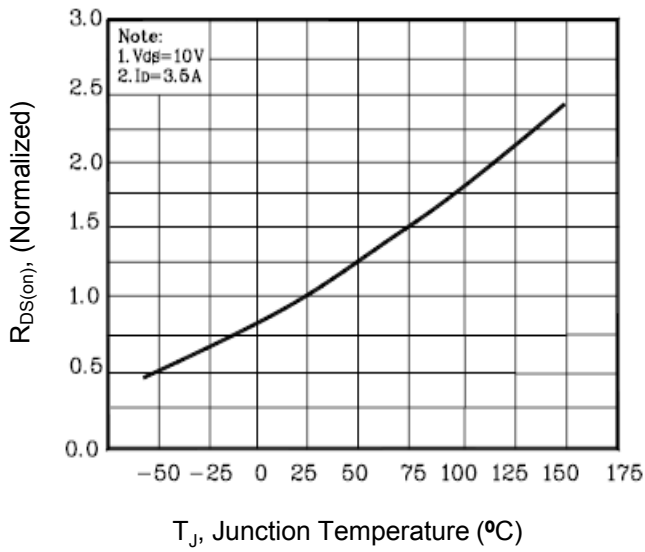
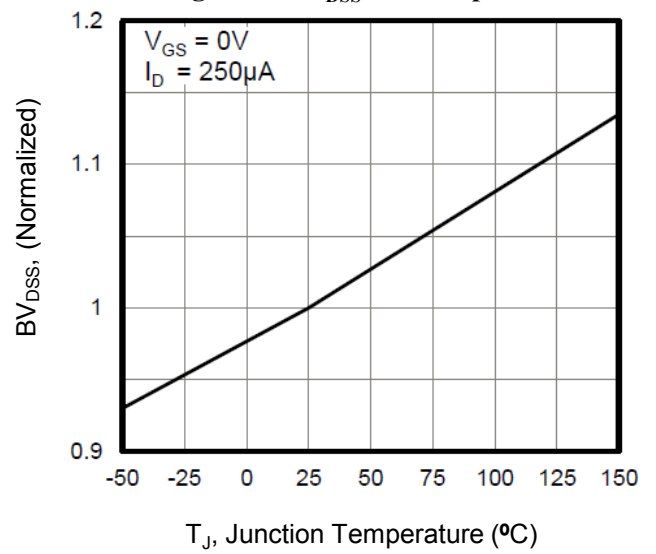
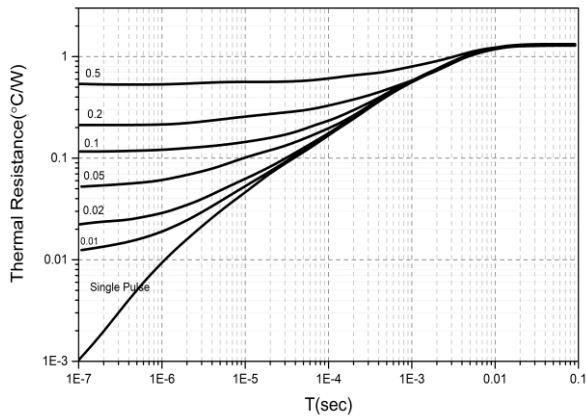
**N-Channel Enhancement Mode MOSFET****Electrical Characteristics** ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	650	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=650V, V_{GS}=0V$	---	---	1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	---	4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 30V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(ON)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=3.5A$	---	1100	1300	$m\Omega$
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=25V,$ Freq.=1MHz	---	1098	---	pF
C_{oss}	Output Capacitance		---	93	---	
C_{rss}	Reverse Transfer Capacitance		---	11	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=325V, R_G=25\Omega,$ $I_D=7A$	---	29	---	nS
T_r	Turn-on Rise Time		---	48	---	
$T_{d(off)}$	Turn-off Delay Time		---	39	---	
T_f	Turn-off Fall Time		---	33	---	
Q_g	Total Gate Charge	$V_{DD}=400V, V_{GS}=10V,$ $I_D=7A$	---	20	---	nC
Q_{gs}	Gate-Source Charge		---	4	---	
Q_{gd}	Gate-Drain Charge		---	7	---	
Source-Drain Characteristics ($T_J=25^\circ\text{C}$)						
V_{SD}	Diode Forward Voltage ₂	$V_{GS}=0V, I_S=7A, T_J=25^\circ\text{C}$	---	---	1.4	V
t_{rr}	Reverse Recovery Time	$V_R=400V, I_S=7A,$ $di/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	365	---	nS
Q_{rr}	Reverse Recovery Charge		---	3.4	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET
Typical Characteristics
Figure 1. Output Characteristics

Figure 2. Transfer Characteristics

Figure 3. Drain Current vs. Temperature

Figure 4. Capacitance

Figure 5. Gate Charge

Figure 6. Body Diode Forward Voltage


N-Channel Enhancement Mode MOSFET
Figure 7. On-Resistance vs. Temperature

Figure 8. BV_{DSS} vs. Temperature

**Figure 9. Transient Thermal Impedance
(TO-252)**


N-Channel Enhancement Mode MOSFET
TO-252 Package Outline Dimensions
