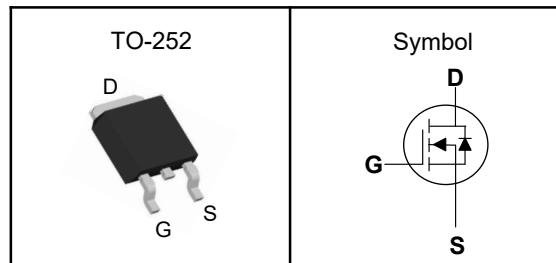


700V Super Junction Power MOSFET

Features

- Low drain-source on-resistance: $R_{DS(ON)}=0.55\Omega(\text{typ})$
- Easy to control gate switching
- Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0\text{V}$
- 100% avalanche tested
- RoHS compliant

Pin Description



Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger, Lighting

V_{DSS}	700	V
$R_{DS(ON)-\text{Typ}}$	550	$\text{m}\Omega$
I_D	8	A

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	700	V
V_{GSS}	Gate-Source Voltage	± 30	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
E_{AS}	Single Pulse Avalanche Energy ^③	240	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	32	A
I_D	Continuous Drain Current	$T_c=25^\circ\text{C}$	8
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	80
			W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁	1.55	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.



FS70R620CD

700V Super Junction Power MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	700	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=700\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	2.0	---	4.0	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
$R_{\text{DS}(\text{ON})}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=4\text{A}$	---	550	620	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=500\text{V}$, Freq.=1.0MHz	---	495	---	pF
C_{oss}	Output Capacitance		---	36	---	
C_{rss}	Reverse Transfer Capacitance		---	2.3	---	
$T_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{GS}}=10\text{V}$, $V_{\text{DD}}=400\text{V}$, $I_{\text{D}}=8\text{A}$, $R_{\text{G}}=25\Omega$	---	17	---	nS
T_r	Turn-on Rise Time		---	26	---	
$T_{\text{d}(\text{off})}$	Turn-off Delay Time		---	53	---	
T_f	Turn-off Fall Time		---	38	---	
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}$, $V_{\text{DD}}=400\text{V}$, $I_{\text{D}}=4\text{A}$	---	11	---	nC
Q_{gs}	Gate-Source Charge		---	2.4	---	
Q_{gd}	Gate-Drain Charge		---	4.7	---	
R_g	Gate resistance	f=1 MHz, open drain	---	26	---	Ω
Source-Drain Characteristics						
I_s	Continuous Source Current		---	---	8	A
ISM	Maximum Pulsed Drain-Source Diode Forward Current		---	---	32	A
V_{SD}	Diode Forward Voltage	$I_s=8\text{A}$, $V_{\text{GS}}=0\text{V}$	---	---	1.4	V
t_{rr}	Reverse recovery time	$I_s=4\text{A}$, $V_{\text{GS}}=0\text{V}$, $dI/dt=100\text{A}/\mu\text{s}$	---	240	---	ns
Q_{rr}	Reverse recovery charge		---	1.5	---	nC
I_{rrm}	Peak Reverse Recovery Current		---	13.5	---	A

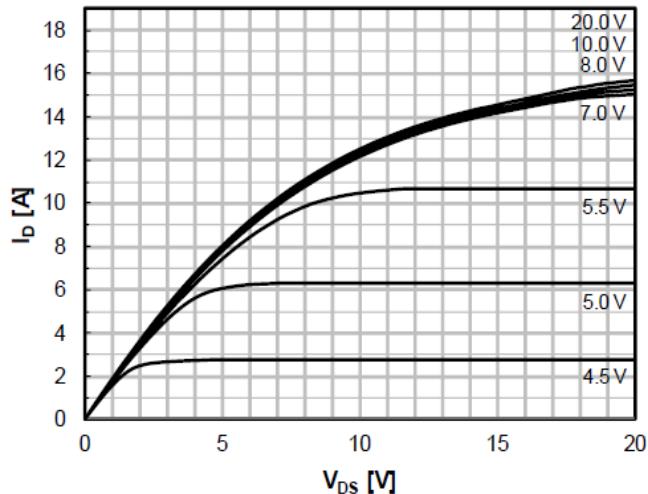
Note ④: Pulse test (pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$).

Note ⑤: Guaranteed by design, not subject to production testing.

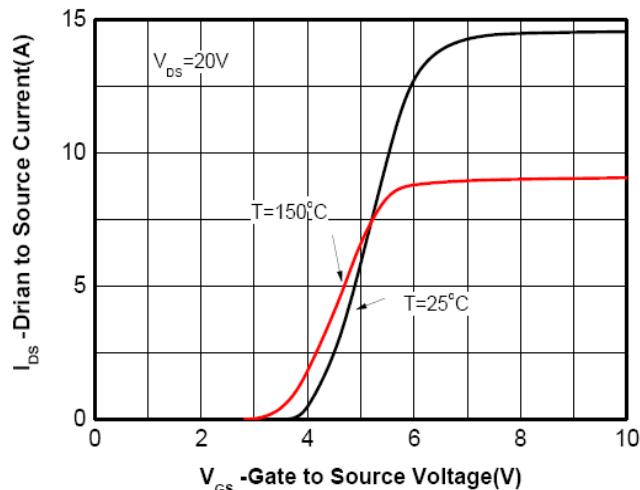
700V Super Junction Power MOSFET

Typical Characteristics

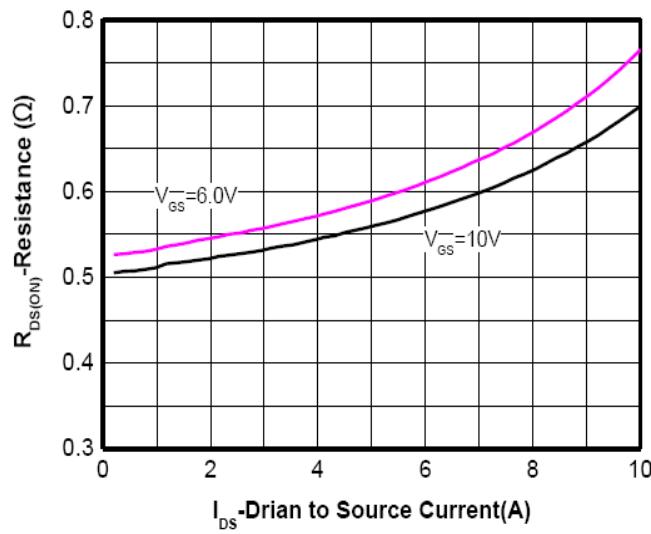
Typ. output characteristics $T_f=25\text{ }^{\circ}\text{C}$



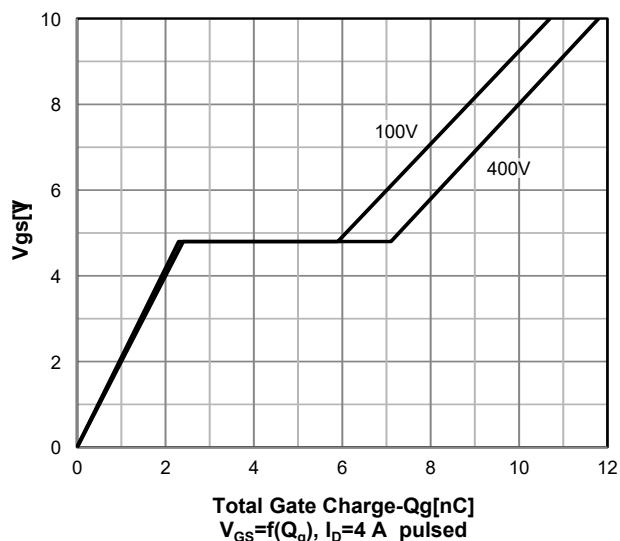
Typ. transfer characteristics



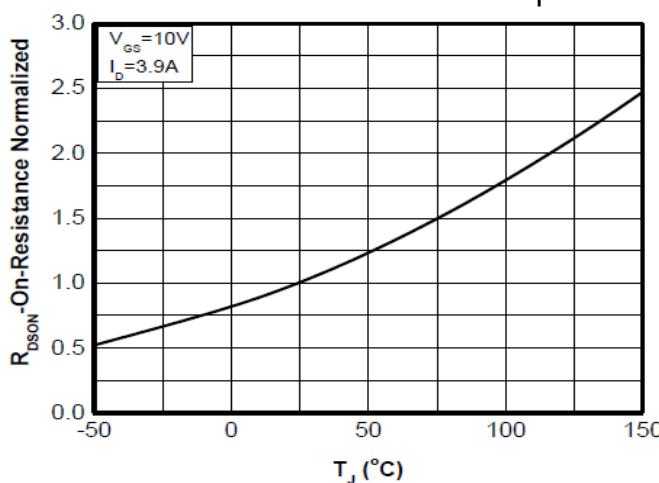
Typ. drain-source on-state resistance



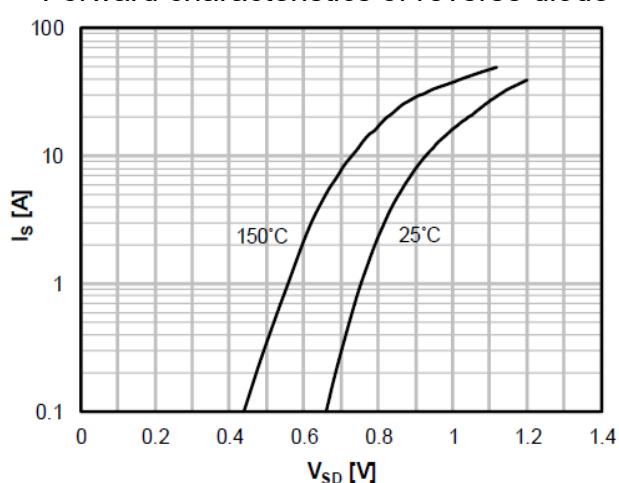
Typ. gate charge characteristics



Normalized on-resistance vs temperature

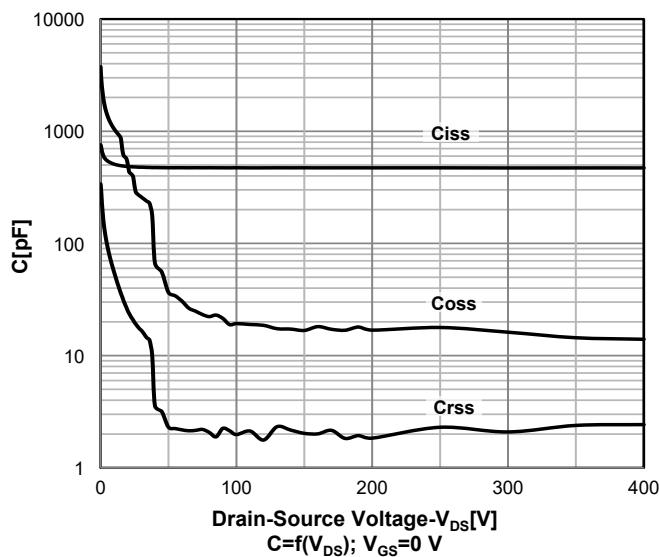


Forward characteristics of reverse diode

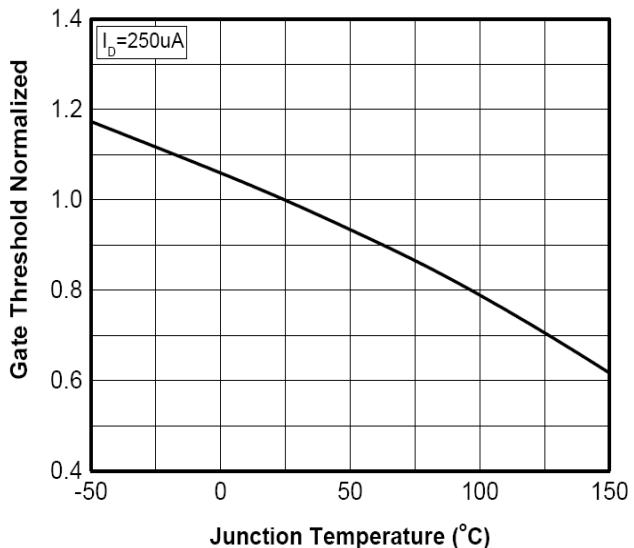


700V Super Junction Power MOSFET

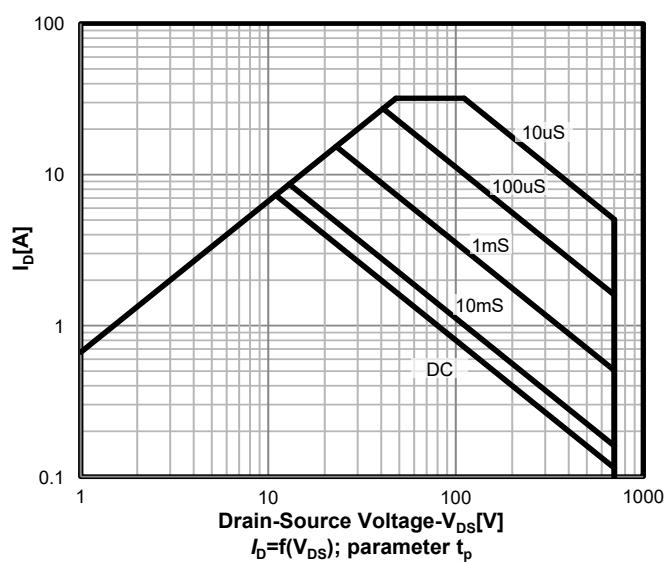
Typ. capacitances



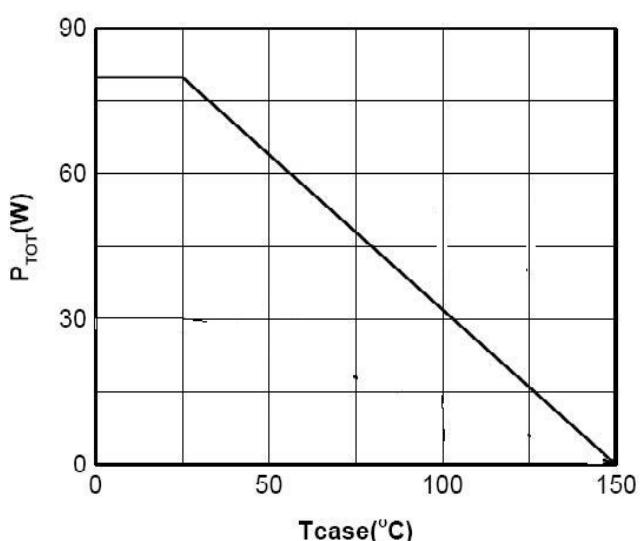
Normalized $V_{GS(th)}$ characteristics



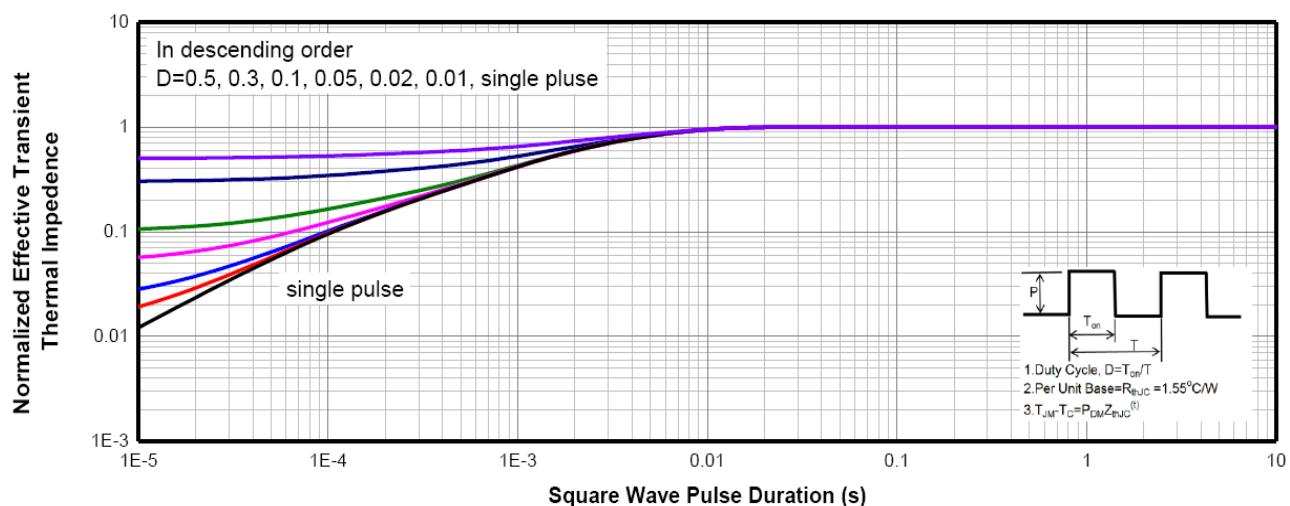
Safe operating area $TC=25\text{ }^{\circ}\text{C}$

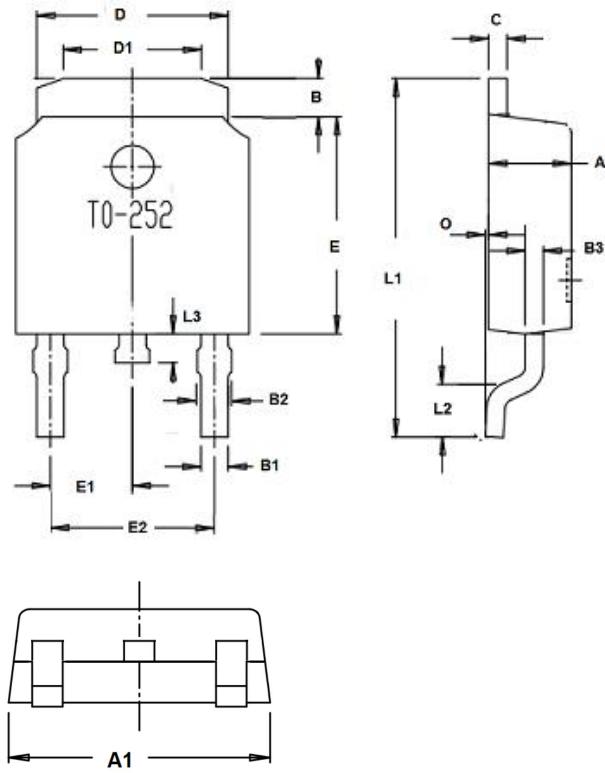


Power dissipation



Max. transient thermal impedance



700V Super Junction Power MOSFET
TO-252 Package Outline Dimensions


Dim.	Min.	Max.
A	2.1	2.5
A1	6.3	6.9
B	0.96	1.42
B1	0.74	0.86
B2	0.74	0.94
C	Typ0.5	
D	5.33	5.53
D1	3.65	4.05
E	6.0	6.2
E1	Typ2.29	
E2	Typ4.58	
O	0	0.15
L1	9.9	10.5
L2	Typ1.65	
L3	0.6	1.0
All Dimensions in millimeter		