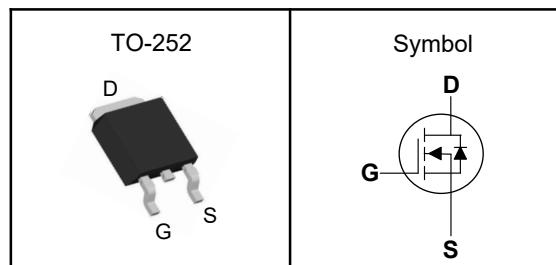


## 700V Super Junction Power MOSFET

### Features

- Low drain-source on-resistance:  $R_{DS(ON)}=0.26\Omega(\text{typ})$
- Easy to control gate switching
- Enhancement mode:  $V_{th} = 2$  to  $4V$
- 100% avalanche tested
- RoHS compliant

### Pin Description



### Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger, Lighting

$V_{DSS}$	700	V
$R_{DS(ON)-\text{Typ}}$	260	$\text{m}\Omega$
$I_D$	14.6	A

### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
$V_{DSS}$	Drain-Source Voltage	700	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	375	mJ
$I_{DM}^{①}$	300 $\mu\text{s}$ Pulse Drain Current Tested	44	A
$I_D$	Continuous Drain Current	14.6	A
$P_D$	Maximum Power Dissipation	120	W
$I_{AS}$	Avalanche Current	5	A
$dv/dt$	MOSFET $dv/dt$ ruggedness, $V_{DS} = 0 \dots 400V$	50	V/ns
	Reverse diode $dv/dt$ <sup>③</sup> $V_{DS}=0 \dots 400V$ , $I_{SD} \leq I_D$	15	

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{③}$	Thermal Resistance Junction-Ambient <sub>1</sub>	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sub>1</sub>	1	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.



FS70R300CD

## 700V Super Junction Power MOSFET

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	700	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=700\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_D=250\mu\text{A}$	2.0	---	4.0	V
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=7.5\text{A}$	---	260	300	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=100\text{V}$ , Freq.=1.0MHz	---	1055	---	pF
$C_{\text{oss}}$	Output Capacitance		---	41	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	2.1	---	
$T_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{GS}}=12\text{V}$ , $V_{\text{DD}}=400\text{V}$ , $I_D=7.5\text{A}$ , $R_G=15\Omega$	---	17	---	nS
$T_r$	Turn-on Rise Time		---	18	---	
$T_{\text{d}(\text{off})}$	Turn-off Delay Time		---	89	---	
$T_f$	Turn-off Fall Time		---	20	---	
$Q_g$	Total Gate Charge	$V_{\text{GS}}=10\text{V}$ , $V_{\text{DD}}=400\text{V}$ , $I_D=7.5\text{A}$	---	25	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	5.2	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	9.3	---	
$R_g$	Gate resistance	f=1 MHz, open drain	---	13	---	$\Omega$
<b>Source-Drain Characteristics</b>						
$I_s$	Continuous Source Current		---	---	14.6	A
ISM	Maximum Pulsed Drain-Source Diode Forward Current		---	---	44	A
$V_{\text{SD}}$	Diode Forward Voltage	$I_s=15\text{A}$ , $V_{\text{GS}}=0\text{V}$	---	---	1.4	V
$t_{\text{rr}}$	Reverse recovery time	$I_s=7.5\text{A}$ , $V_{\text{GS}}=0\text{V}$ $dI/dt=100\text{A}/\mu\text{s}$	---	295	---	ns
$Q_{\text{rr}}$	Reverse recovery charge		---	3.1	---	nC
$I_{\text{rrm}}$	Peak Reverse Recovery Current		---	20.5	---	A

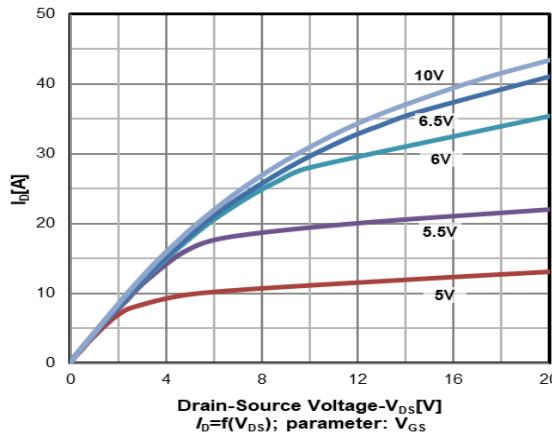
Note ④: Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤: Guaranteed by design, not subject to production testing.

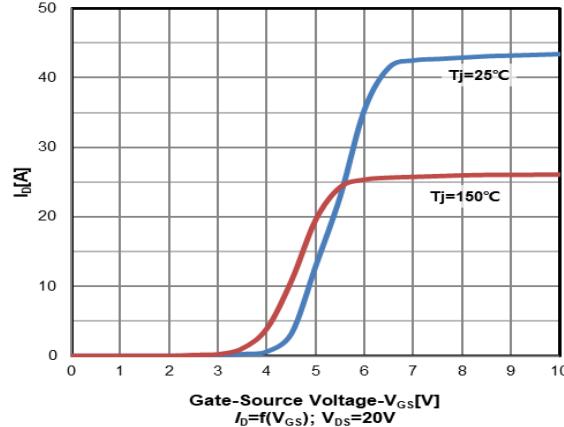
## 700V Super Junction Power MOSFET

### Typical Characteristics

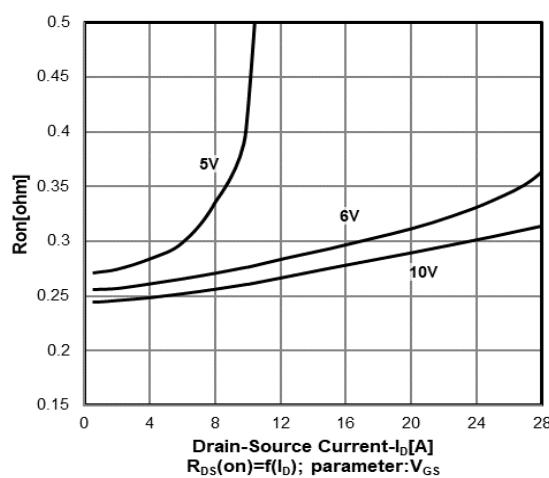
Typ. output characteristics  $T_j=25\text{ }^\circ\text{C}$



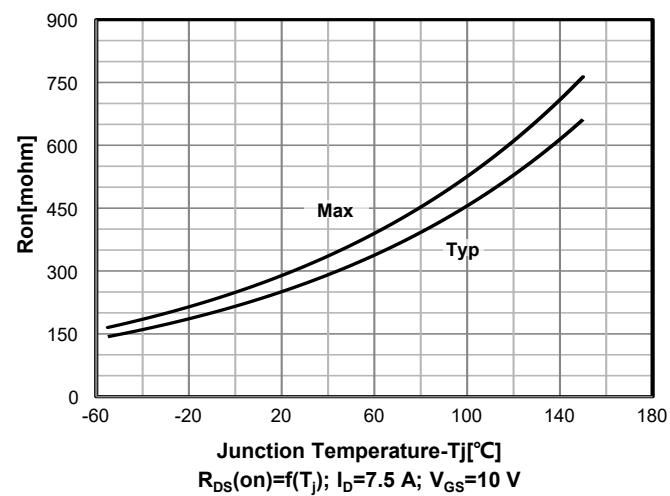
Typ. transfer characteristics



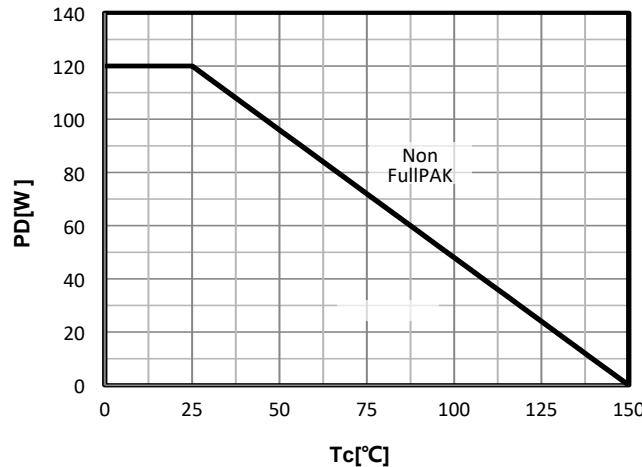
Typ. drain-source on-state resistance



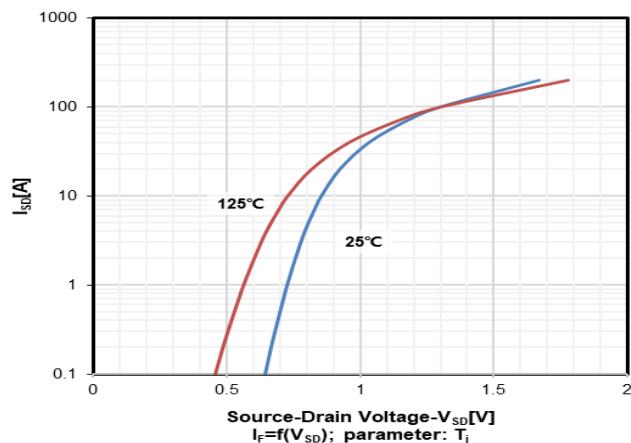
On resistance vs temperature



Power dissipation

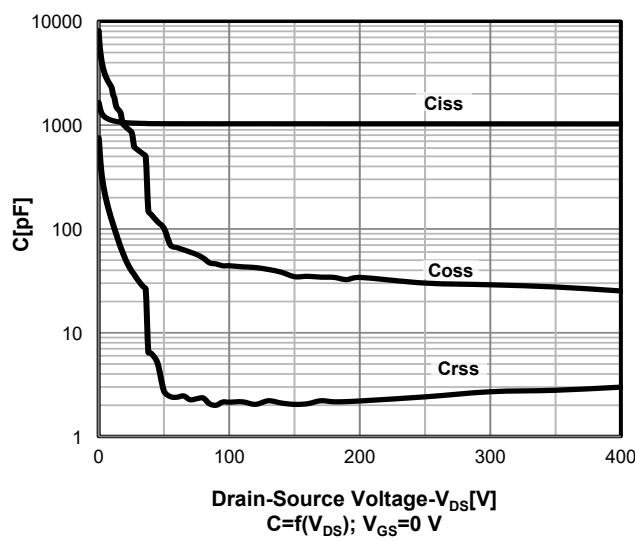


Forward characteristics of reverse diode

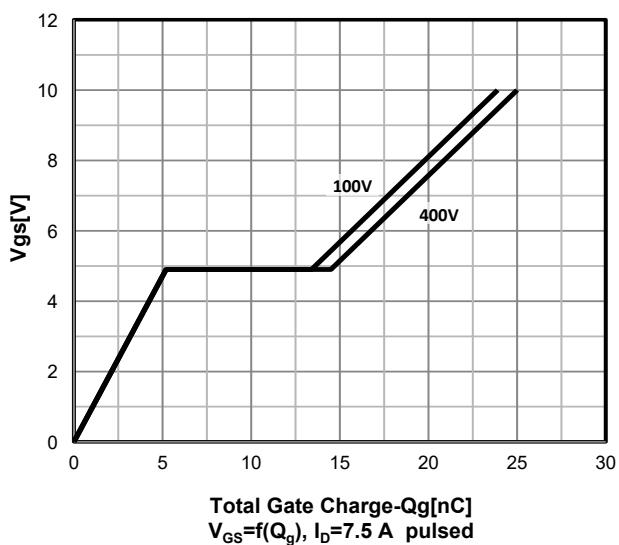


## 700V Super Junction Power MOSFET

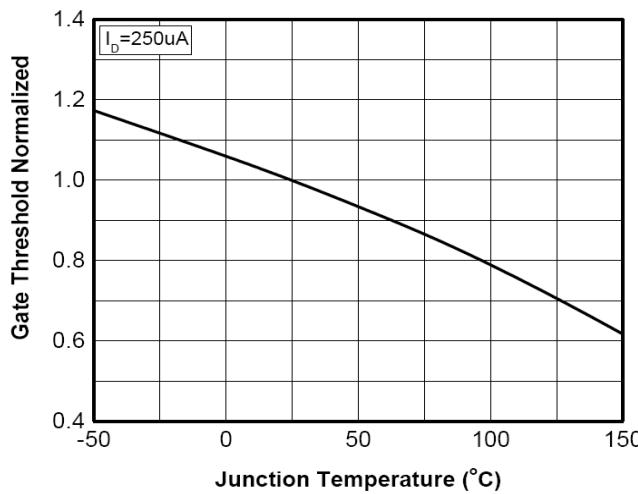
Typ. capacitances



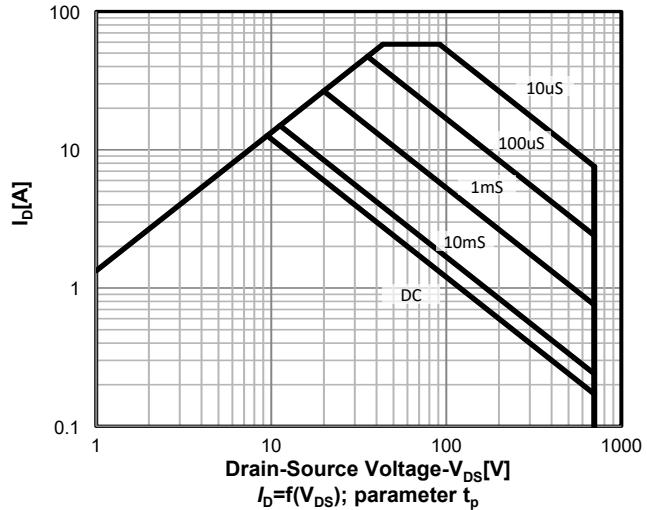
Typ. gate charge characteristics



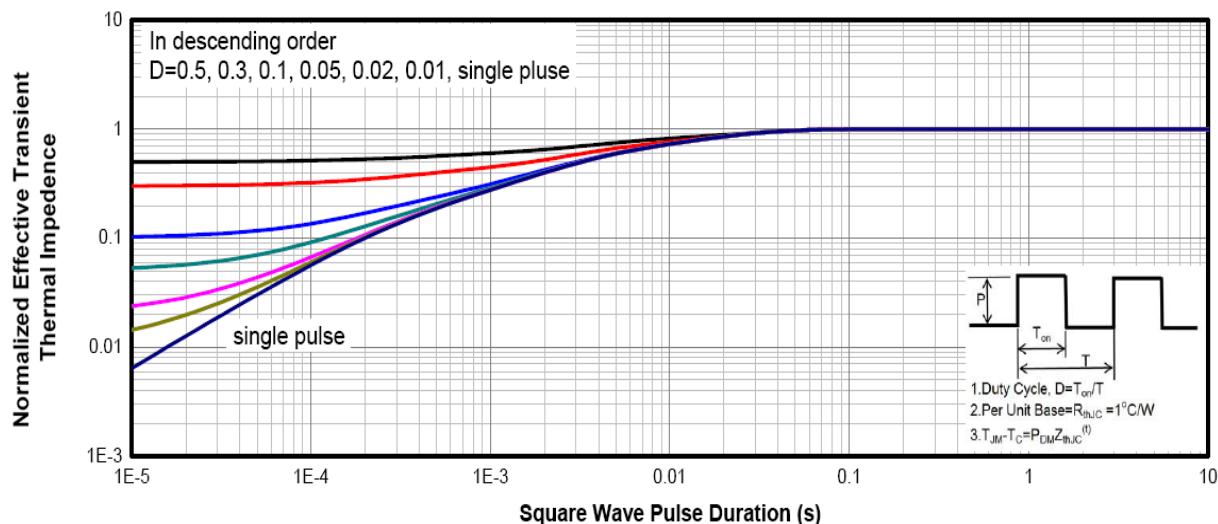
Normalized  $V_{GS(th)}$  characteristics

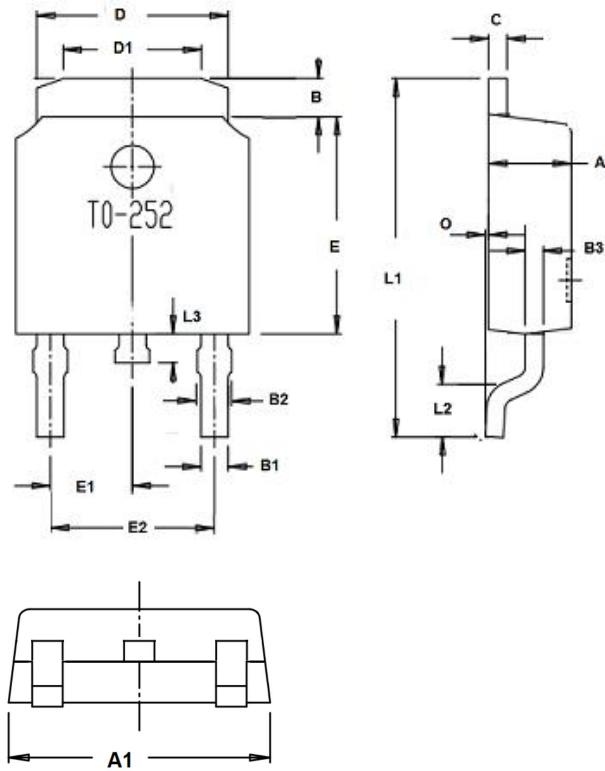


Safe operating area  $TC=25$  °C



Max. transient thermal impedance



**700V Super Junction Power MOSFET**
**TO-252 Package Outline Dimensions**


Dim.	Min.	Max.
A	2.1	2.5
A1	6.3	6.9
B	0.96	1.42
B1	0.74	0.86
B2	0.74	0.94
C	Typ0.5	
D	5.33	5.53
D1	3.65	4.05
E	6.0	6.2
E1	Typ2.29	
E2	Typ4.58	
O	0	0.15
L1	9.9	10.5
L2	Typ1.65	
L3	0.6	1.0
All Dimensions in millimeter		