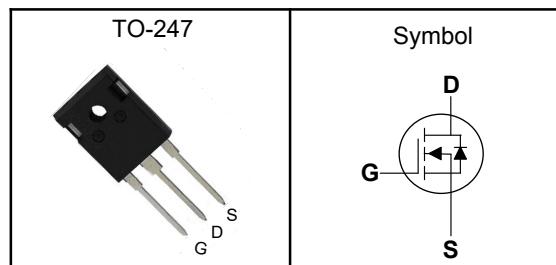


650V Super Junction Power MOSFET

Features

- Low drain-source on-resistance: $R_{DS(ON)}=0.12\Omega(\text{typ})$
- Easy to control gate switching
- Enhancement mode: $V_{th} = 2.5 \text{ to } 4.5\text{V}$
- 100% avalanche tested
- RoHS compliant

Pin Description



Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger, Lighting

V_{DSS}	650	V
$R_{DS(ON)-\text{Typ}}$	120	$\text{m}\Omega$
I_D	24	A

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	650	V
V_{GSS}	Gate-Source Voltage	± 30	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
E_{AS}	Single Pulse Avalanche Energy ^③	455	mJ
$I_{DM}^{①}$	300 μs Pulse Drain Current Tested	72	A
I_D	Continuous Drain Current	24	A
P_D	Maximum Power Dissipation	192	W
I_{AS}	Avalanche Current	3.9	A
dv/dt	MOSFET dv/dt ruggedness, $V_{DS} = 0 \dots 400\text{V}$	50	V/ns
	Reverse diode dv/dt ^③ $V_{DS}=0 \dots 400\text{V}$, $I_{SD} \leq I_D$	100	

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{③}$	Thermal Resistance Junction-Ambient ₁ (Max)	40	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁ (Max)	0.65	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

650V Super Junction Power MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	650	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=600\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	10	μA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	2.5	---	4.5	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=12\text{A}$	---	120	135	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=100\text{V}$, Freq.=1MHz	---	2072	---	pF
C_{oss}	Output Capacitance		---	101	---	
C_{rss}	Reverse Transfer Capacitance		---	1.3	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=400\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=4.7\Omega$, $I_D=12\text{A}$	---	21	---	nS
T_r	Turn-on Rise Time		---	40	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	56	---	
T_f	Turn-off Fall Time		---	14	---	
R_g	Gate Resistance	$f = 1.0\text{MHz}$, open drain	---	1.9	---	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=400\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=12\text{A}$	---	46	---	nC
Q_{gs}	Gate-Source Charge		---	8	---	
Q_{gd}	Gate-Drain Charge		---	15	---	
Source-Drain Characteristics ($T_J=25^\circ\text{C}$)						
$V_{\text{SD}}^{④}$	Diode Forward Voltage	$I_S=12\text{A}$, $V_{\text{GS}}=0\text{V}$	---	---	1.4	V
t_{rr}	Reverse Recovery Time	$V_R=400\text{V}$, $I_F=12\text{A}$, $dI/dt=130\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	110	---	nS
Q_{rr}	Reverse Recovery Charge		---	0.8	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

650V Super Junction Power MOSFET

Typical Characteristics

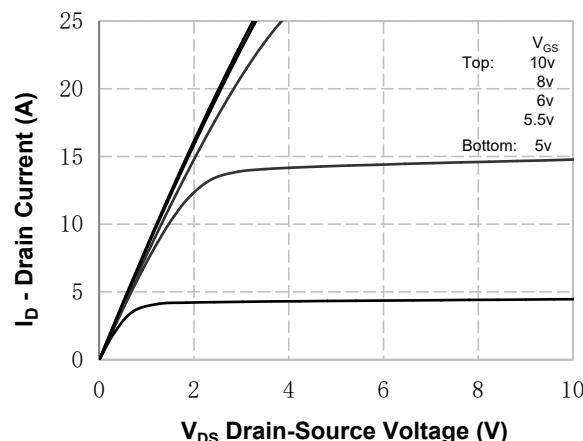


Figure 1. On-Region Characteristics

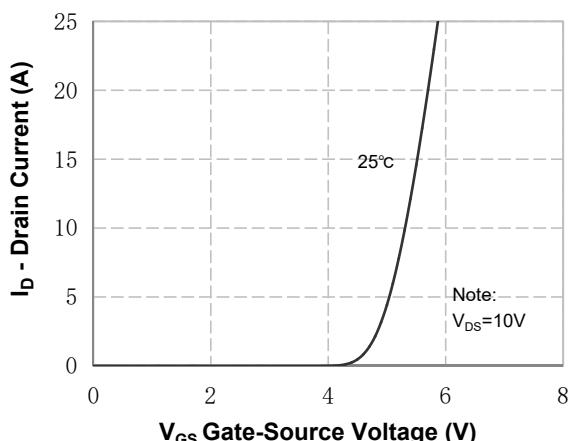


Figure 2. Transfer Characteristics

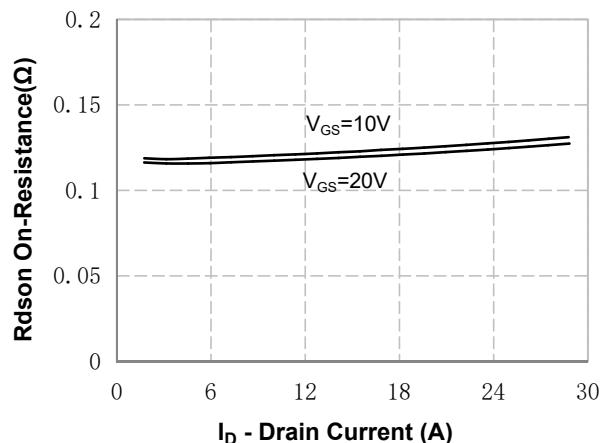


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

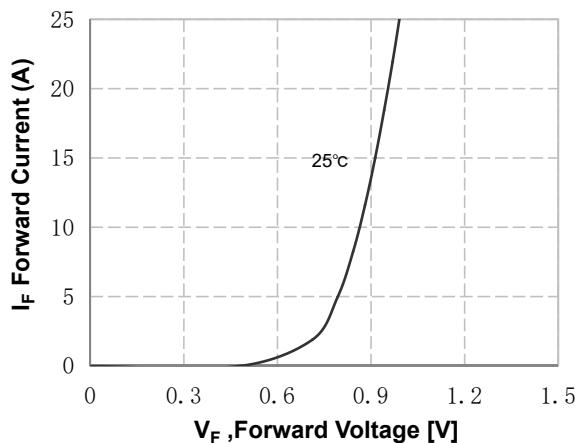


Figure 4. Body Diode Forward Voltage Variation with Source Current

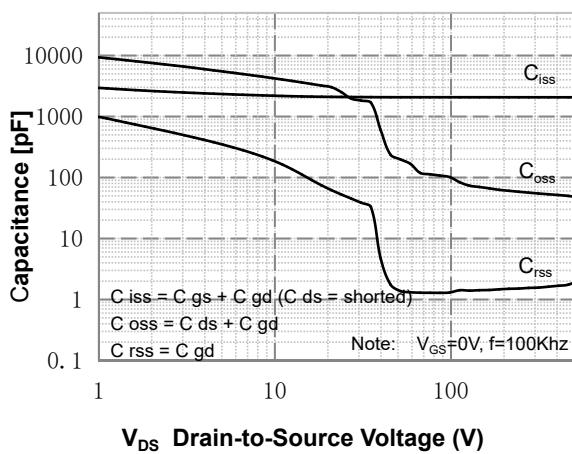


Figure 5. Capacitance Characteristics

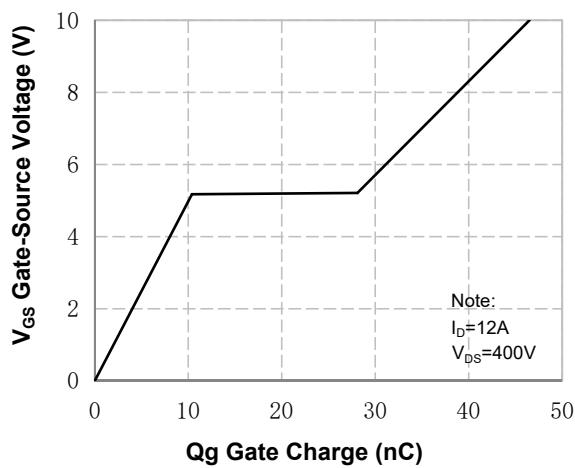


Figure 6. Gate Charge Characteristics

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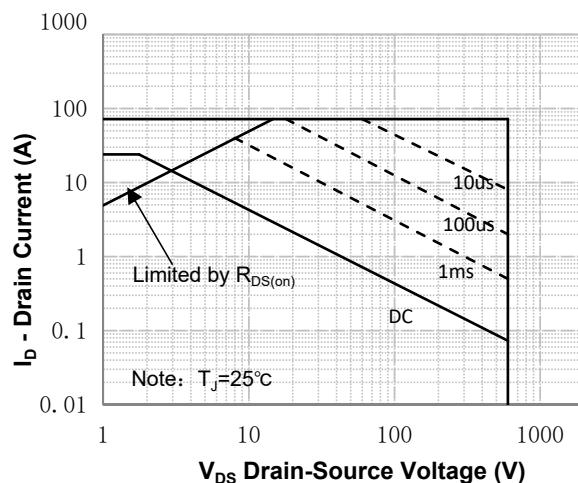


Figure 7. Maximum Safe Operating Area

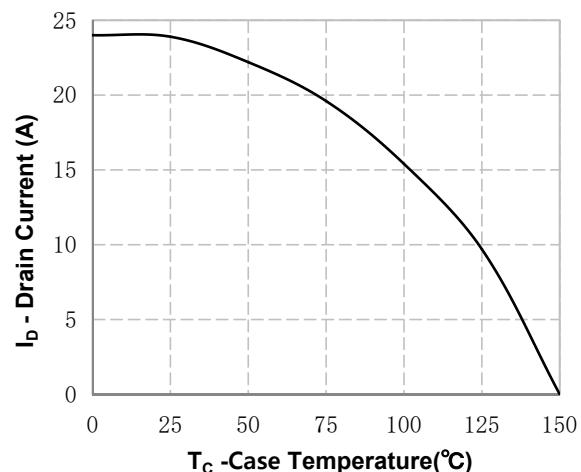


Figure 8. Maximum Drain Current vs Case Temperature

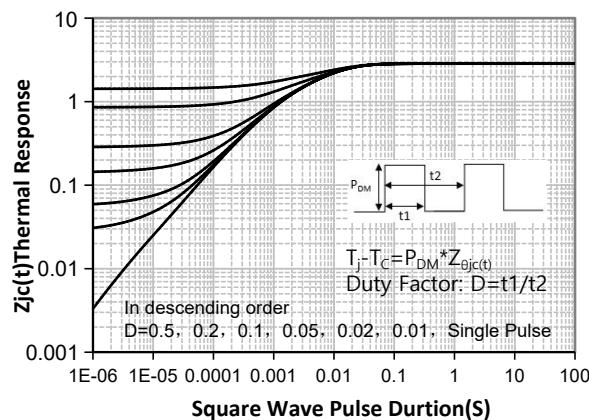
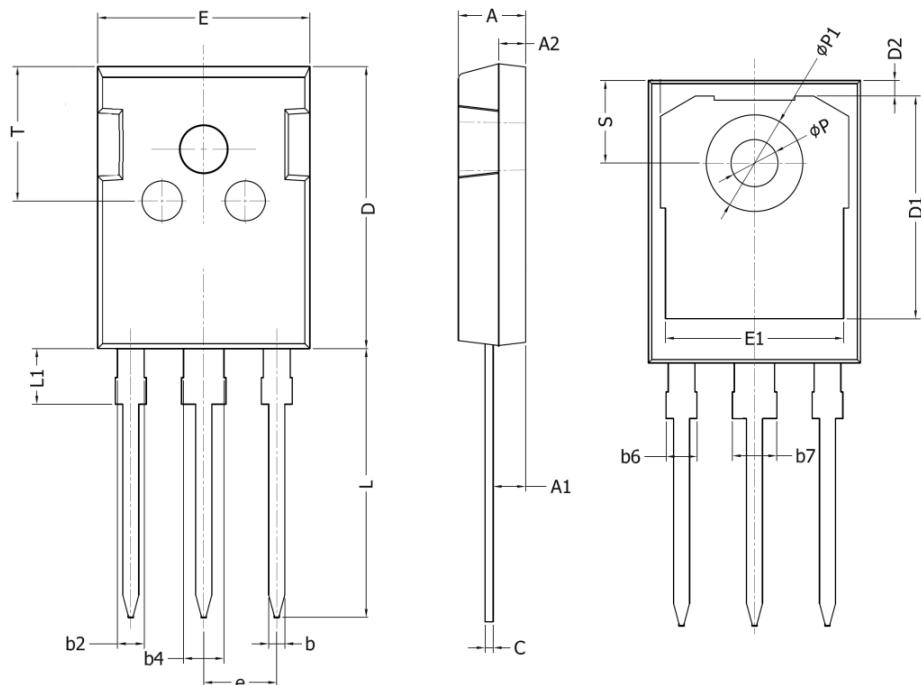


Figure 9. Transient Thermal Response Curve

650V Super Junction Power MOSFET

TO-247 Package Outline Dimensions



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.90	5.20
A1	2.31	2.51
A2	1.9	2.1
b	1.16	1.26
b2	1.96	2.06
b4	2.96	3.06
b6	-	2.25
b7	-	3.25
C	0.59	0.66
D	20.90	21.20
D1	16.25	16.85
D2	1.05	1.35
E	15.75	16.10
E1	13.00	13.60
e	5.436 BSC	
L	19.80	20.20
L1	-	4.30
P	3.40	3.60
P1	7.00	7.40
S	6.05	6.25
T	9.80	10.20