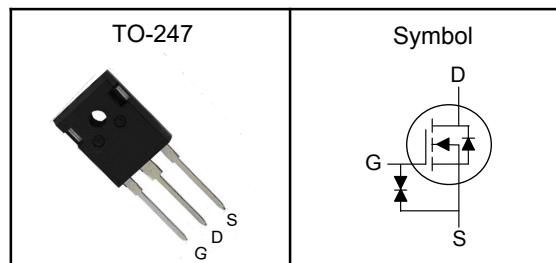


## 650V Super Junction Power MOSFET

### Features

- Low drain-source on-resistance:  $R_{DS(ON)}=0.056\Omega(\text{typ})$
- Very Low FOM ( $R_{DS(ON)} \times Q_g$ )
- Extremely low switching loss
- 100% avalanche tested
- RoHS compliant

### Pin Description



### Applications

- Switch Mode Power Supply (SMPS)
- TV power & LED Lighting Power
- AC to DC Converters
- Telecom

$V_{DSS}$	650	V
$R_{DS(ON)-\text{Typ}}$	56	$\text{m}\Omega$
$I_D$	47	A

### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
$V_{DSS}$	Drain-Source Voltage	650	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	1567	mJ
$I_{DM}^{①}$	300 $\mu\text{s}$ Pulse Drain Current Tested	188	A
$I_D$	Continuous Drain Current	47	A
$P_D$	Maximum Power Dissipation	156	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>①</sup>	40	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>①</sup>	0.8	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

## 650V Super Junction Power MOSFET

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_D=1\text{mA}$	650	---	---	V
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=520\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}$ , $\text{I}_D=250\mu\text{A}$	3.0	---	5.0	V
$\text{I}_{\text{GSS}}$	Gate Leakage Current	$\text{V}_{\text{GS}}=\pm 24\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 10$	$\mu\text{A}$
$\text{R}_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=20\text{A}$	---	56	65	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{V}_{\text{DS}}=350\text{V}$ , Freq.=1MHz	---	6200	---	pF
$\text{C}_{\text{oss}}$	Output Capacitance		---	128	---	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	4	---	
$\text{T}_{\text{d(on)}}$	Turn-on Delay Time	$\text{V}_{\text{DS}}=350\text{V}$ , $\text{R}_G=4.7\Omega$ , $\text{I}_D=20\text{A}$	---	83	---	nS
$\text{T}_r$	Turn-on Rise Time		---	38	---	
$\text{T}_{\text{d(off)}}$	Turn-off Delay Time		---	63	---	
$\text{T}_f$	Turn-off Fall Time		---	19	---	
$\text{Q}_g$	Total Gate Charge	$\text{V}_{\text{DS}}=350\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=20\text{A}$	---	104	---	nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	34	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	37	---	
<b>Source-Drain Characteristics</b> ( $T_J=25^\circ\text{C}$ )						
$\text{V}_{\text{SD}}^{④}$	Diode Forward Voltage	$\text{I}_S=1\text{A}$ , $\text{V}_{\text{GS}}=0\text{V}$	---	---	1.3	V
$\text{t}_{\text{rr}}$	Reverse Recovery Time	$\text{I}_S=20\text{A}$ , $\text{di}/\text{dt}=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	177.5	---	nS
$\text{Q}_{\text{rr}}$	Reverse Recovery Charge		---	1.6	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu\text{s}$ , duty cycle $\leq 2\%$ ).

Note ⑤ : Guaranteed by design, not subject to production testing.

## 650V Super Junction Power MOSFET

### Product Features

Fig. 1 Typical Output Characteristics

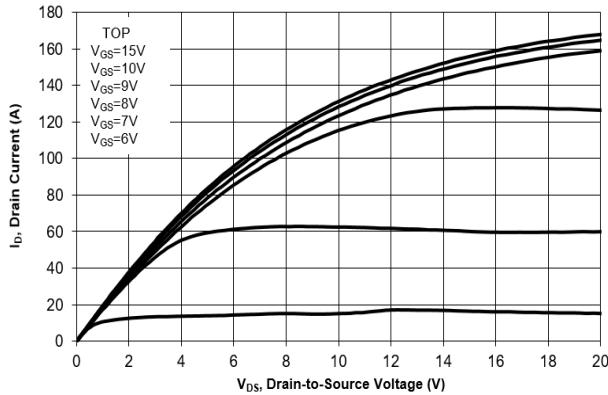


Fig. 2 Typical Transfer Characteristics

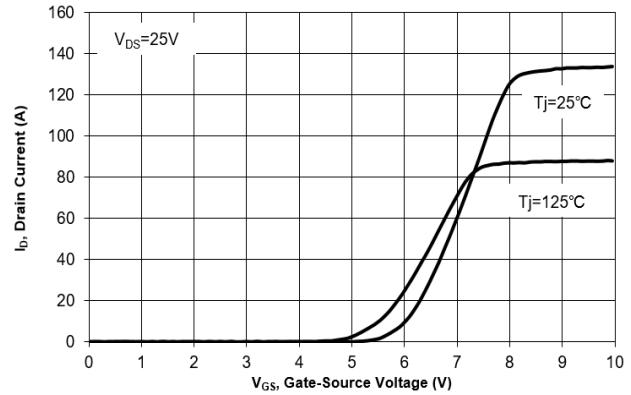


Fig. 3 On-Resistance vs. Drain Current

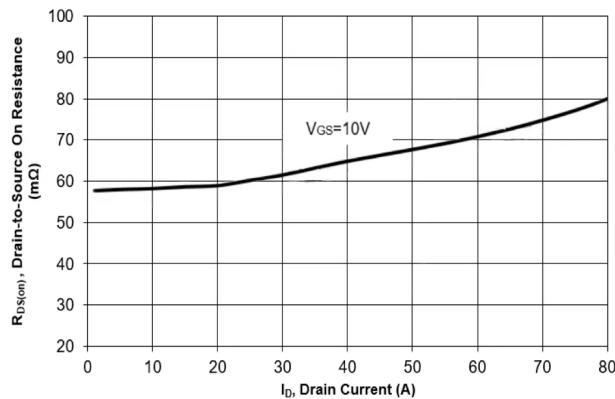


Fig. 4 On-Resistance vs. Gate to Source Voltage

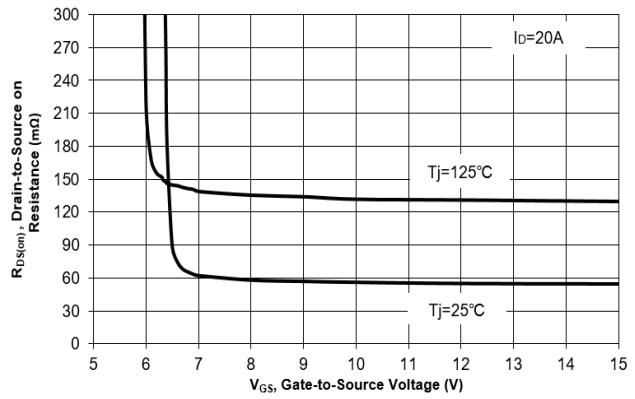


Fig. 5 On-Resistance vs.  $T_j$

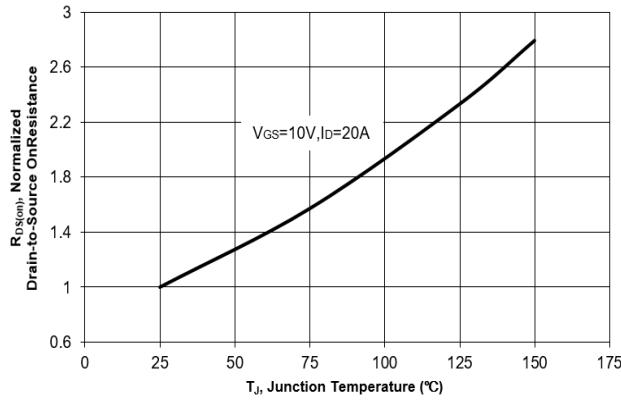
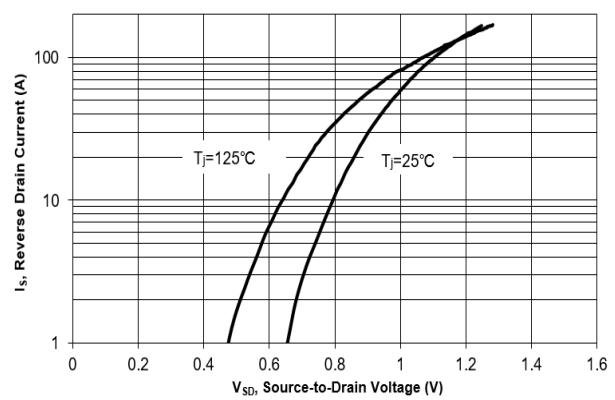


Fig. 6 Typical Body-Diode Forward Characteristics



## 650V Super Junction Power MOSFET

Fig. 7 Typical Junction Capacitance

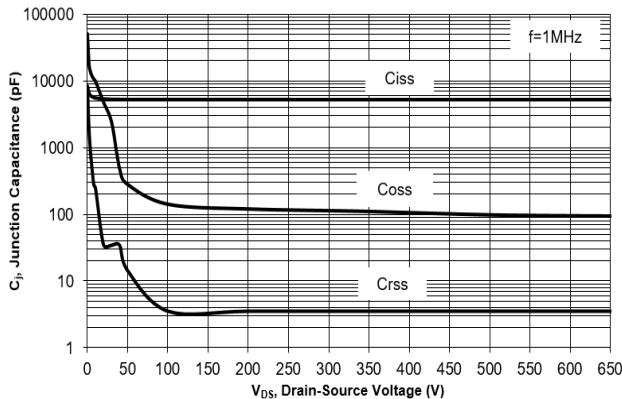


Fig. 8 Drain-Source Leakage Current vs.  $T_J$

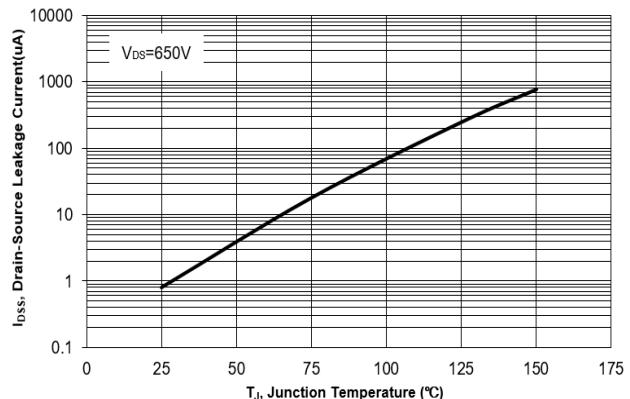


Fig. 9  $V_{(BR)DSS}$  vs. Junction Temperature

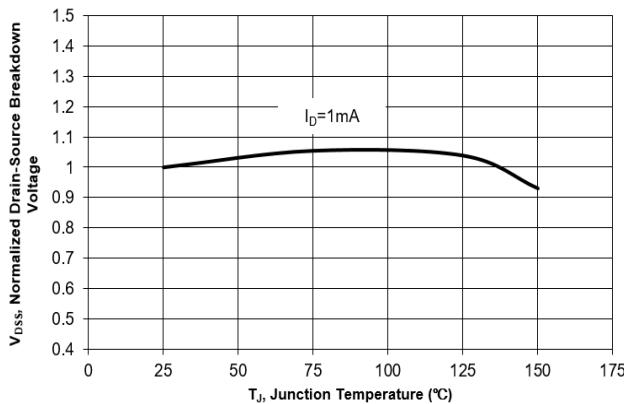


Fig. 10 Gate Threshold Variation vs.  $T_J$

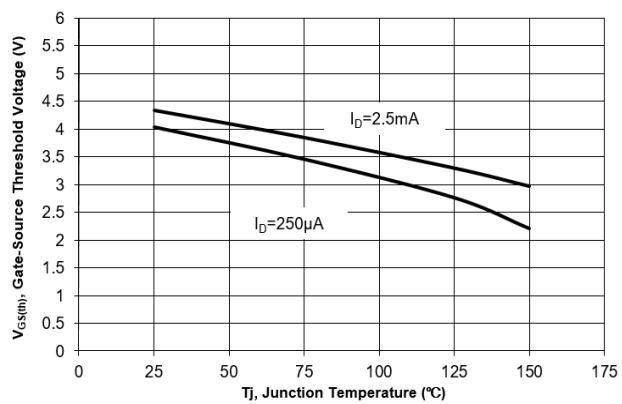


Fig. 11 Gate Charge

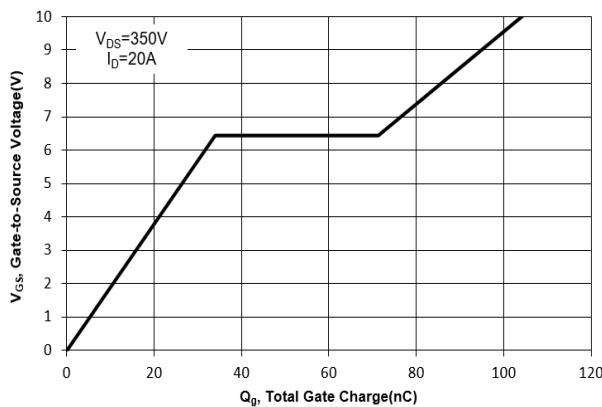
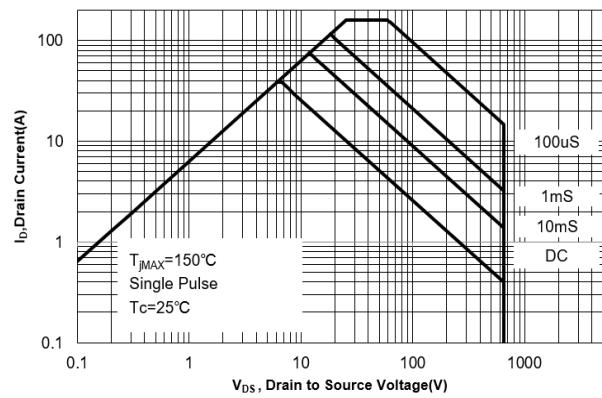
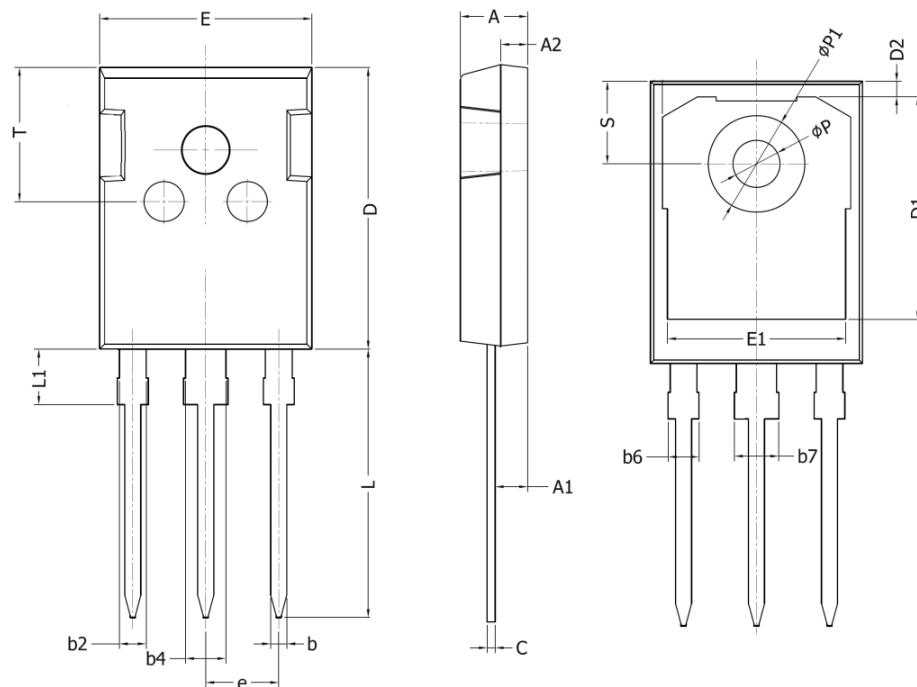


Fig. 12 Safe Operation Area



## 650V Super Junction Power MOSFET

### TO-247 Package Outline Dimensions



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.90	5.20
A1	2.31	2.51
A2	1.9	2.1
b	1.16	1.26
b2	1.96	2.06
b4	2.96	3.06
b6	-	2.25
b7	-	3.25
C	0.59	0.66
D	20.90	21.20
D1	16.25	16.85
D2	1.05	1.35
E	15.75	16.10
E1	13.00	13.60
e	5.436 BSC	
L	19.80	20.20
L1	-	4.30
P	3.40	3.60
P1	7.00	7.40
S	6.05	6.25
T	9.80	10.20