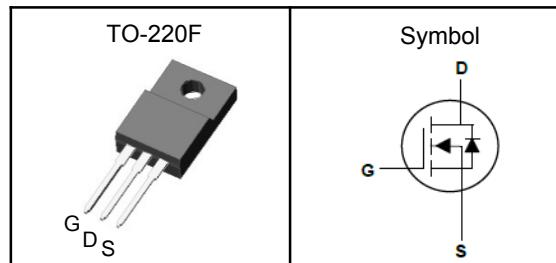


## N-Channel Enhancement Mode MOSFET

### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

$V_{DSS}$	900	V
$R_{DS(ON)-Typ}$	3600	$\text{m}\Omega$
$I_D$	4	A

### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
$V_{DSS}$	Drain-Source Voltage	900	V	
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V	
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$	
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	650	mJ	
$I_{DM}^{①}$	Pulse Drain Current Tested	16	A	
$I_D$	Continuous Drain Current	$T_c=25^\circ\text{C}$	4	A
$P_D$	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	30	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{θJA}$	Thermal Resistance Junction-Ambient <sup>①</sup>	100	$^\circ\text{C}/\text{W}$
$R_{θJC}$	Thermal Resistance Junction-Case <sup>①</sup>	4.17	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

**N-Channel Enhancement Mode MOSFET**
**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	900	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=900\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_D=250\mu\text{A}$	2.0	---	4.0	V
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=2\text{A}$	---	3600	4800	$\text{m}\Omega$
<b>Dynamic Characteristics</b> <sup>⑤</sup>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=25\text{V}$ , Freq.=1MHz	---	490	---	pF
$C_{\text{oss}}$	Output Capacitance		---	50	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	25	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=400\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=12\Omega$ , $I_D=4\text{A}$	---	10	---	nS
$T_r$	Turn-on Rise Time		---	10	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	30	---	
$T_f$	Turn-off Fall Time		---	15	---	
$Q_g$	Total Gate Charge	$V_{\text{DD}}=400\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=4\text{A}$	---	16	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	3	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	6	---	
<b>Source-Drain Characteristics</b> ( $T_J=25^\circ\text{C}$ )						
$V_{\text{SD}}$	Diode Forward Voltage <sup>②</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=4\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.5	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_F=4\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	135	---	nS
$Q_{\text{rr}}$	Reverse Recovery Charge		---	446	---	nC

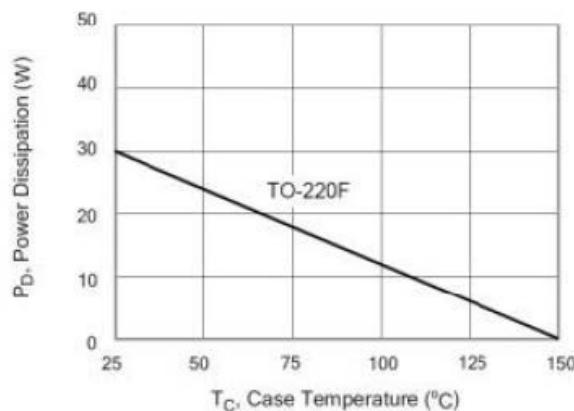
Note ④ : Pulse test (pulse width $\leq 300\mu\text{s}$ , duty cycle $\leq 2\%$ ).

Note ⑤ : Guaranteed by design, not subject to production testing.

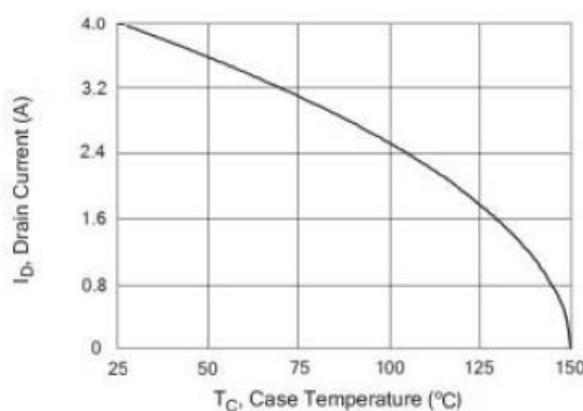
## N-Channel Enhancement Mode MOSFET

### Typical Characteristics

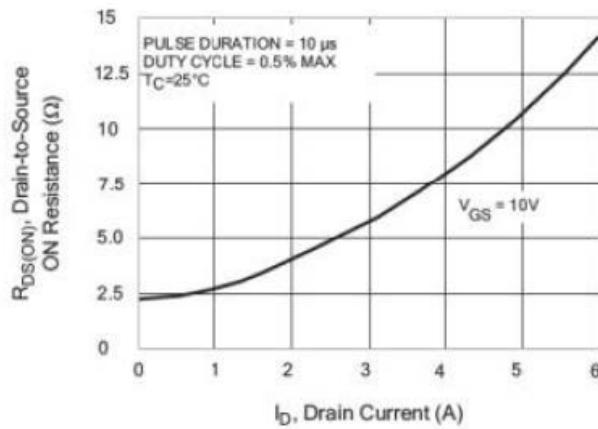
**Figure 1.** Maximum Power Dissipation vs Case Temperature



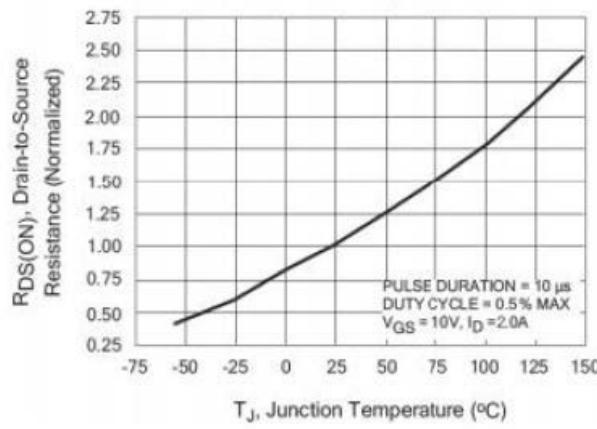
**Figure 2.** Maximum Continuous Drain Current vs Case Temperature



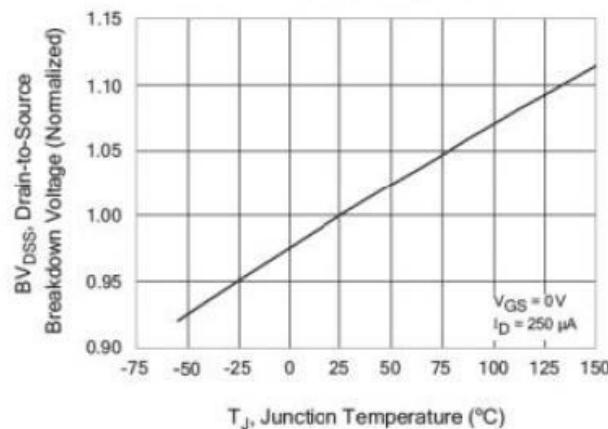
**Figure 3.** Typical Drain-to-Source ON Resistance vs Drain Current



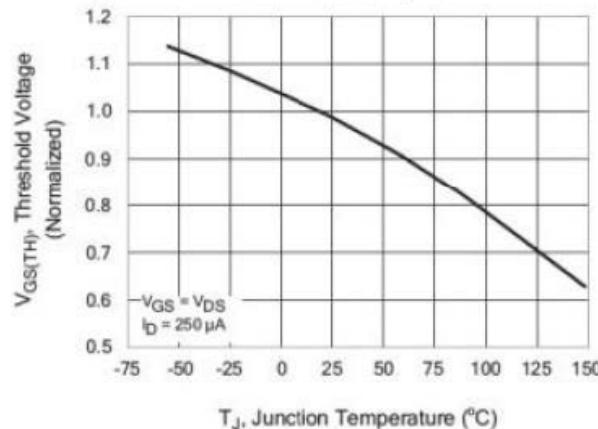
**Figure 4.** Typical Drain-to-Source ON Resistance vs Junction Temperature



**Figure 5.** Typical Breakdown Voltage vs Junction Temperature

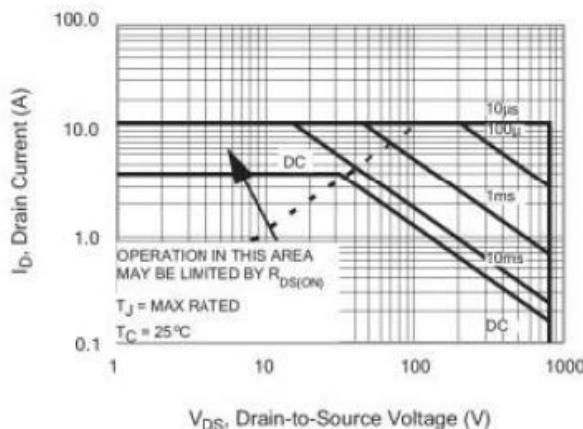


**Figure 6.** Typical Threshold Voltage vs Junction Temperature

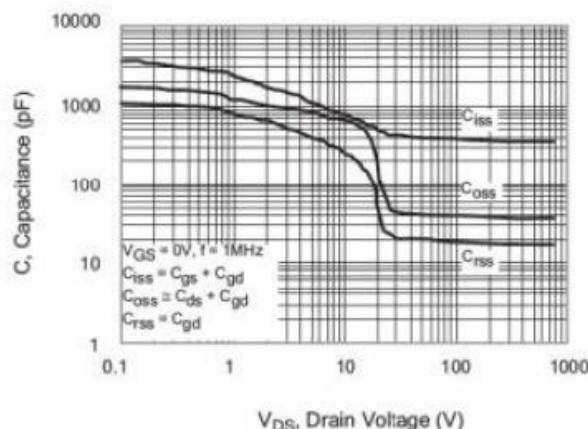


## N-Channel Enhancement Mode MOSFET

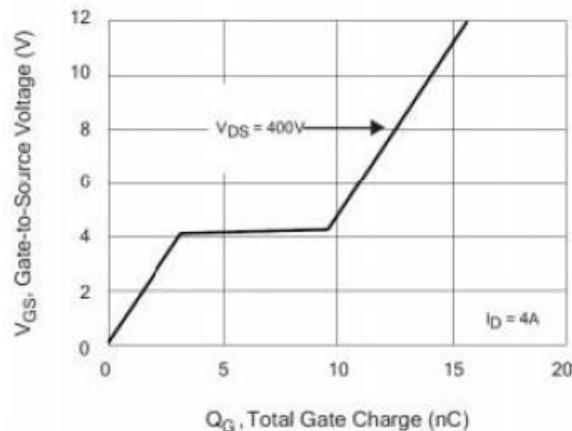
**Figure 7.** Maximum Forward Bias Safe Operating Area



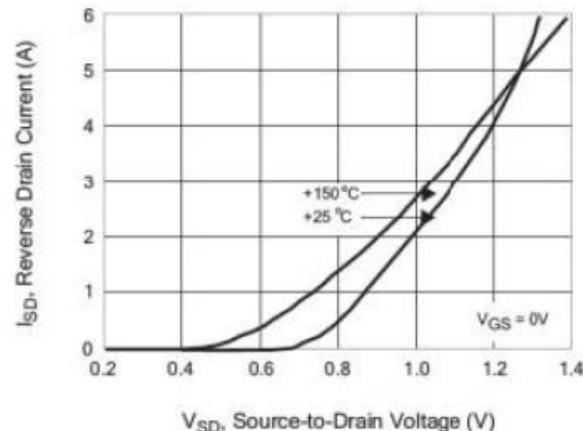
**Figure 8.** Typical Capacitance vs Drain-to-Source Voltage



**Figure 9.** Typical Gate Charge vs Gate-to-Source Voltage

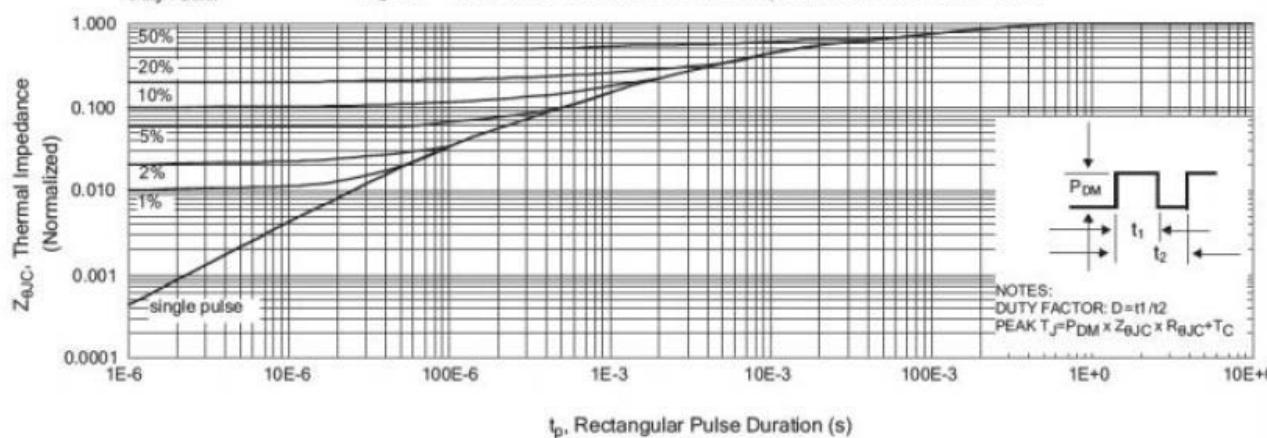


**Figure 10.** Typical Body Diode Transfer Characteristics



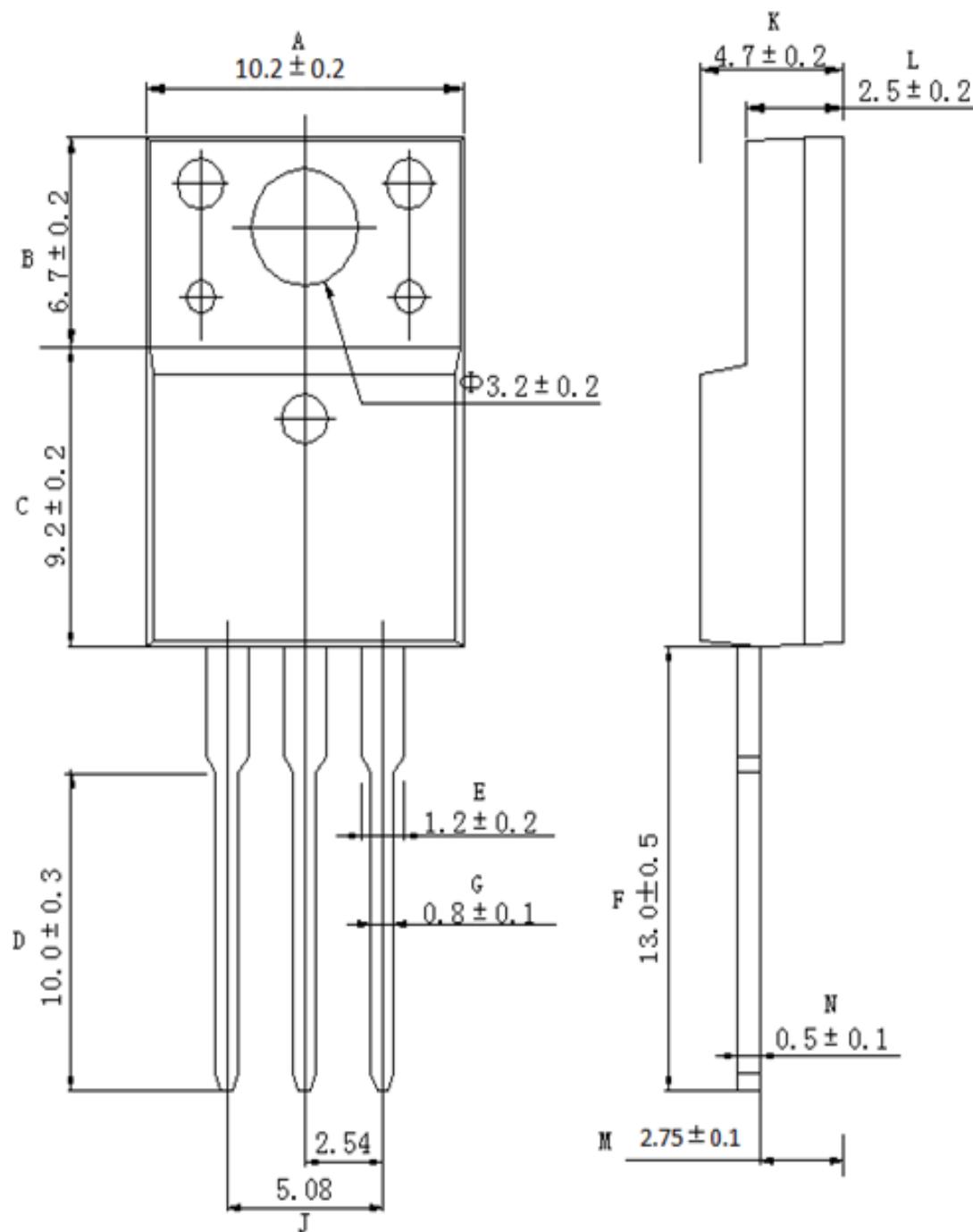
Duty Factor

**Figure 11. Maximum Effective Thermal Impedance, Junction-to-Case**



## N-Channel Enhancement Mode MOSFET

## TO-220F Package Outline Data





FS4N90UF

N-Channel Enhancement Mode MOSFET

## 印字说明

### 印字说明

FS4N90UF

AABBCC

第一行标记为物料型号代码

第二行为AA为内部识别码，BB为表示年份，例如22即表示2022年，CC表示周期，例如01即表示第一周；  
2201即表示2022年第一周生产。