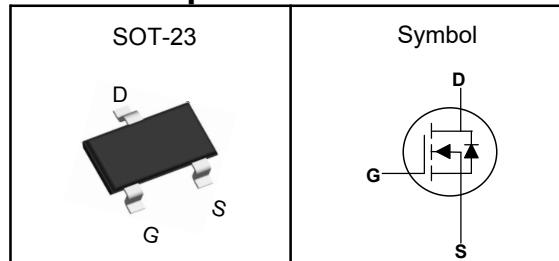


## N-Channel Enhancement Mode MOSFET

### Features

- Low  $R_{dson}$  for low conduction loss
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

$V_{DSS}$	100	V
$R_{DS(ON)-Typ}$	300	$\text{m}\Omega$
$I_D$	1	A

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	4	A
$I_D$	Continuous Drain Current	1	A
$P_D$	Maximum Power Dissipation	1.4	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	125	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

## N-Channel Enhancement Mode MOSFET

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	100	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=100\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_D=250\mu\text{A}$	1.0	---	2.9	V
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=1\text{A}$	---	300	380	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=0.8\text{A}$	---	350	450	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=50\text{V}$ , Freq.=1MHz	---	100	---	pF
$C_{\text{oss}}$	Output Capacitance		---	13	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	5	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=50\text{V}$ , $R_G=3\Omega$ , $V_{\text{GS}}=10\text{V}$ , $R_L=50\Omega$	---	5	---	nS
$T_r$	Turn-on Rise Time		---	4	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	12	---	
$T_f$	Turn-off Fall Time		---	5	---	
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=1\text{A}$	---	3	---	S
$Q_g$	Total Gate Charge	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=1\text{A}$	---	2.8	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	0.4	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	0.8	---	
<b>Source-Drain Characteristics</b> ( $T_J=25^\circ\text{C}$ )						
$V_{\text{SD}}^{④}$	Diode Forward Voltage	$I_S=1\text{A}$ , $V_{\text{GS}}=0\text{V}$	---	---	1.2	V

Note ④ : Pulse test (pulse width $\leq 300\text{us}$ , duty cycle $\leq 2\%$ ).

Note ⑤ : Guaranteed by design, not subject to production testing.

## N-Channel Enhancement Mode MOSFET

### Typical Characteristics

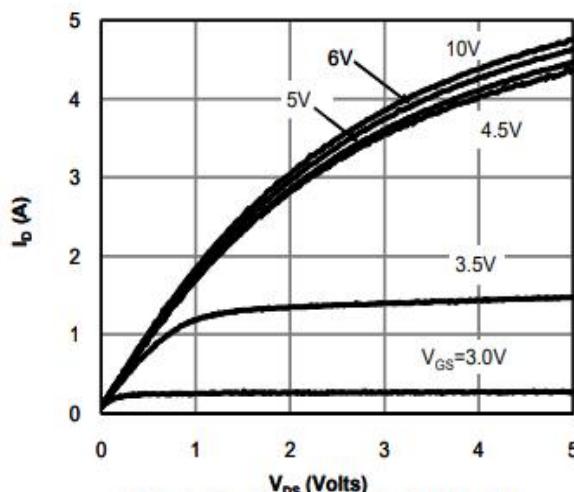


Fig 1: On-Region Characteristics (Note E)

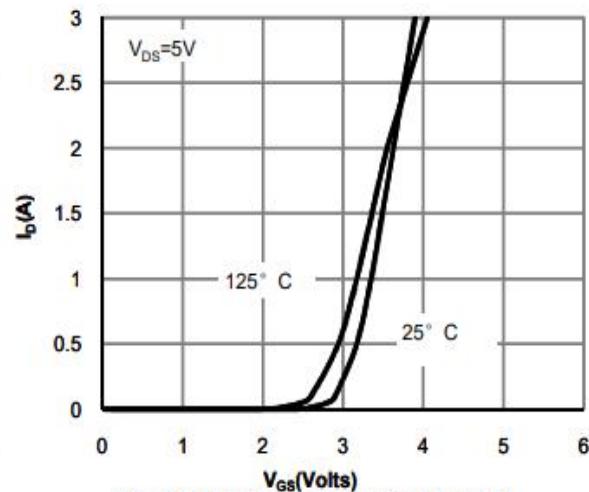


Figure 2: Transfer Characteristics (Note E)

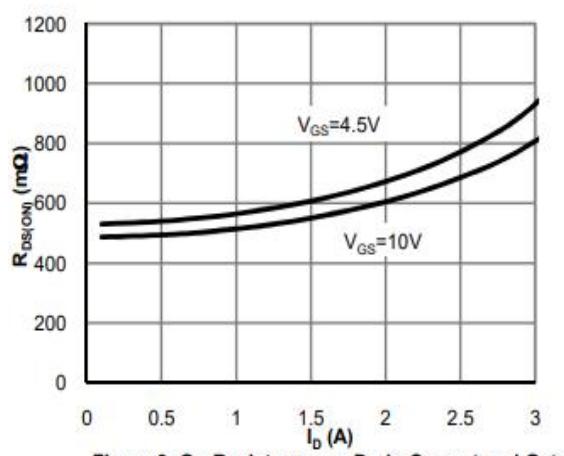


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

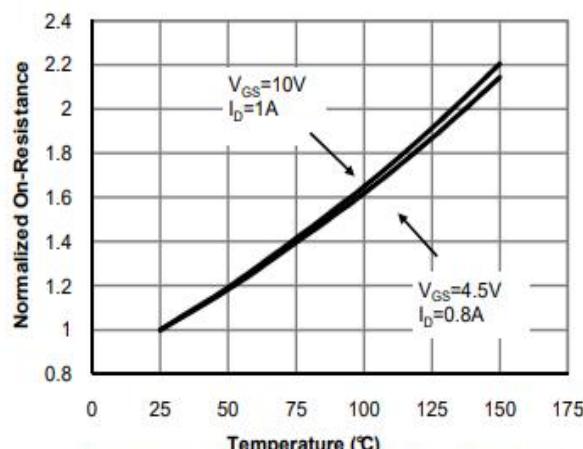


Figure 4: On-Resistance vs. Junction Temperature (Note E)

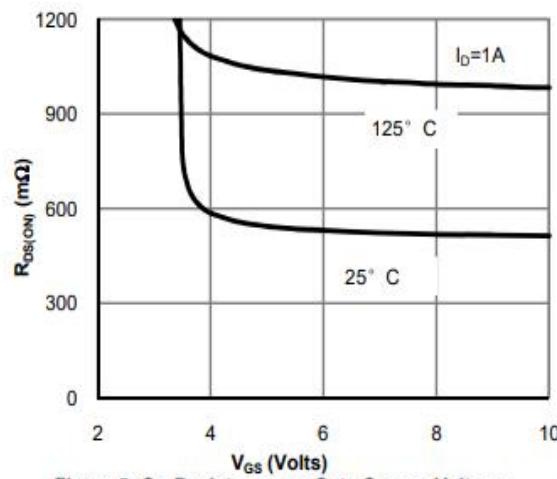


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

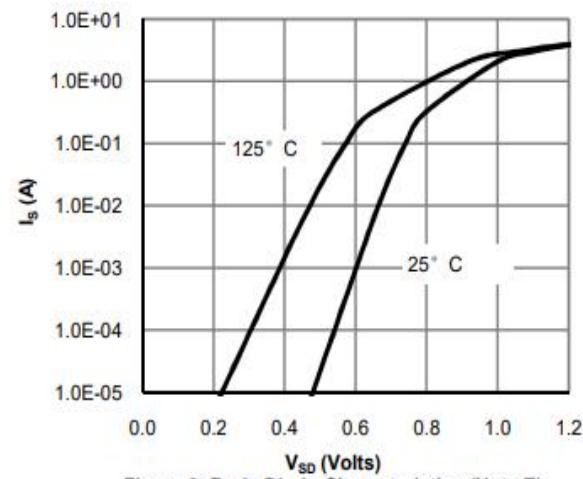


Figure 6: Body-Diode Characteristics (Note E)

## N-Channel Enhancement Mode MOSFET

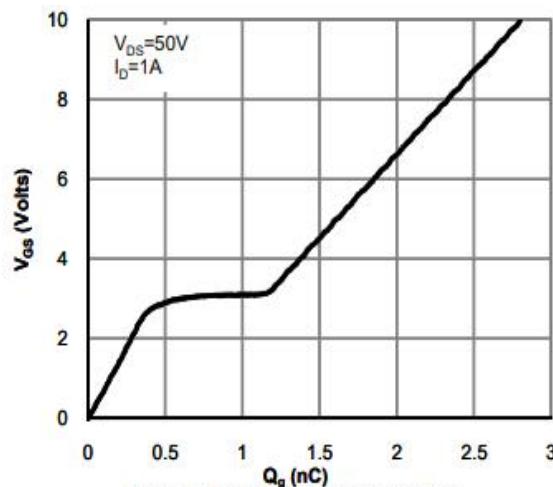


Figure 7: Gate-Charge Characteristics

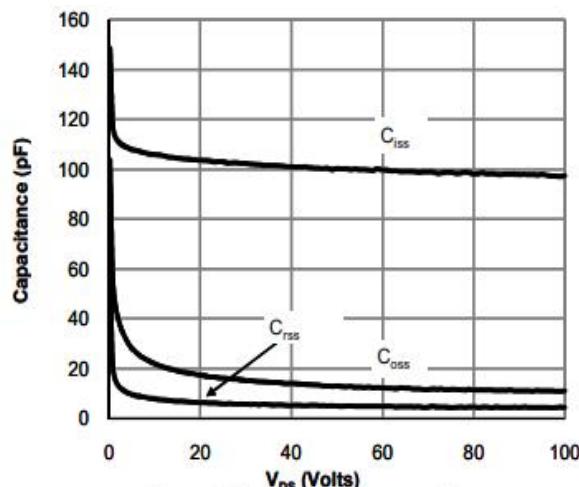


Figure 8: Capacitance Characteristics

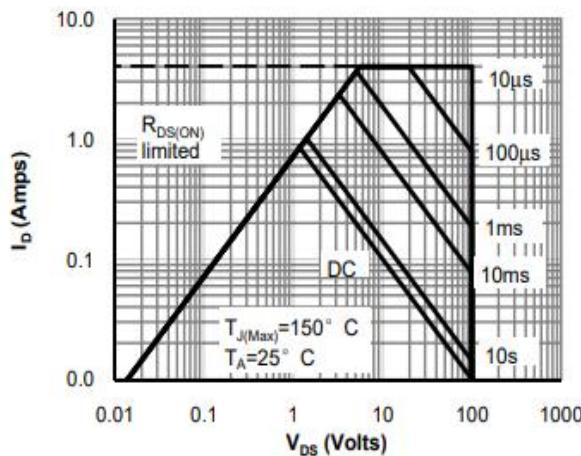


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

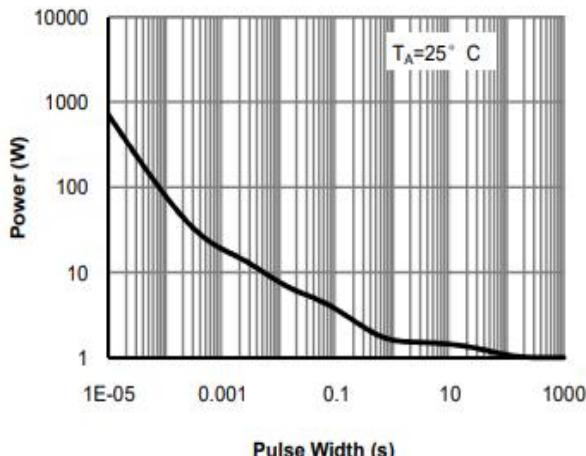


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

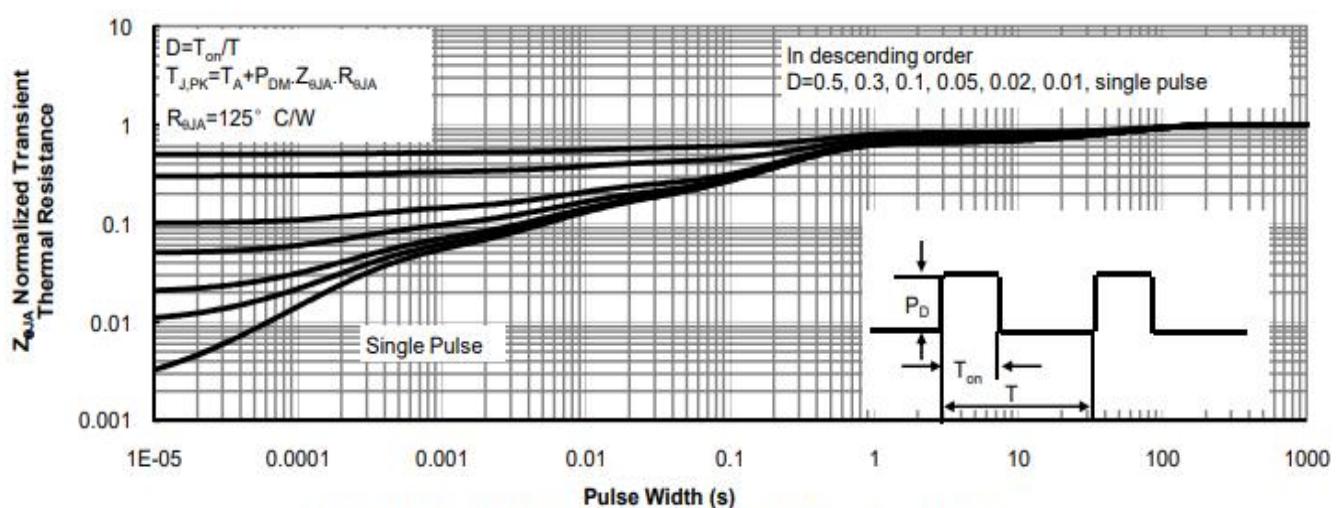
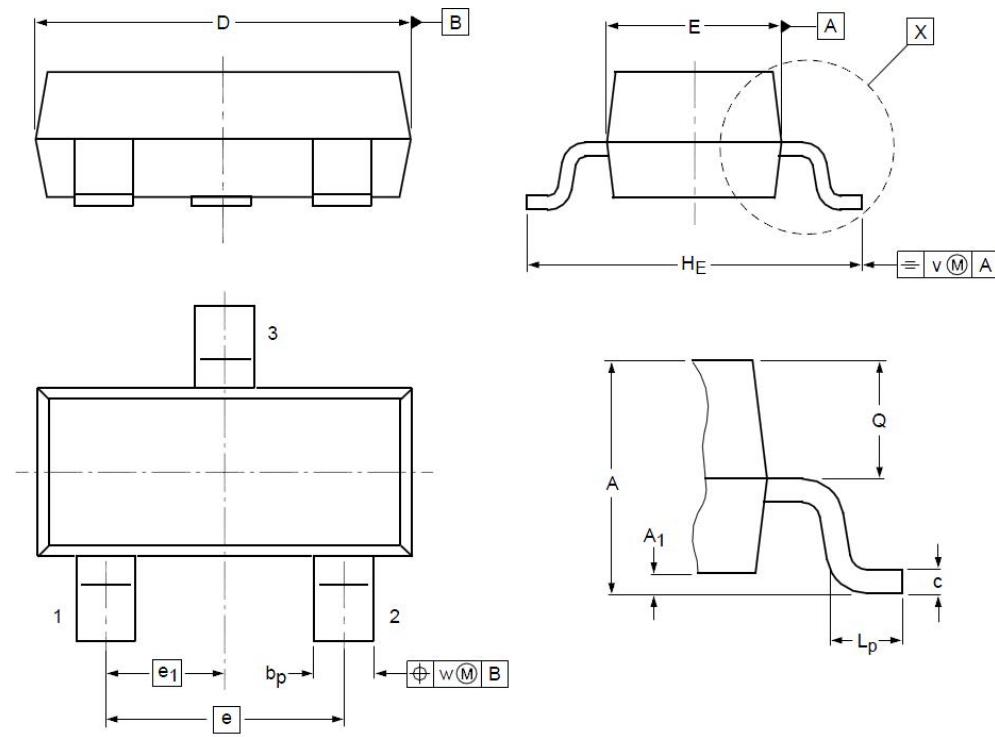


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

## N-Channel Enhancement Mode MOSFET

### SOT23 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.90	1.05	1.20	<b>e<sub>1</sub></b>	--	0.95	--
<b>A<sub>1</sub></b>	0.01	0.05	0.10	<b>H<sub>E</sub></b>	2.10	2.40	2.50
<b>b<sub>p</sub></b>	0.38	0.42	0.48	<b>L<sub>p</sub></b>	0.40	0.50	0.60
<b>c</b>	0.09	0.13	0.15	<b>Q</b>	0.45	0.49	0.55
<b>D</b>	2.80	2.92	3.00	<b>V</b>	--	0.20	--
<b>E</b>	1.20	1.33	1.40	<b>W</b>	--	0.10	--
<b>e</b>	--	1.90	--				