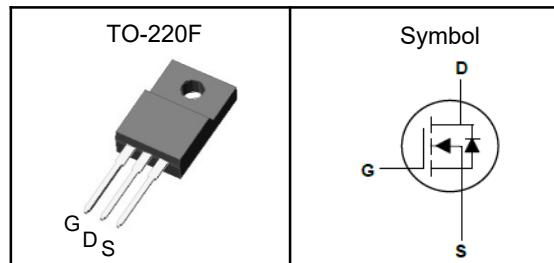


## N-Channel Enhancement Mode MOSFET

### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

$V_{DSS}$	200	V
$R_{DS(ON)-Typ}$	80	$\text{m}\Omega$
$I_D$	32	A

### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
$V_{DSS}$	Drain-Source Voltage	200	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	530	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	128	A
$I_D$	Continuous Drain Current	$T_c=25^\circ\text{C}$	A
$P_D$	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{θJA}$	Thermal Resistance Junction-Ambient <sub>1</sub>	62.5	$^\circ\text{C}/\text{W}$
$R_{θJC}$	Thermal Resistance Junction-Case <sub>1</sub>	3.0	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.



FS32N20F

## N-Channel Enhancement Mode MOSFET

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

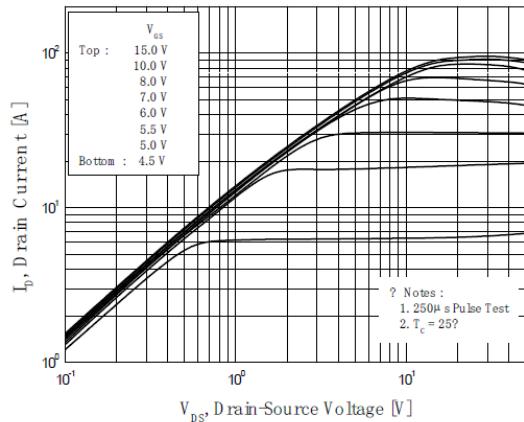
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	200	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=200\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_D=250\mu\text{A}$	2	---	4	V
$I_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	80	100	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=25\text{V}$ , Freq.=1MHz	---	1560	---	pF
$C_{\text{oss}}$	Output Capacitance		---	370	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	150	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=125\text{V}$ , $R_G=25\Omega$ , $I_D=32\text{A}$	---	26	---	nS
$T_r$	Turn-on Rise Time		---	32	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	141	---	
$T_f$	Turn-off Fall Time		---	83	---	
$Q_g$	Total Gate Charge	$V_{\text{DS}}=200\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=32\text{A}$	---	50	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	12	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	22	---	
<b>Source-Drain Characteristics</b> ( $T_J=25^\circ\text{C}$ )						
$V_{\text{SD}}$	Diode Forward Voltage <sup>②</sup>	$V_{\text{GS}}=0\text{V}$ , $I_S=16\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.4	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}}=0\text{V}$ , $I_S=16\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	220	---	nS
$Q_{\text{rr}}$	Reverse Recovery Charge		---	2.0	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu\text{s}$ , duty cycle $\leq 2\%$ ).

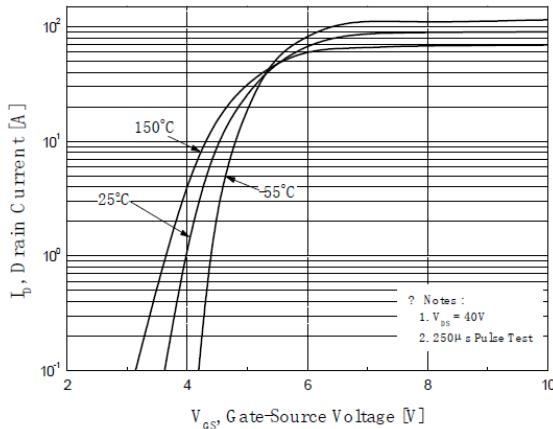
Note ⑤ : Guaranteed by design, not subject to production testing.

## N-Channel Enhancement Mode MOSFET

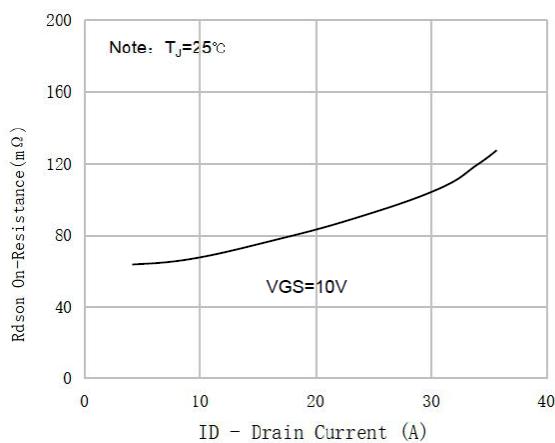
### Typical Characteristics



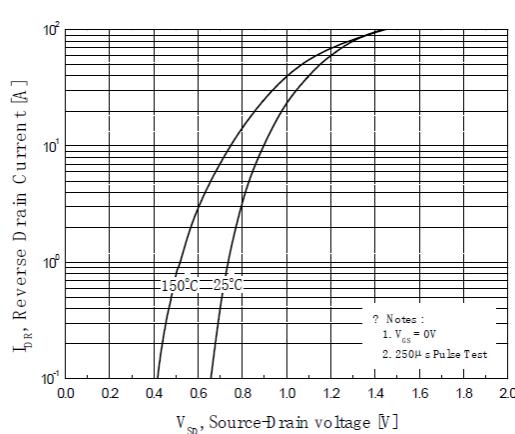
**Figure 1. On-Region Characteristics**



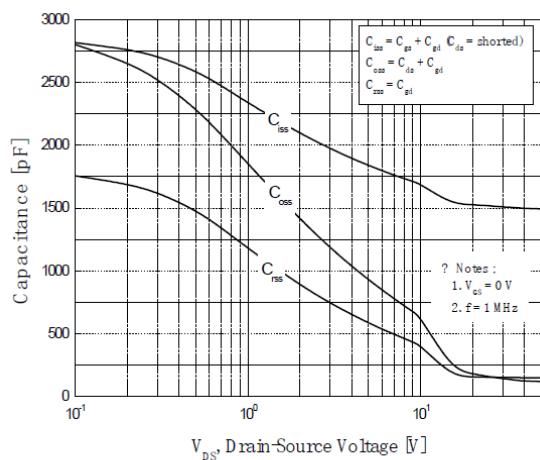
**Figure 2. Transfer Characteristics**



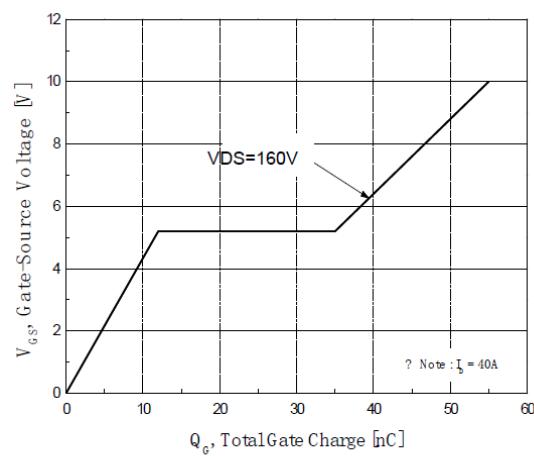
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



**Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature**

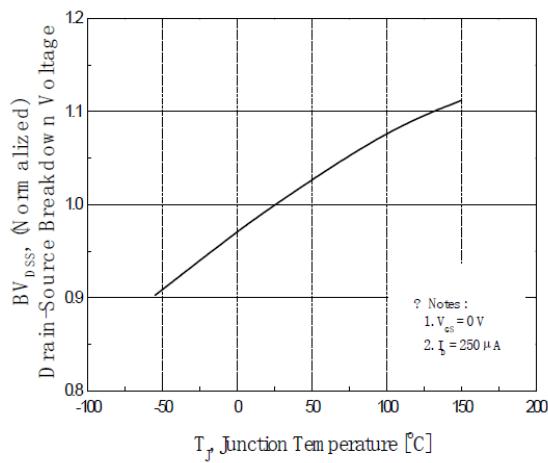


**Figure 5. Capacitance Characteristics**

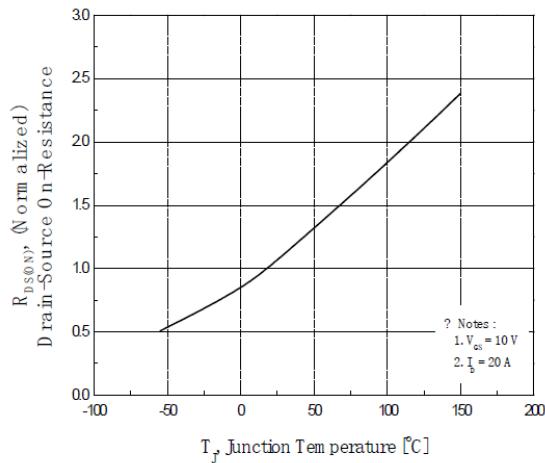


**Figure 6. Gate Charge Characteristics**

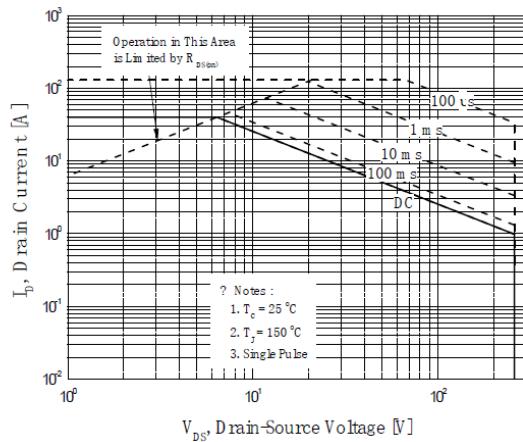
## N-Channel Enhancement Mode MOSFET



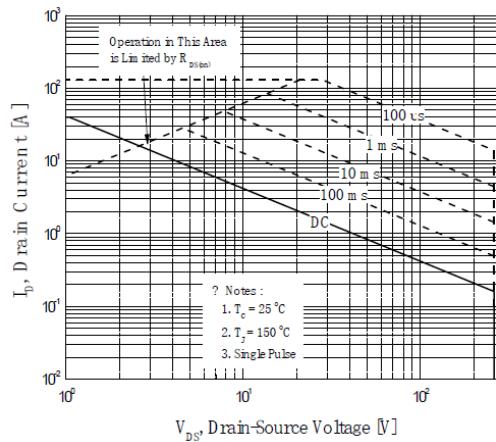
**Figure 7. Breakdown Voltage Variation vs Temperature**



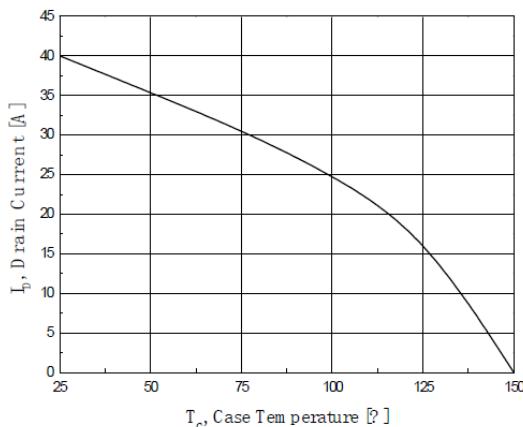
**Figure 8. On-Resistance Variation vs Temperature**



**Figure 9-1. Maximum Safe Operating Area**



**Figure 9-2. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs Case Temperature**

## N-Channel Enhancement Mode MOSFET

## TO-220F Package Outline Data

