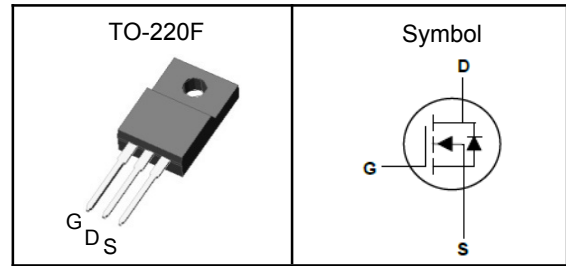


N-Channel Enhancement Mode MOSFET
Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description


V_{DSS}	500	V
$R_{DS(ON)-Typ}$	210	m Ω
I_D	18	A

Absolute Maximum Ratings($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
V_{DSS}	Drain-Source Voltage	500	V	
V_{GSS}	Gate-Source Voltage	± 30	V	
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
E_{AS}	Single Pulse Avalanche Energy ^③	689	mJ	
$I_{DM}^{①}$	Pulse Drain Current Tested	72	A	
I_D	Continuous Drain Current	$T_c=25^\circ\text{C}$	18	A
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	41	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ^①	63	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ^①	3.04	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$.

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.



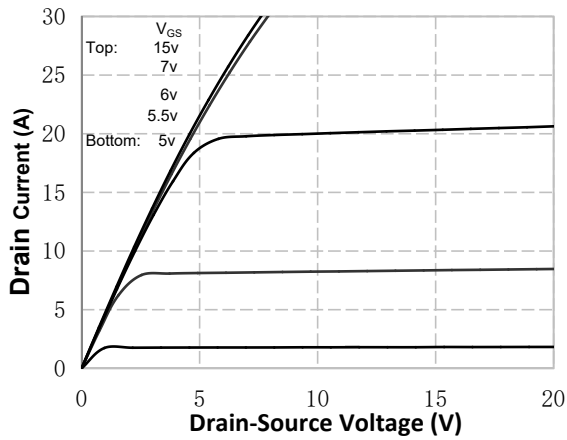
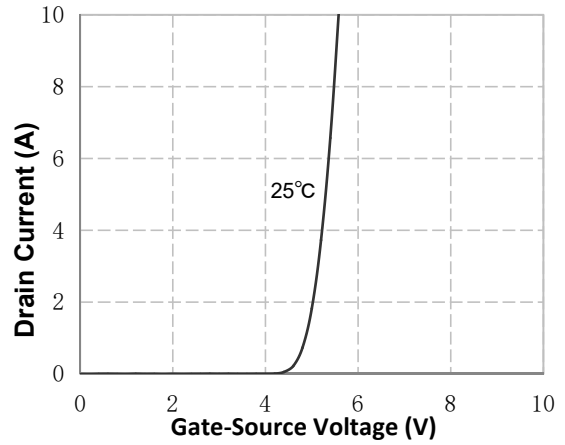
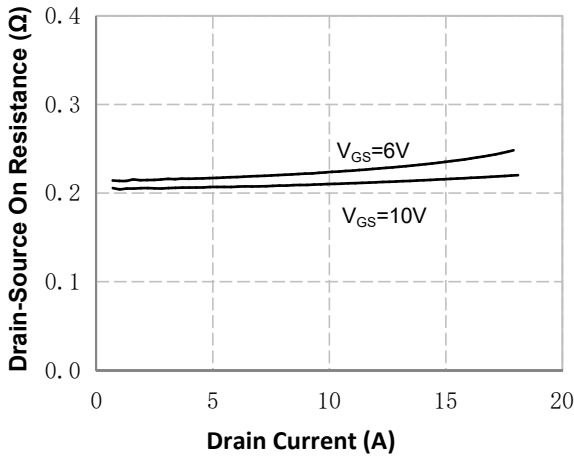
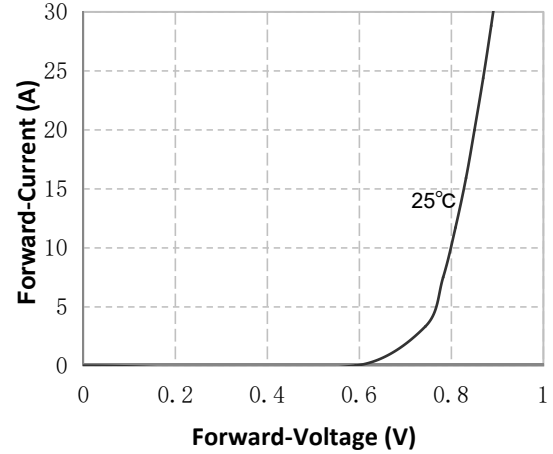
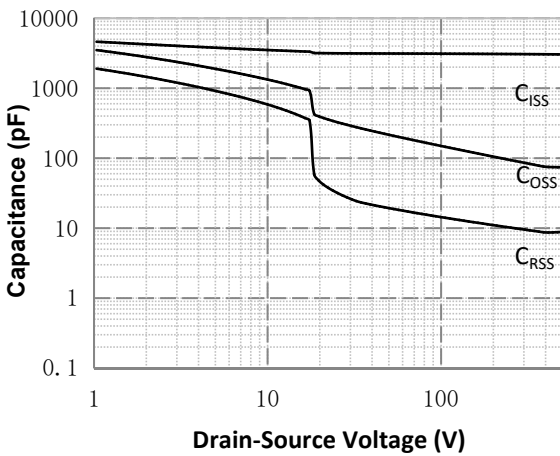
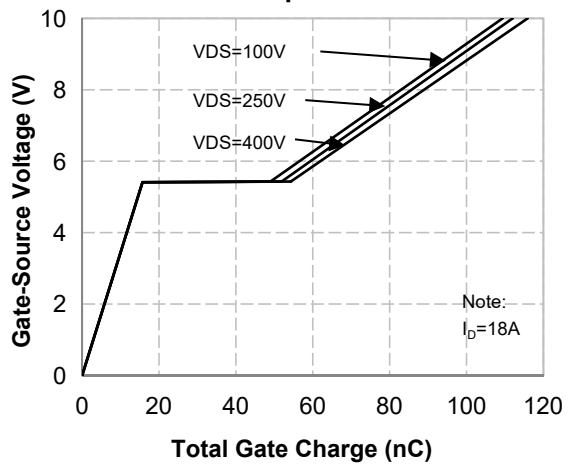
N-Channel Enhancement Mode MOSFET

Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	500	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =500V, V _{GS} =0V	---	---	1	uA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	3.0	---	5.0	V
I _{GSS}	Gate Leakage Current	V _{GS} =±30V, V _{DS} =0V	---	---	±100	nA
R _{DS(ON)}	Drain-Source On-state Resistance	V _{GS} =10V, I _D =9A	---	210	265	mΩ
Dynamic Characteristics ^⑤						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, Freq.=1MHz	---	3110	---	pF
C _{oss}	Output Capacitance		---	328	---	
C _{rss}	Reverse Transfer Capacitance		---	32	---	
T _{d(on)}	Turn-on Delay Time	V _{DD} =250V, V _{GS} =10V, R _G =25Ω, I _D =18A	---	65	---	nS
T _r	Turn-on Rise Time		---	40	---	
T _{d(off)}	Turn-off Delay Time		---	245	---	
T _f	Turn-off Fall Time		---	68	---	
Q _g	Total Gate Charge	V _{DD} =250V, V _{GS} =10V, I _D =18A	---	116	---	nC
Q _{gs}	Gate-Source Charge		---	16	---	
Q _{gd}	Gate-Drain Charge		---	38	---	
Source-Drain Characteristics (T _J =25°C)						
V _{SD}	Diode Forward Voltage ₂	V _{GS} =0V, I _S =9A, T _J =25°C	---	---	1.4	V
t _{rr}	Reverse Recovery Time	I _S =18A, di/dt=100A/μs, T _J =25°C	---	525	---	nS
Q _{rr}	Reverse Recovery Charge		---	6.2	---	nC

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET
Typical Characteristics

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics

Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

Figure 5. Capacitance Characteristics

Figure 6. Gate Charge Characteristics

N-Channel Enhancement Mode MOSFET

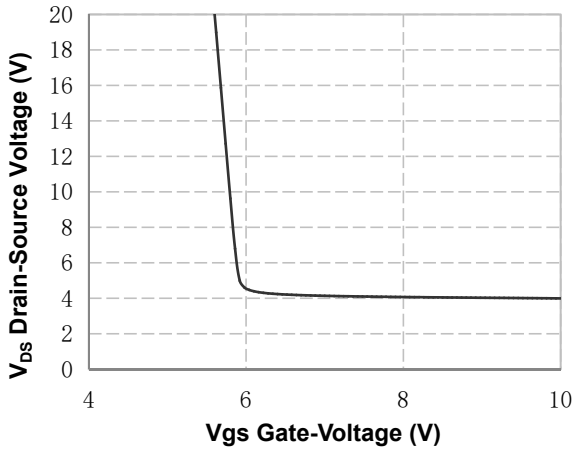


Figure 7. Vds Drain-Source Voltage vs Gate Voltage

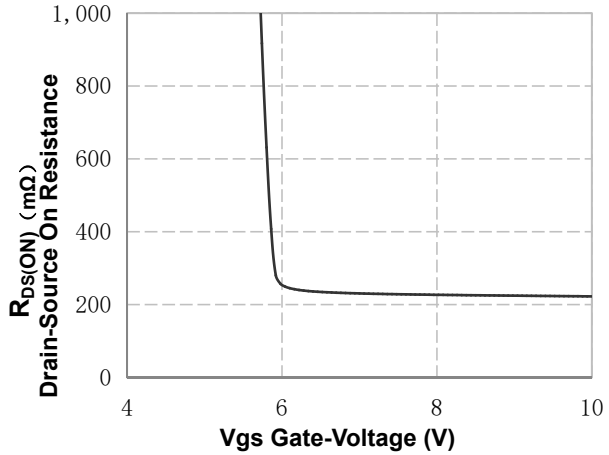


Figure 8. On-Resistance vs Gate Voltage

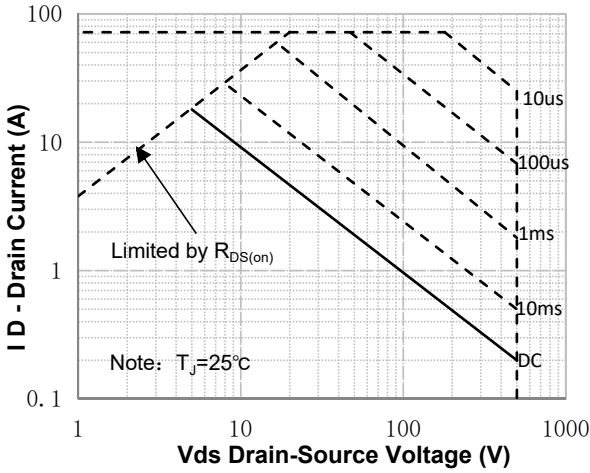


Figure 9. Maximum Safe Operating Area

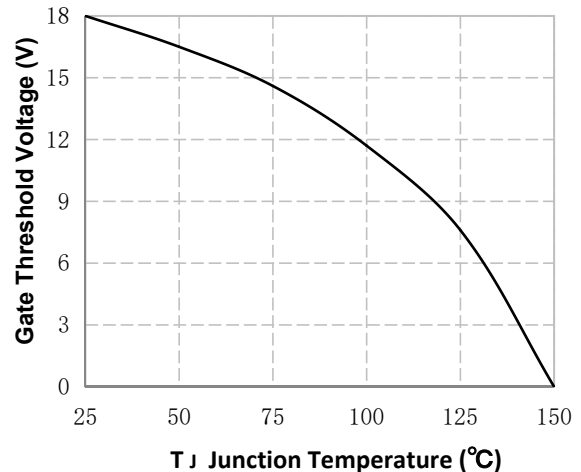


Figure 10. Maximum Drain Current vs Temperature

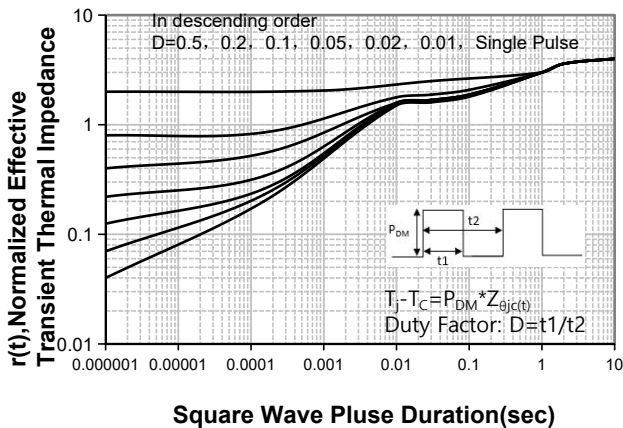


Figure 11. Transient Thermal Response Curve

N-Channel Enhancement Mode MOSFET
TO-220F Package Outline Data
